

# ReZone: Disarming TrustZone with TEE Privilege Reduction

#### **David Cerdeira**, José Martins, Nuno Santos, Sandro Pinto

















**Biometric Authentication** 







**Biometric Authentication** 

Digital Rights Management







**Biometric Authentication** 

ESRGv3

Digital Rights Management

**Electronic Payments** 







• REE OS (EL1) and Apps (EL0) in the normal word

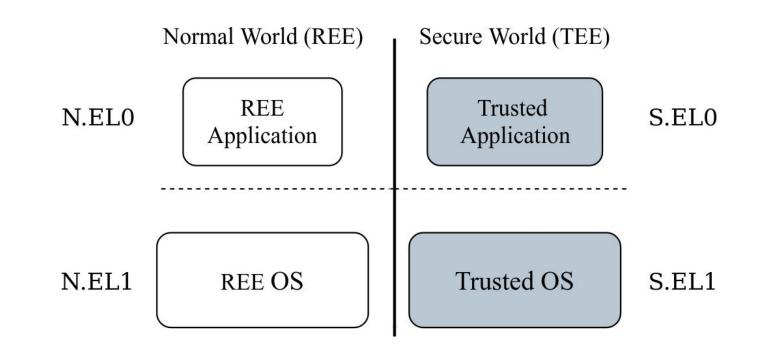
N.EL0	REE Application
N.EL1	REE OS

Normal World (REE)





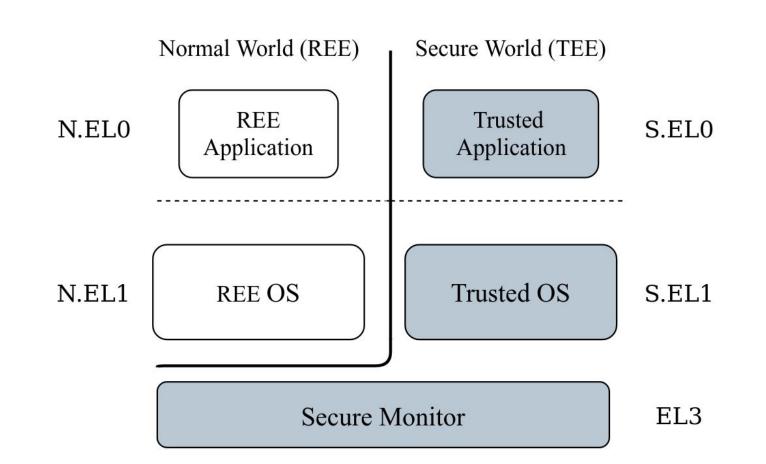
- REE OS (EL1) and Apps (EL0) in the normal word
- Trusted OS (S.EL1) and TAs (S.EL0) in the secure world







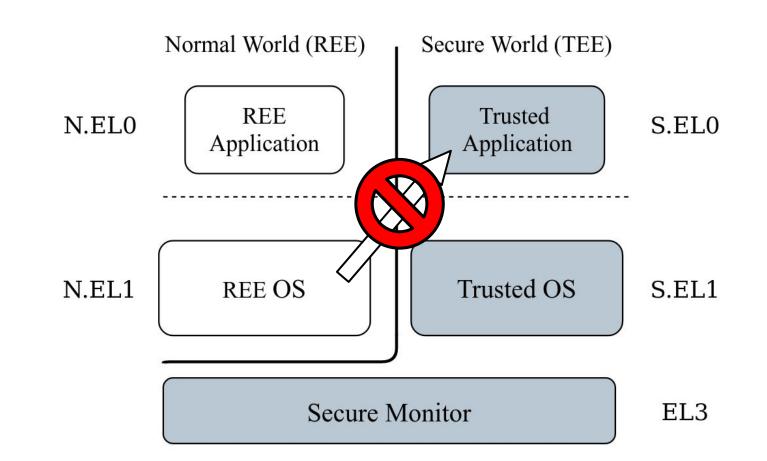
- REE OS (EL1) and Apps (EL0) in the normal word
- Trusted OS (S.EL1) and TAs (S.EL0) in the secure world
- Secure Monitor (EL3) manages world switch







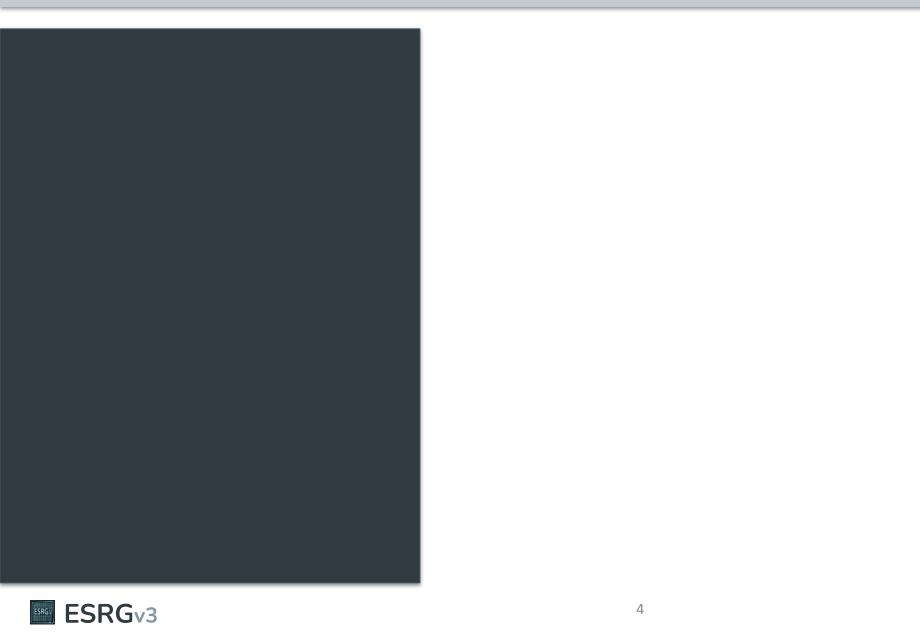
- REE OS (EL1) and Apps (EL0) in the normal word
- Trusted OS (S.EL1) and TAs (S.EL0) in the secure world
- Secure Monitor (EL3) manages world switch



TrustZone prevents the REE from directly compromising the TEE.









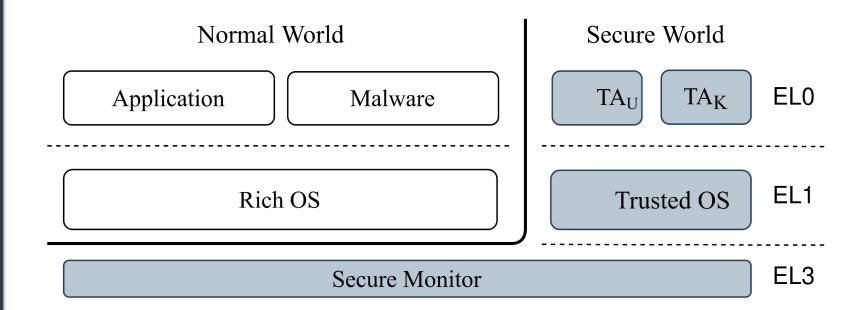
Trusted OS has unrestricted access to the full system







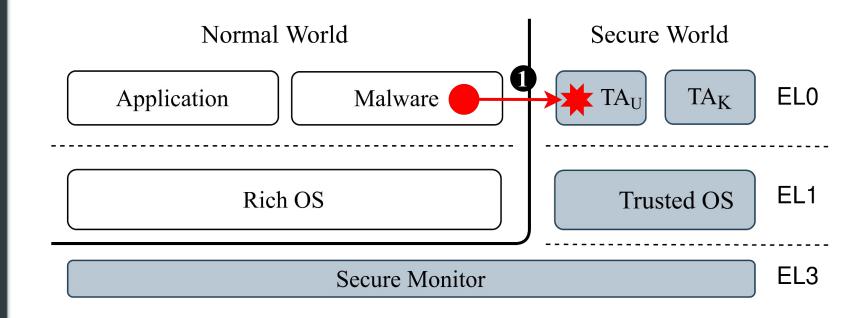
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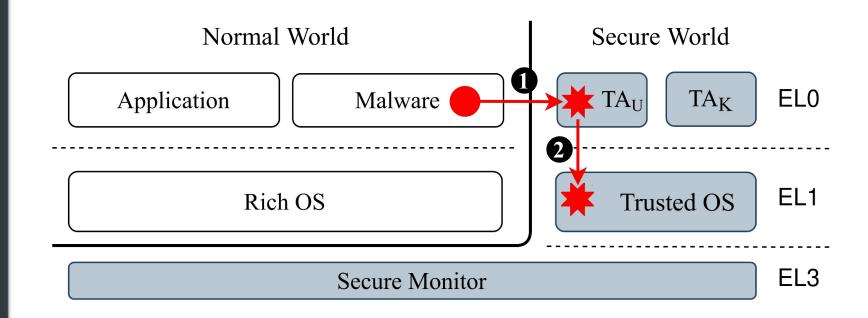


1. Hijack a user-level TA





## Trusted OS has unrestricted access to the full system

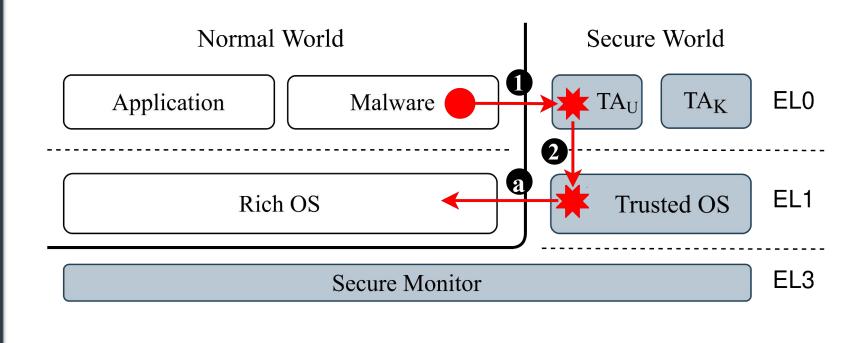


- 1. Hijack a user-level TA
- 2. Hijack the Trusted OS





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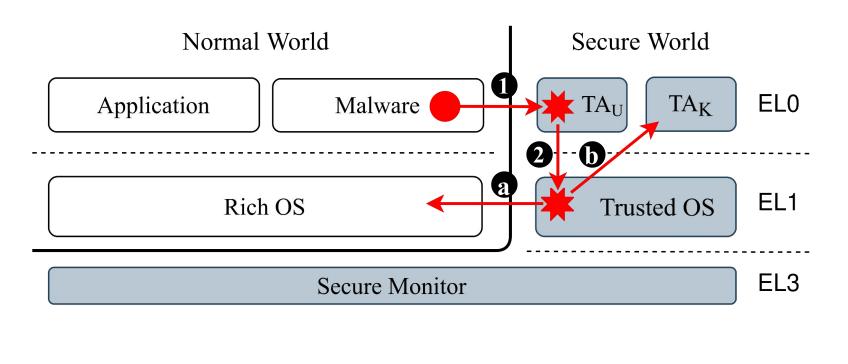
A. Control the rich OS

2. Hijack the Trusted OS





## Trusted OS has unrestricted access to the full system



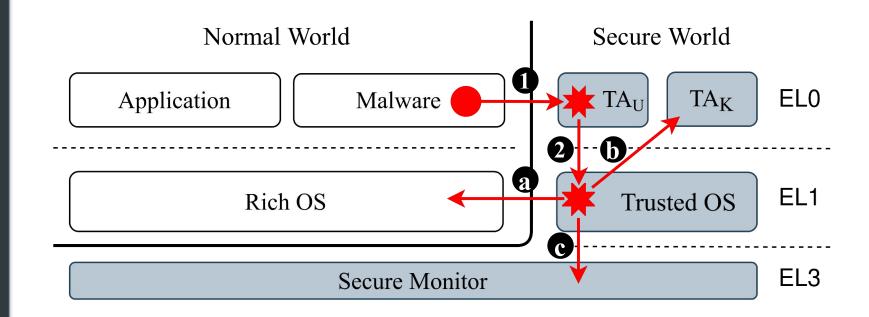
- 1. Hijack a user-level TA
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- A. Control the rich OS
- B. Control a kernel-level TA





## Trusted OS has unrestricted access to the full system

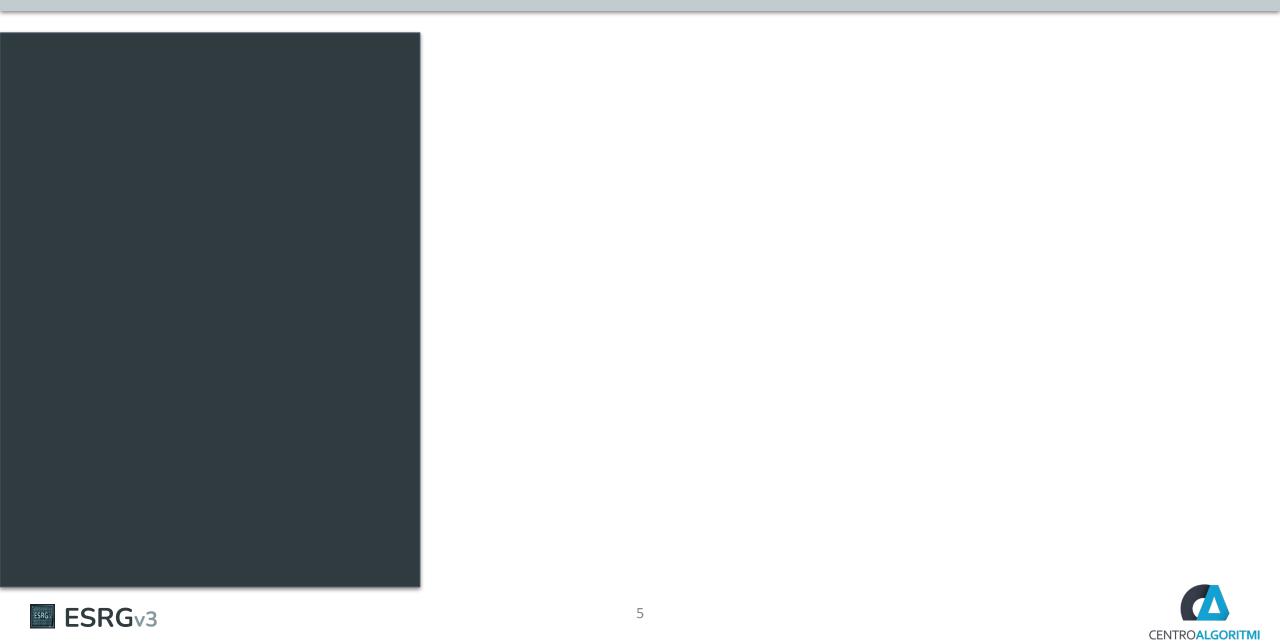


- 1. Hijack a user-level TA
- 2. Hijack the Trusted OS

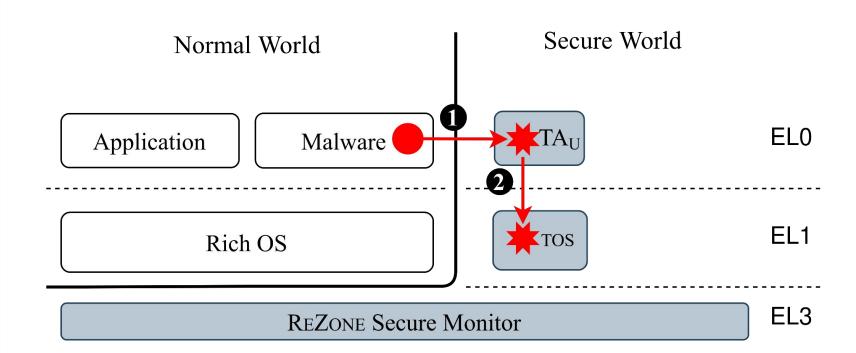
- A. Control the rich OS
- B. Control a kernel-level TA
- C. Control the secure Monitor







Deprivileging the TEE increases protection for:





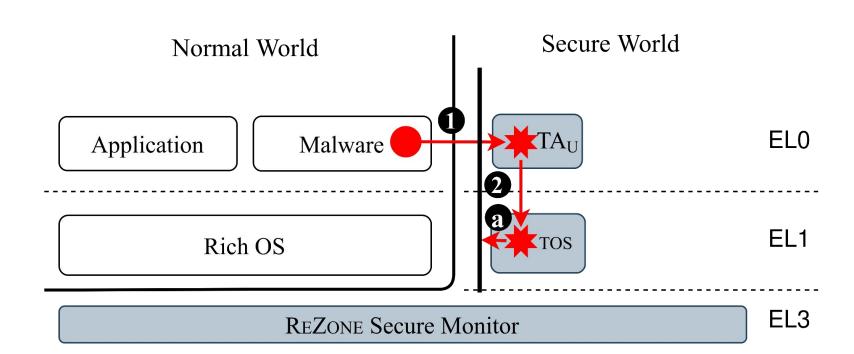


Deprivileging the TEE increases protection for:

a) Normal World REE

ESRGv3

ESRG.

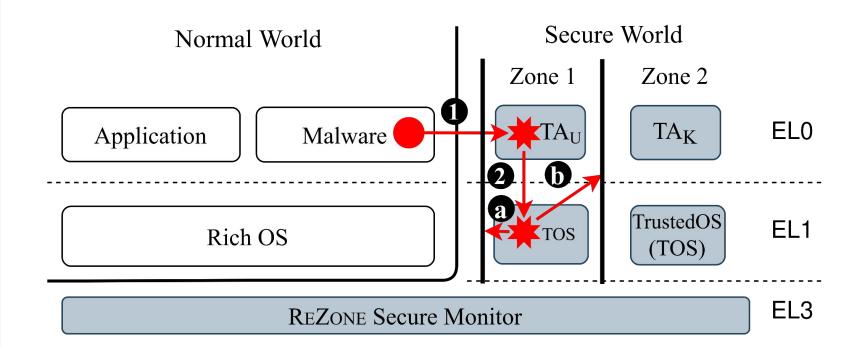




Deprivileging the TEE increases protection for:

a) Normal World REE

b) Other TEEs / Zones





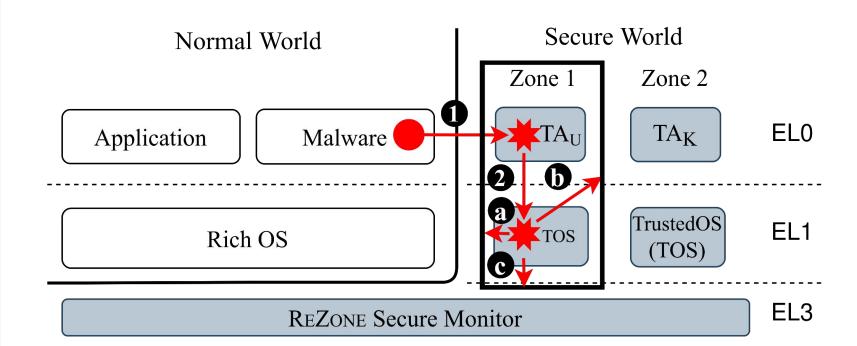


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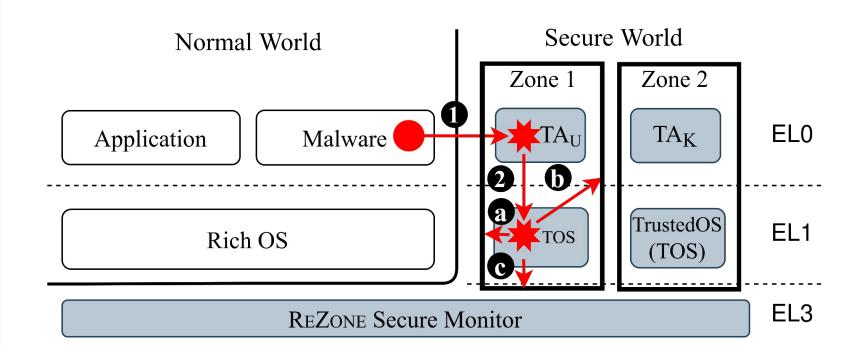


Deprivileging the TEE increases protection for:

a) Normal World REE

b) Other TEEs / Zones

c) Secure Monitor







#### We Propose ReZone







- An approach to **deprivilege** the Trusted OS
  - Use TrustZone orthogonal hardware features present in COTS platforms





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- Implementation in a real-world platform and software
  - Evaluate ReZone in Embedded Linux and Android software stacks



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- Performance penalty does **not degrade UX** 
  - Evaluate two use cases: Bitcoin Wallet and DRM



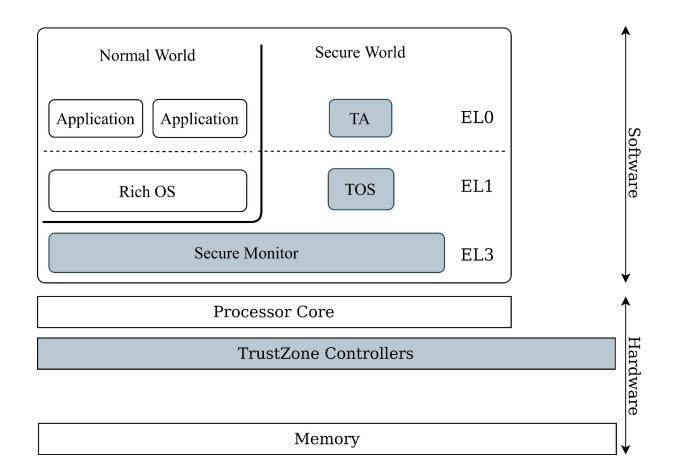
- An approach to **deprivilege** the Trusted OS
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- Implementation in a real-world platform and software
  - Evaluate ReZone in Embedded Linux and Android software stacks
- Performance penalty does not degrade UX
  - Evaluate two use cases: Bitcoin Wallet and DRM
- Mitigate ~87% of critical surveyed CVEs (80)
  - Mitigation of most Trusted OS and Trusted Applications vulnerabilities





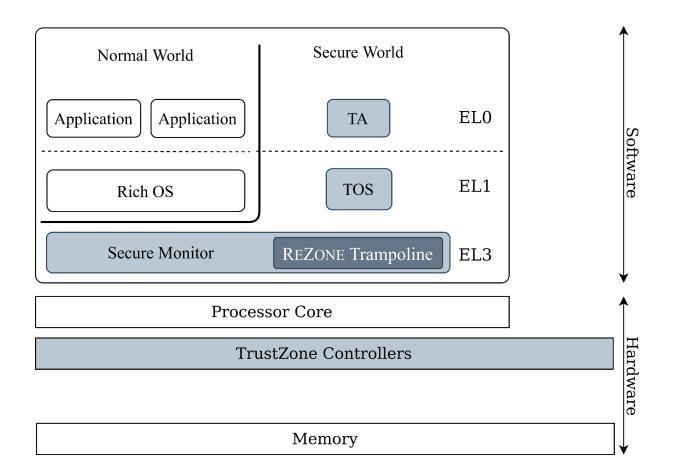






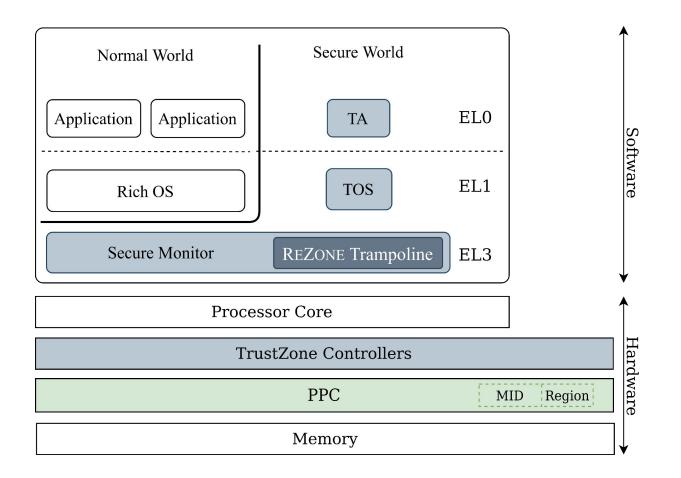






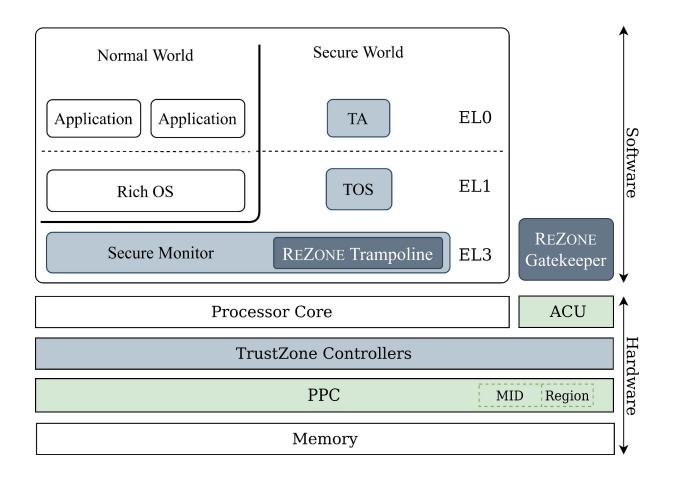






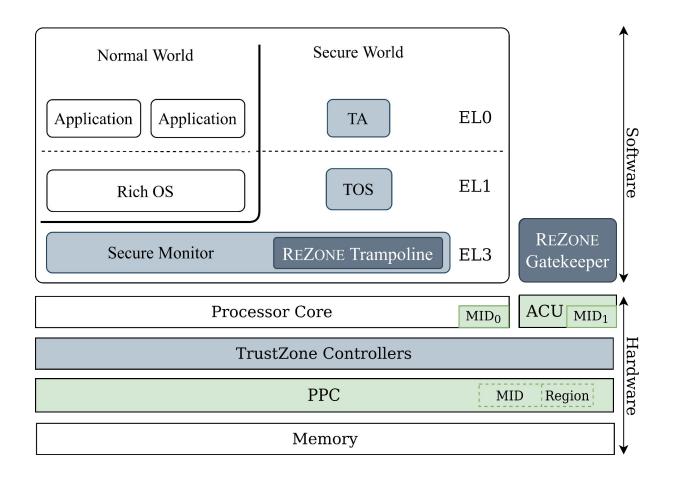






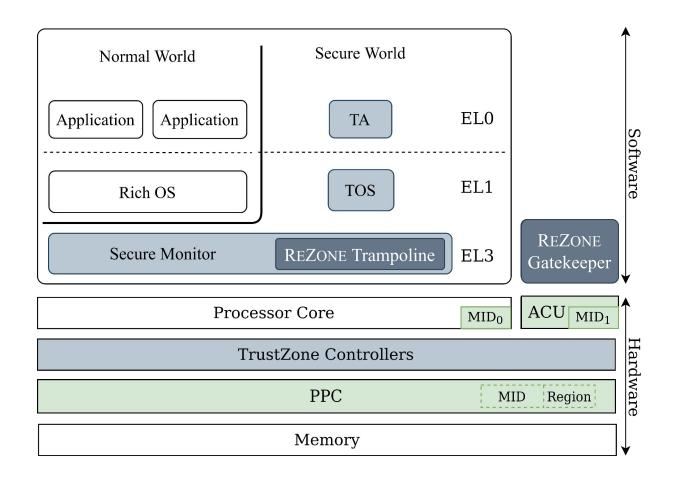










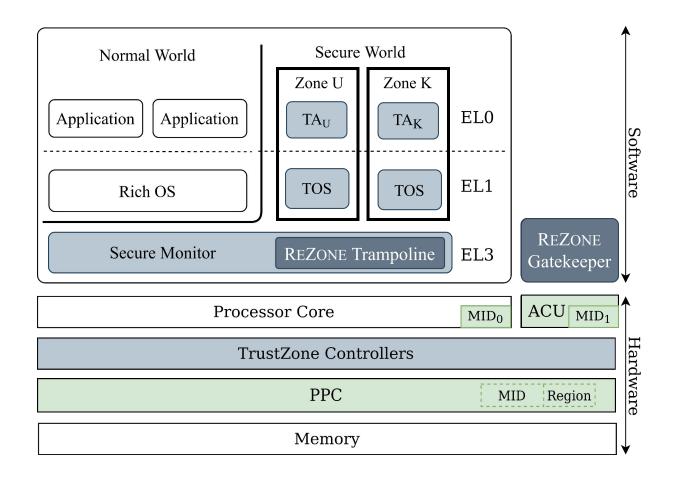


By leveraging the **PPC and ACU** we can deprivilege the Trusted OS.





### ReZone Design



By leveraging the **PPC and ACU** we can deprivilege the Trusted OS.



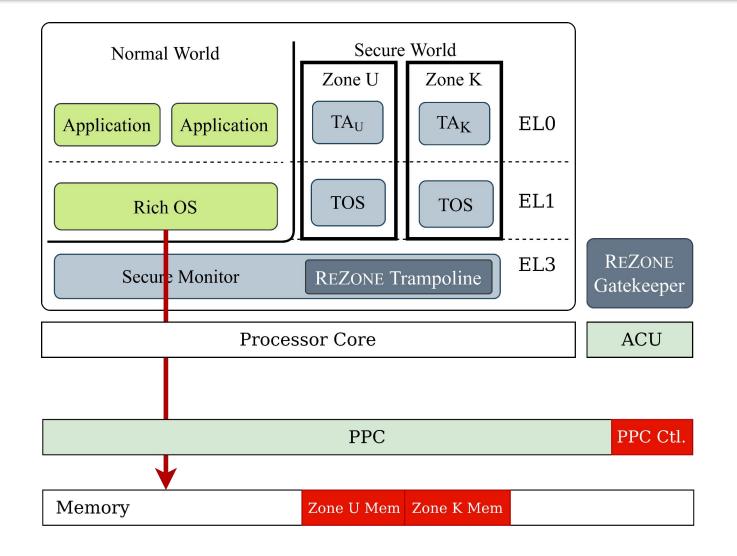








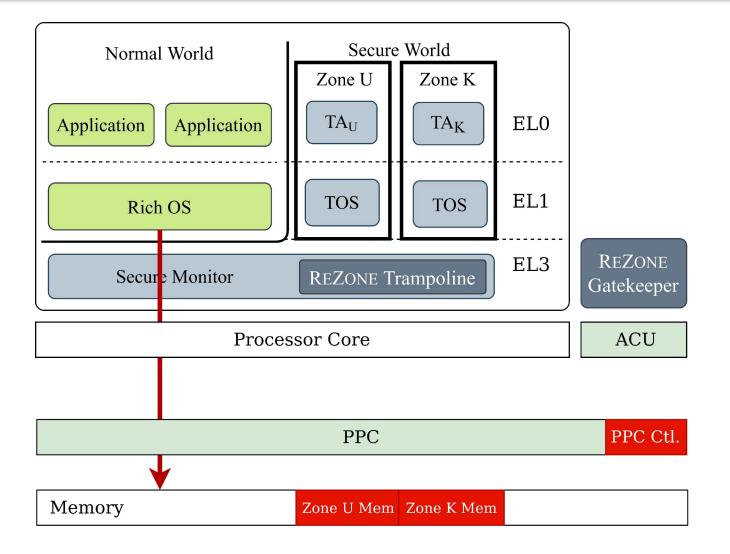
• Starting from normal world execution





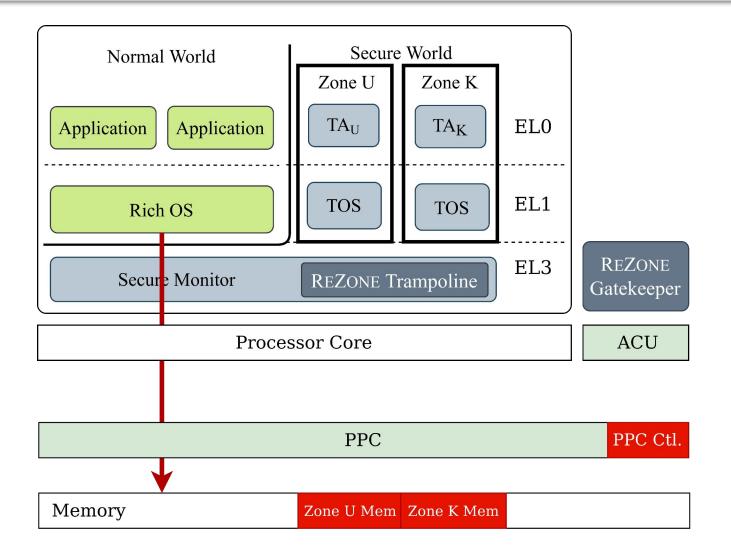


- Starting from normal world execution
- Normal world can access the normal memory



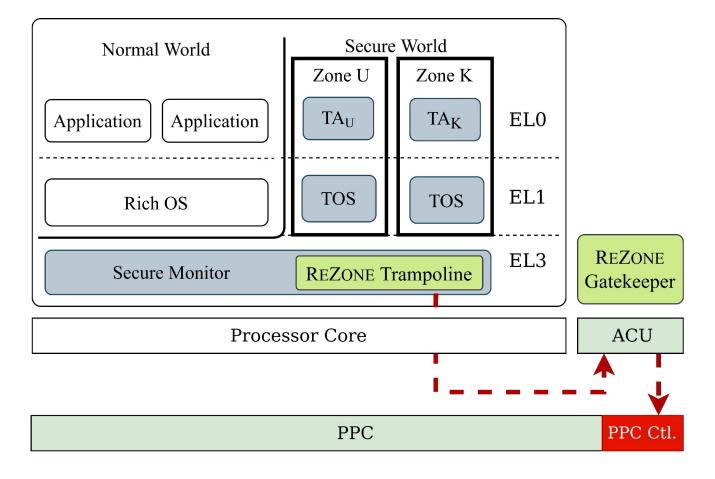


- Starting from normal world execution
- Normal world can access the normal memory
- TrustZone prevents accesses to secure world





• Trampoline interacts with the Gatekeeper to reconfigure the PPC

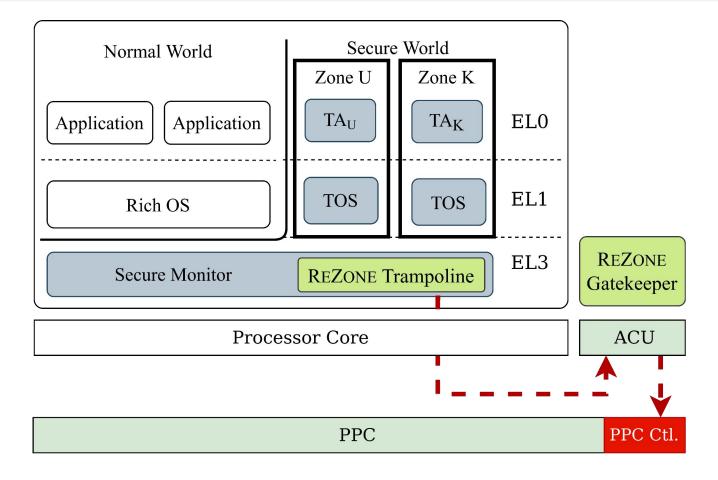


Memory	Zone U Mem	Zone K Mem	
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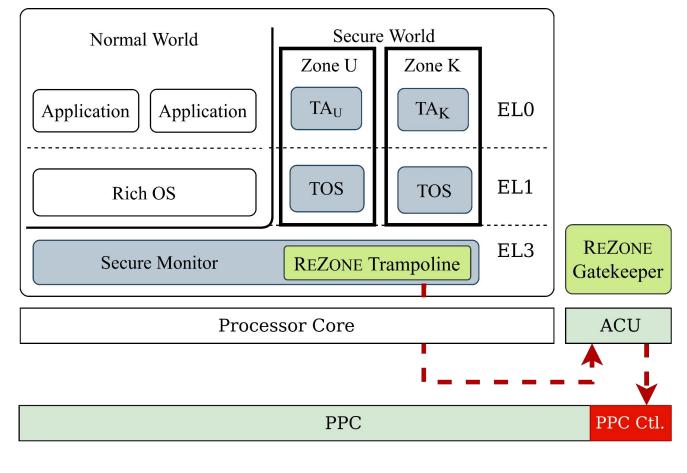
- Trampoline interacts with the Gatekeeper to reconfigure the PPC
- To prevent a Zone from undoing access control policy the processor cannot access the PPC Ctl. directly



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- Trampoline interacts with the Gatekeeper to reconfigure the PPC
- To prevent a Zone from undoing access control policy the processor cannot access the PPC Ctl. directly
- The ACU validates the interaction and performs reconfiguration

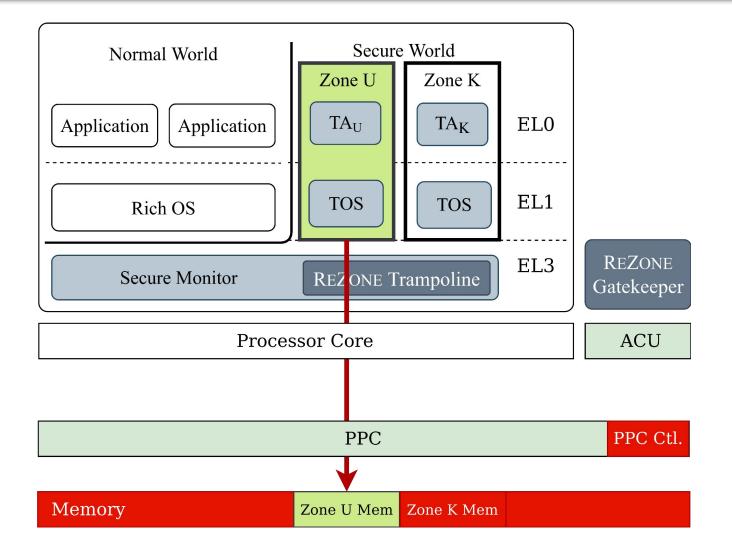


Memory	Zone U Mem	Zone K Mem	
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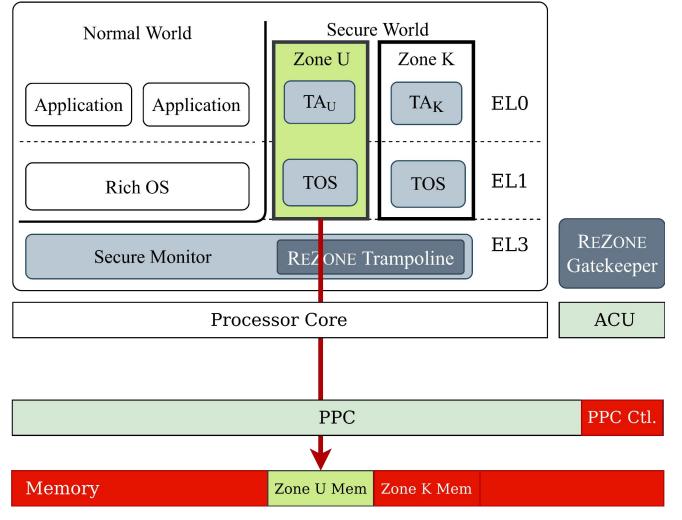
• The Zone will execute







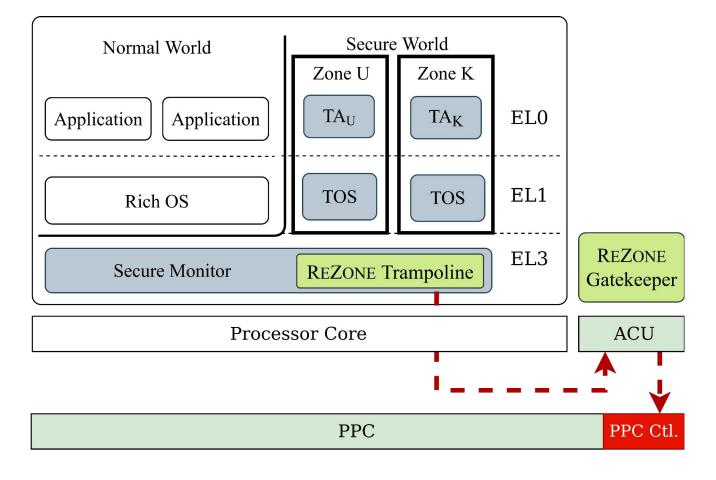
- The Zone will execute
- PPC will only allow access to the zone memory







• On a Zone exit the PPC is reconfigured again to undo the policy

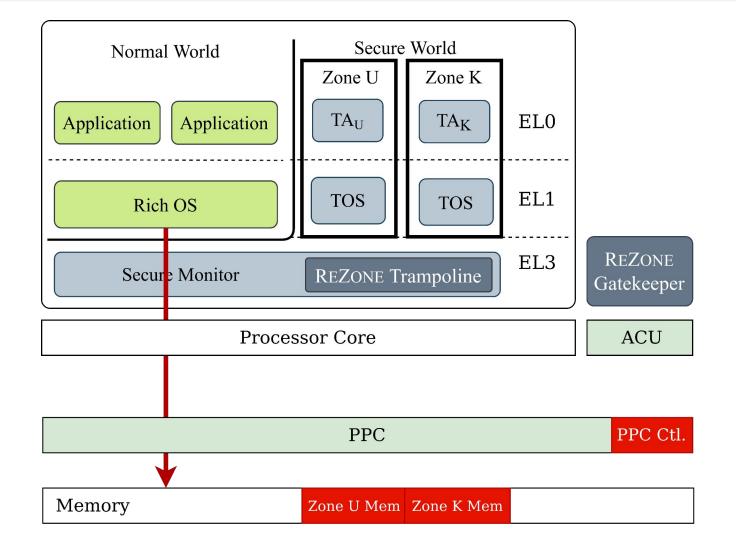


Memory
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• The normal world can resume execution













- Cross-core synchronization
  - Use of synchronization primitives, and core suspension



- Cross-core synchronization
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- Microarchitectural maintenance
  - Cache and TLB Maintenance



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- Dynamic PPC reconfiguration
  - PPC reconfiguration optimization





- Cross-core synchronization
  - Use of synchronization primitives, and core suspension
- Microarchitectural maintenance
  - Cache and TLB Maintenance
- Dynamic PPC reconfiguration
  - PPC reconfiguration optimization
- Handling Zone Exits
  - Preventing crashes and cache code injection









We evaluate ReZone across three vectors





- Micro-Benchmarks
  - Evaluate overheads of **REE-TA interaction**

We evaluate ReZone across three vectors

ESRGv3

ESRG.





# We evaluate ReZone across three vectors

• Micro-Benchmarks

**ReZone Performance Evaluation** 

- Evaluate overheads of **REE-TA interaction**
- Performance of Real-World Use Cases
  - Two TAs that implement real workloads





Micro-Benchmarks

- Evaluate overheads of **REE-TA interaction**
- Performance of Real-World Use Cases
  - Two TAs that implement real workloads
- Impact on REE performance
  - Impact of calls to zones during the execution of an Android benchmark



# We evaluate ReZone across three vectors





- Micro benchmarks
  - OPTEE's xtest
  - GP Client API



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- Real world use cases
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  - DRM

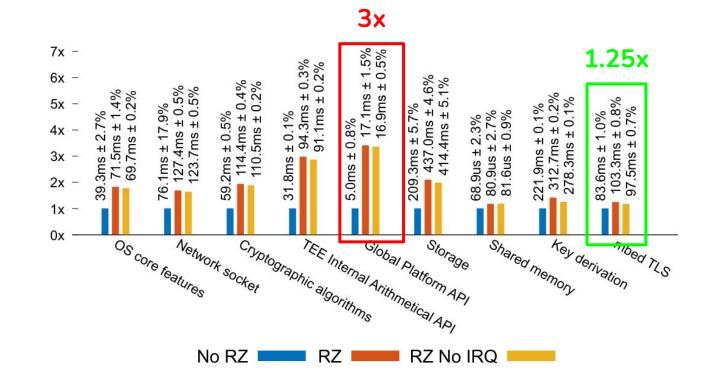




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• Simple workloads with many world switches  $\rightarrow$  higher overheads

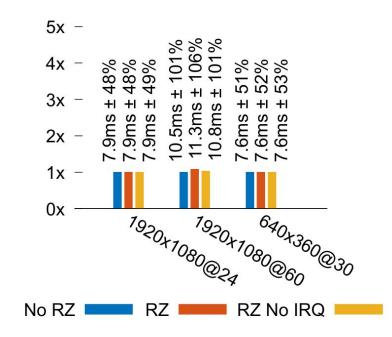






- Micro benchmarks
  - OPTEE's xtest
  - GP Client API
- Real world use cases
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  - DRM

- Simple workloads with many world switches  $\rightarrow$  higher overheads
- $UX \rightarrow not$  significantly affected









**CENTROALGORITMI** 

- Normal World Impact
  - PCMark 3.0 for Android







• Single-Core interrupt

Interval (ms)	Score	Penalty
10	4369	12.97%
100	4776	4.86%
1000	4983	0.74%

- Normal World Impact
  - PCMark 3.0 for Android

ESRGv3

ESRG.



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Interval (ms)	Score	Penalty
10	4369	12.97%
100	4776	4.86%
1000	4983	0.74%

- Normal World Impact
  - PCMark 3.0 for Android

• Multi-Core interrupt (1s interval)

# of Cores	Score	Penalty
1	4983	0.74%
2	4938	1.63%
4	4833	3.73%











CVE	С	CVE	С	CVE	С	CVE	С
2014-9979	TO	2017-11011	TO	2015-9000	TA	2015-8997	TO/TA
2015-8999	TO	2017-14912	TO	2015-9002	TA	2015-8998	TO/TA
2015-9070	TO	2017-14916	TO	2015-9162	TA	2015-9005	TO/TA
2015-9071	TO	2017-14917	TO	2015-9174	TA	2015-9007	TO/TA
2015-9072	TO	2017-17176	TO	2015-9183	TA	2016-2432	TO/TA
2015-9073	TO	2017-18071	TO	2017-6293	TA	2016-10297	TO/TA
2015-9108	TO	2017-18128	TO	2017-18310	TA	2017-6289	TO/TA
2015-9112	TO	2017-18129	TO	2017-18312	TA	2017-14913	TO/TA
2015-9113	TO	2017-18132	TO	2017-18317	TA	2017-18293	TO/TA
2015-9198	TO	2017-18133	TO	2018-5210	TA	2017-18296	TO/TA
2015-9199	TO	2017-18311	TO	2018-5885	TA	2017-18297	TO/TA
2015-9200	TO	2017-18314	TO	2014-9932	TO/TA	2017-18298	TO/TA
2016-2431	TO	2017-18315	TO	2014-9935	TO/TA	2018-5866	TO/TA
2016-10238	TO	2018-3588	TO	2014-9936	TO/TA	2017-18282	HW
2016-10432	TO	2018-5870	TO	2014-9937	TO/TA	2015-9003	CI
2017-6290	TO	2016-10239	TO	2014-9945	TO/TA	2016-10398	CI
2017-6292	TO	2018-11950	TO	2014-9948	TO/TA	2017-14907	CI
2017-6294	TO	2015-4422	TA	2014-9949	TO/TA	2017-18146	CI
2017-8274	TO	2015-6639	TA	2015-8995	TO/TA	2016-10458	BL
2017-11010	TO	2015-6647	TA	2015-8996	TO/TA	2017-14911	BL





CVE	С	CVE	С	CVE	С		CVE	С	
2014-9979	TO	2017-11011	TO	2015-9000	TA	ſ	2015-8997	TO/TA	
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2015-9073	TO	2017-18071	TO	2017-6293	TA		2016-10297	TO/TA	
2015-9108	TO	2017-18128	TO	2017-18310	TA		2017-6289	TO/TA	
2015-9112	TO	2017-18129	TO	2017-18312	TA	?	2017-14913	TO/TA	
2015-9113	TO	2017-18132	TO	2017-18317	TA	?	2017-18293	TO/TA	
2015-9198	TO	2017-18133	TO	2018-5210	TA		2017-18296	TO/TA	?
2015-9199	TO	2017-18311	TO	2018-5885	TA		2017-18297	TO/TA	
2015-9200	TO	2017-18314	<b>TO</b> ?	2014-9932	TO/TA		2017-18298	TO/TA	
2016-2431	TO	2017-18315	TO	2014-9935	TO/TA		2018-5866	TO/TA	
2016-10238	TO	2018-3588	TO	2014-9936	TO/TA		2017-18282	HW	
2016-10432	TO	2018-5870	TO	2014-9937	TO/TA		2015-9003	CI	
2017-6290	TO	2016-10239	TO	2014-9945	TO/TA		2016-10398	CI	
2017-6292	TO	2018-11950	TO	2014-9948	TO/TA		2017-14907	CI	
2017-6294	TO	2015-4422	TA	2014-9949	TO/TA		2017-18146	CI	
2017-8274	TO	2015-6639	TA	2015-8995	TO/TA		2016-10458	BL	
2017-11010	ТО	2015-6647	TA	2015-8996	TO/TA	1	2017-14911	BL	





• Assuming multiple Zones with one TA per zone

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2014-9979	TO	2017-11011	TO		2015-9000	TA	£	2015-8997	TO/TA	
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2015-9071	TO	2017-14917	TO		2015-9174	TA		2015-9007	TO/TA	
2015-9072	TO	2017-17176	TO		2015-9183	TA		2016-2432	TO/TA	
2015-9073	TO	2017-18071	TO		2017-6293	TA		2016-10297	TO/TA	
2015-9108	TO	2017-18128	TO		2017-18310	TA		2017-6289	TO/TA	
2015-9112	TO	2017-18129	TO		2017-18312	TA	?	2017-14913	TO/TA	
2015-9113	TO	2017-18132	TO		2017-18317	TA	?	2017-18293	TO/TA	
2015-9198	TO	2017-18133	TO		2018-5210	TA		2017-18296	TO/TA	?
2015-9199	TO	2017-18311	TO		2018-5885	TA		2017-18297	TO/TA	
2015-9200	TO	2017-18314	TO ?	)	2014-9932	TO/TA		2017-18298	TO/TA	
2016-2431	TO	2017-18315	TO		2014-9935	TO/TA		2018-5866	TO/TA	
2016-10238	TO	2018-3588	TO		2014-9936	TO/TA		2017-18282	HW	
2016-10432	TO	2018-5870	TO		2014-9937	TO/TA		2015-9003	CI	
2017-6290	TO	2016-10239	TO		2014-9945	TO/TA		2016-10398	CI	
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2017-6294	TO	2015-4422	TA		2014-9949	TO/TA		2017-18146	CI	
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2017-11010	ТО	2015-6647	TA		2015-8996	TO/TA	1	2017-14911	BL	





- Assuming multiple Zones with one TA per zone
- Mitigates ~87% CVEs:
  - Most TOS and TA vulnerabilities

CVE	С		C	VE	С		CVE	E	С		CVE	С	
2014-9979	TO		2017-	11011	TO	1	2015-9	000	TA	1	2015-8997	TO/TA	
2015-8999	TO		2017-	14912	TO	$\checkmark$	2015-9	002	TA	$\checkmark$	2015-8998	TO/TA	
2015-9070	TO		2017-	14916	TO	$\checkmark$	2015-9	162	TA	$\checkmark$	2015-9005	TO/TA	
2015-9071	TO		2017-	14917	TO	$\checkmark$	2015-9	174	TA	$\checkmark$	2015-9007	TO/TA	
2015-9072	TO		2017-	17176	TO	$\checkmark$	2015-9	183	TA	$\checkmark$	2016-2432	TO/TA	
2015-9073	TO		2017-	18071	TO	$\checkmark$	2017-6	293	TA	$\checkmark$	2016-10297	TO/TA	
2015-9108	TO		2017-	18128	TO		2017-18	310	TA		2017-6289	TO/TA	
2015-9112	TO		2017-	18129	TO	$\checkmark$	2017-18	312	TA	?	2017-14913	TO/TA	
2015-9113	TO		2017-	18132	TO	$\checkmark$	2017-18	317	TA	?	2017-18293	TO/TA	
2015-9198	TO		2017-	18133	TO	$\checkmark$	2018-5	210	TA	$\checkmark$	2017-18296	TO/TA	?
2015-9199	TO		2017-	18311	TO	$\checkmark$	2018-5	885	TA	$\checkmark$	2017-18297	TO/TA	
2015-9200	TO		2017-	18314	TO	?	2014-9	932	TO/TA	$\checkmark$	2017-18298	TO/TA	
2016-2431	TO		2017-	18315	TO	$\checkmark$	2014-9	935	TO/TA	$\checkmark$	2018-5866	TO/TA	
2016-10238	TO		2018	-3588	TO	$\checkmark$	2014-9	936	TO/TA	$\checkmark$	2017-18282	HW	
2016-10432	TO		2018	-5870	TO	$\checkmark$	2014-9	937	TO/TA	$\checkmark$	2015-9003	CI	
2017-6290	TO		2016-	10239	TO	$\checkmark$	2014-9	945	TO/TA	$\checkmark$	2016-10398	CI	
2017-6292	TO		2018-	11950	TO	$\checkmark$	2014-9	948	TO/TA	$\checkmark$	2017-14907	CI	
2017-6294	TO		2015	-4422	TA	$\checkmark$	2014-9	949	TO/TA	$\checkmark$	2017-18146	CI	
2017-8274	TO		2015	-6639	TA	$\checkmark$	2015-8	995	TO/TA	$\checkmark$	2016-10458	BL	
2017-11010	TO		2015	-6647	TA	1	2015-8	996	TO/TA	$\checkmark$	2017-14911	BL	
In Scope	? 7	TO/	/TA	TO	Т	A	HW	CI	BL		Total Pe	rcentag	ge
Y		2	1	34	1	1					66 8	6.84%	



- Assuming multiple Zones with one TA per zone
- Mitigates ~87% CVEs:
  - Most TOS and TA vulnerabilities
- Doesn't Mitigate against:
  - Secret disclosures, Hardware attacks, Cryptographic flaws, Bootloader flaws

OVE	C		OVE		C		CUT	,	C	-	OVE		C	
CVE	С		CVE		С	_	CVE	-64 C	С		CVE		С	
2014-9979	TO	~	2017-110		ТО	~	2015-9		TA	1	2015-89		TO/TA	•
2015-8999	TO		2017-149	10 TO TO TO TO	ТО	<ul> <li>Image: A start of the start of</li></ul>	2015-9		TA	1	2015-89		TO/TA	
2015-9070	TO		2017-149	016	ТО	$\checkmark$	2015-9		TA	1	2015-90	05	TO/TA	
2015-9071	TO		2017-149	017	ТО	-	2015-9	174	TA	-	2015-90	07	TO/TA	
2015-9072	TO		2017-171	76	ТО	$\checkmark$	2015-9	183	TA	1	2016-24	32	TO/TA	
2015-9073	TO		2017-180	)71 [	ТО	$\checkmark$	2017-6	293	TA	$\checkmark$	2016-102	297	TO/TA	$\checkmark$
2015-9108	ТО		2017-181	28	ТО	-	2017-18	310	TA		2017-62	89	TO/TA	$\checkmark$
2015-9112	ТО		2017-181	29	ТО	$\checkmark$	2017-18	312	TA	?	2017-149	913	TO/TA	$\checkmark$
2015-9113	TO		2017-181	32 1	ТО	$\checkmark$	2017-18	317	TA	?	2017-182	293	TO/TA	$\checkmark$
2015-9198	TO		2017-181	33 1	ТО	$\checkmark$	2018-5	210	TA	$\checkmark$	2017-182	296	TO/TA	?
2015-9199	ТО	-	2017-183	311 1	то	$\checkmark$	2018-5	885	TA	1	2017-182	297	TO/TA	$\checkmark$
2015-9200	ТО		2017-183	314	ТО	?	2014-9	932	TO/TA	$\checkmark$	2017-182	298	TO/TA	1
2016-2431	ТО		2017-183	315	ТО	1	2014-9	935	TO/TA	1	2018-58	66	TO/TA	$\checkmark$
2016-10238	то		2018-35	88 1	ТО	1	2014-9	936	TO/TA	1	2017-182	282	HW	-
2016-10432	ТО		2018-58	70 7	ТО	1	2014-9	937	TO/TA	1	2015-90	03	CI	-
2017-6290	то		2016-102	239 7	то	1	2014-9	945	TO/TA	1	2016-103	398	CI	-
2017-6292	ТО		2018-119		ТО	1	2014-9		TO/TA	1	2017-149		CI	-
2017-6294	TO		2015-44		TA	1	2014-9		TO/TA	1	2017-18		CI	-
2017-8274	ТО		2015-66		TA	1	2015-8	995	TO/TA	1	2016-104		BL	-
2017-11010	TO		2015-66		TA	1	2015-8		TO/TA	1	2017-149	1000	BL	-
2017 11010	10		2012 00				2010 0		10/111		2017 112		DL	
										<b>1</b>				
In Scope	? [	ΓO	TA T	O	TA	4	HW	CI	BL		Total	Per	rcentag	e
Y		2	1 3	34	1	1					66	8	6.84%	
-		4												
N				2	1		1	4	2		10	1	3.16%	



76

4

2



Total

21

36

12

# Availability of PPC/ACU primitives on COTS platforms

Vendor	SoC Platform	PPC	ACU	RZ?
NXP	iMX8MQ iMX8QM	RDC xRDC	Cortex-M4 SCU	YES YES
Xilinx	Ultrascale+ MPSoC Versal ACAP	XMPU, XPPU, (SMMU)	PMU	YES YES
Nvidia	Tegra X1/X2 Xavier	SMMU	BPMP	YES YES
Socionext	SC2A11	SMMU	SCP	YES
Qualcomm	Snapdragon 845, 855, 865, 888, 8gen1	SMMU, XPU*	SPU	YES
Broadcom	Stingray	SMMU	SCP	YES
Samsung	Exynos 990 Exynos 2100	Periph. MMUs Periph. MMUs	iSE eSE	N/A N/A
Mediatek	Dimensity 1200	X	X	No
HiSilicon	Kirin 9000	X	PMU	No



# In Summary





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- Performance of real-world TAs (e.g., DRM) is not significantly affected
- **ReZone** could help **mitigate** many high severity **vulnerabilities**





# QUESTIONS?

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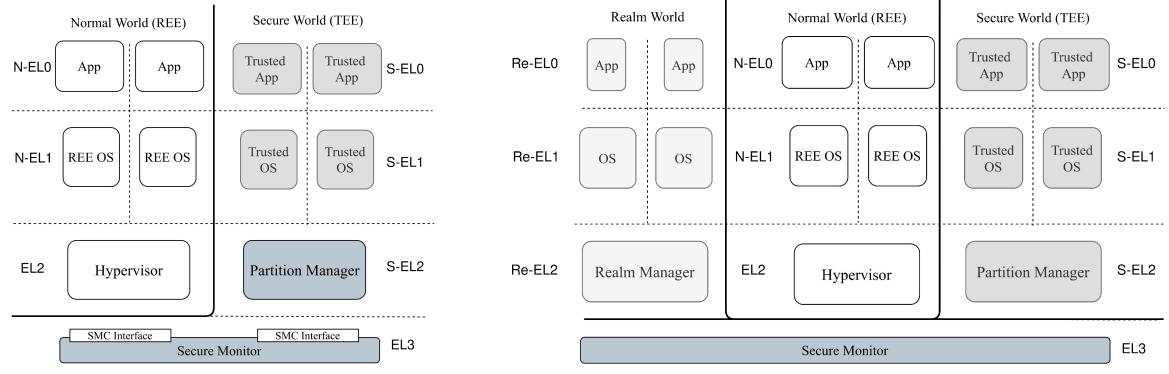
#### ReZone in Perspective

Armv8.4 S.EL2

ESRGv3

ESRG.

#### Armv9 CCA



Root World

