

SMASH

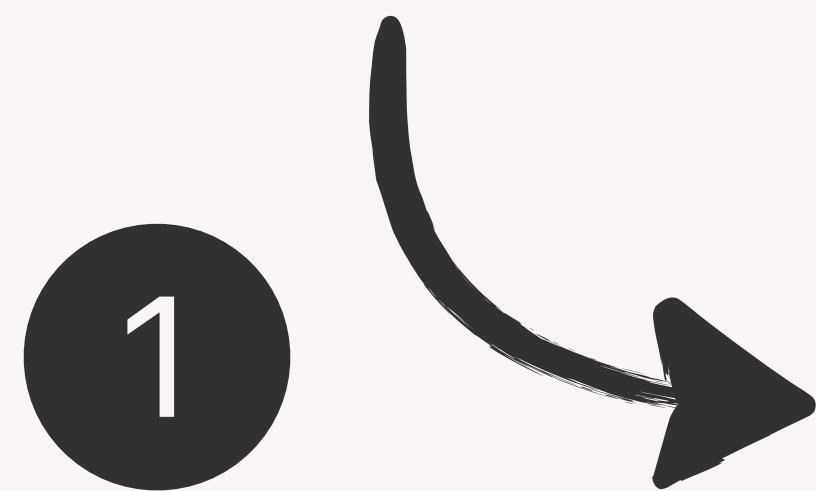
Synchronized Many-sided Rowhammer Attacks from JavaScript

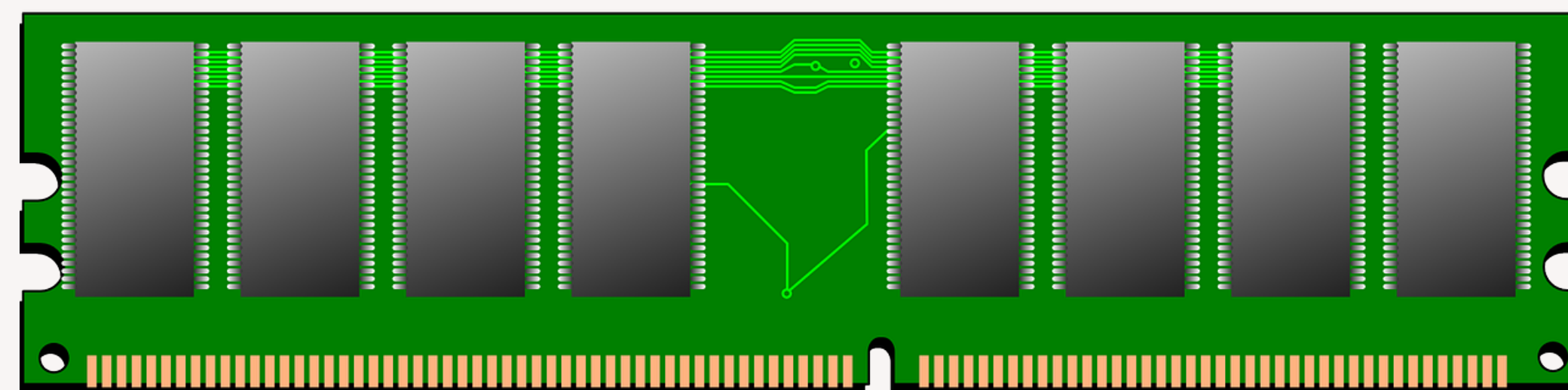
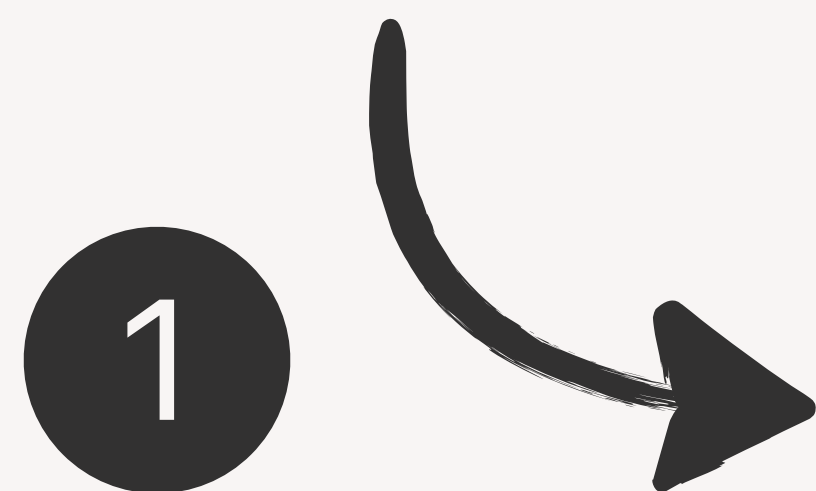
Demo on YouTube and source code on GitHub

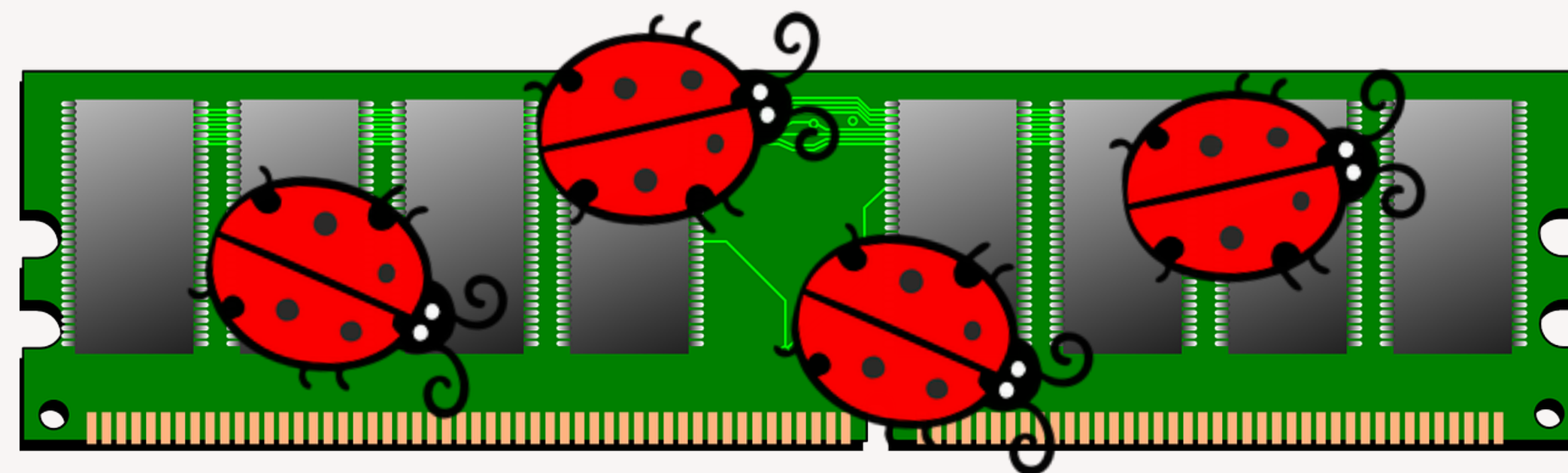
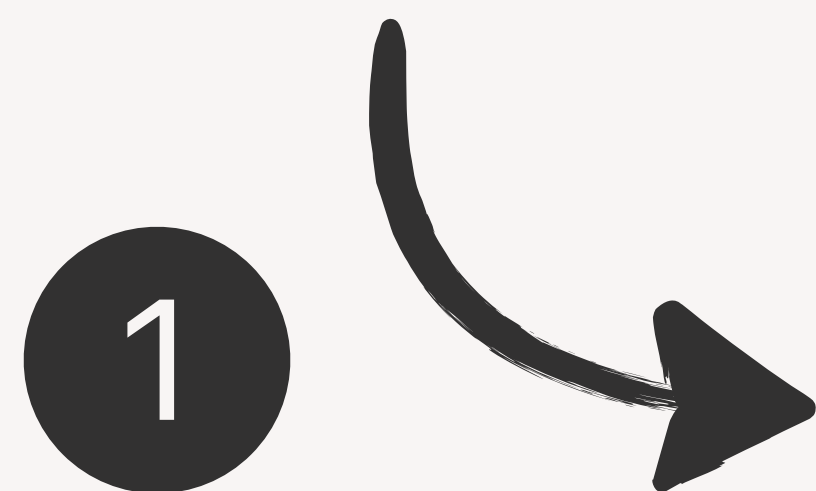
Finn de Ridder · Pietro Frigo · Emanuele Vannacci · Herbert Bos · Cristiano Giuffrida · Kaveh Razavi

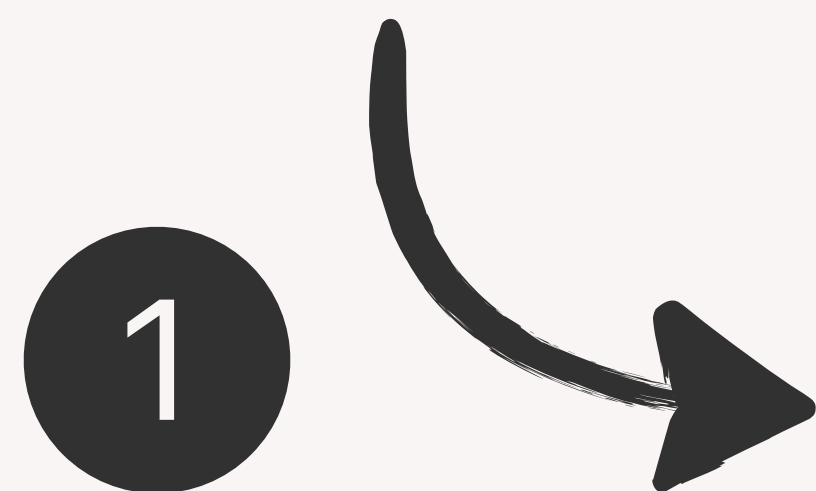
ETH Zurich and VU Amsterdam



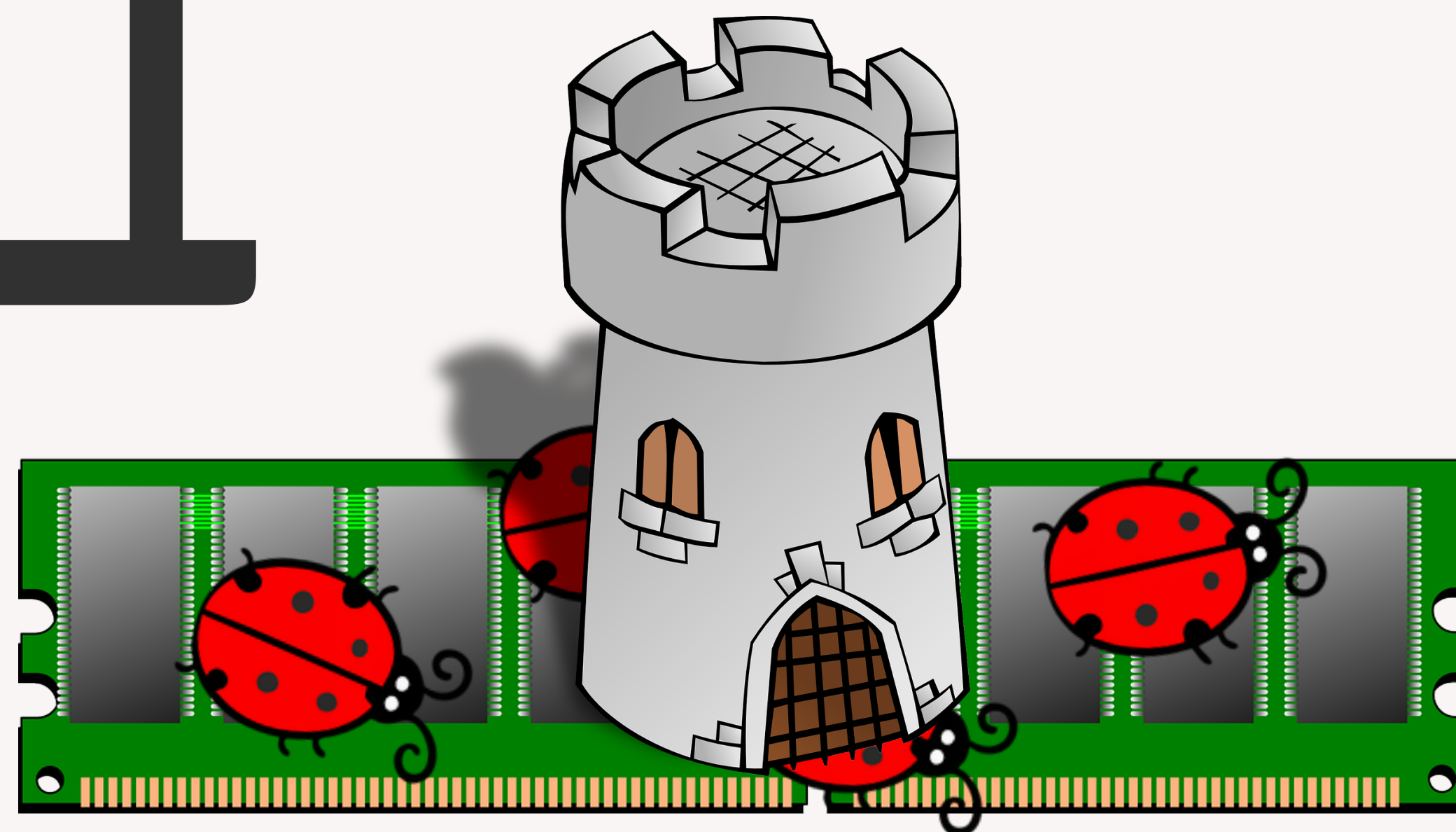


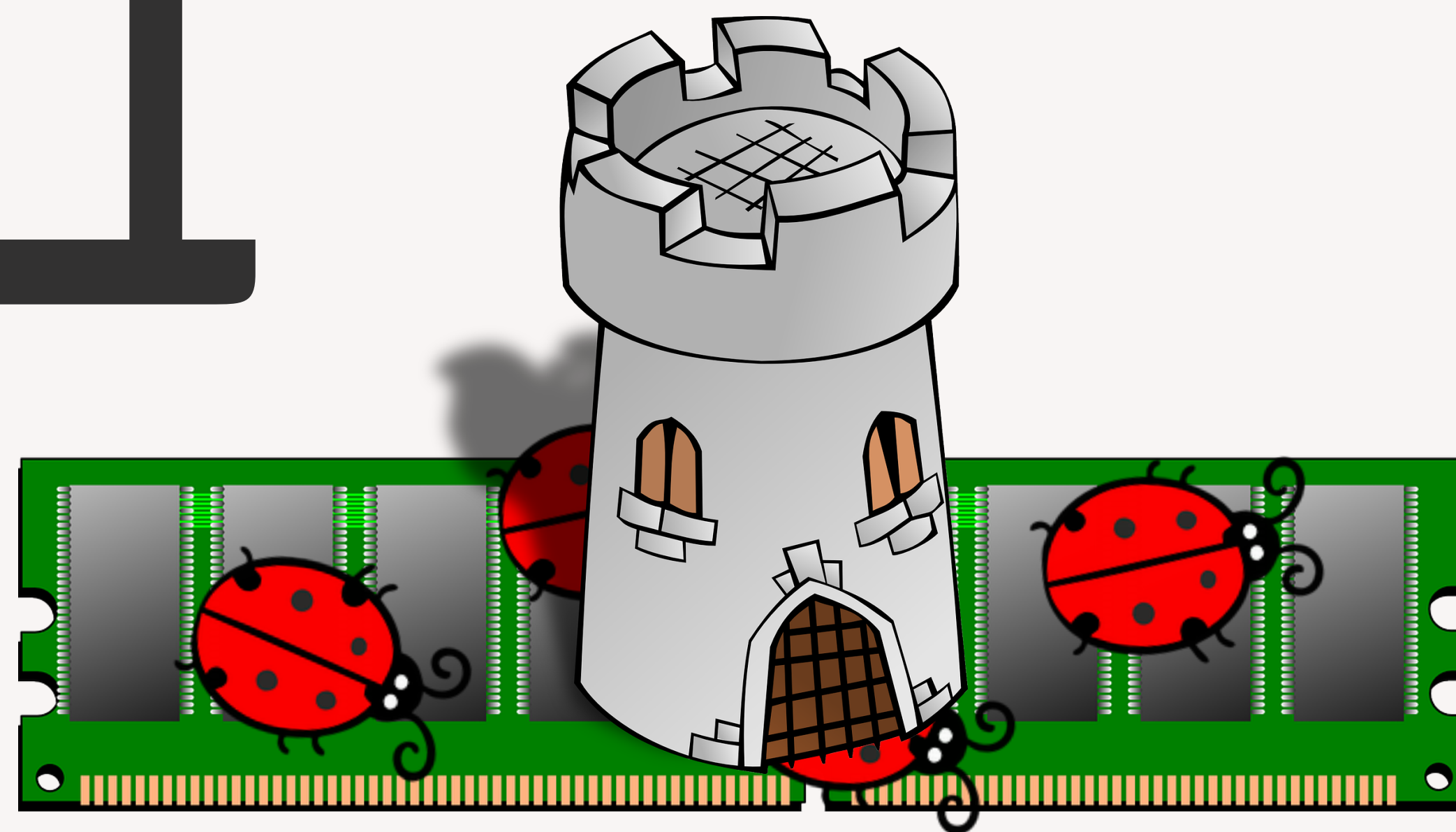
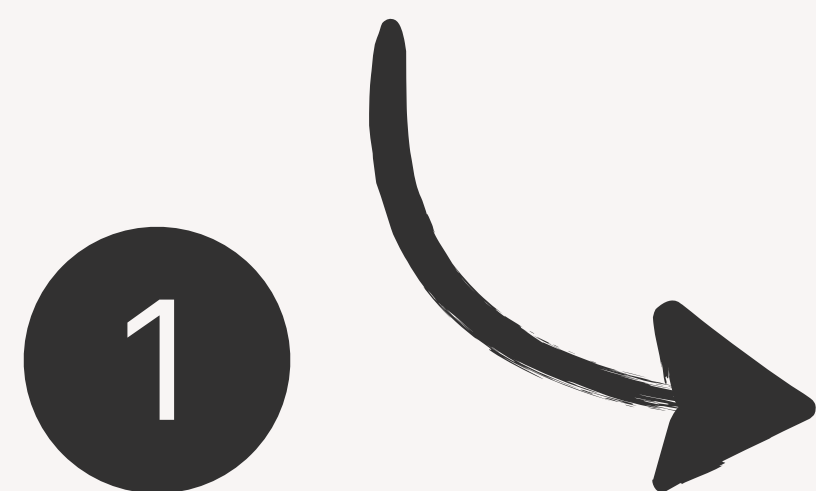




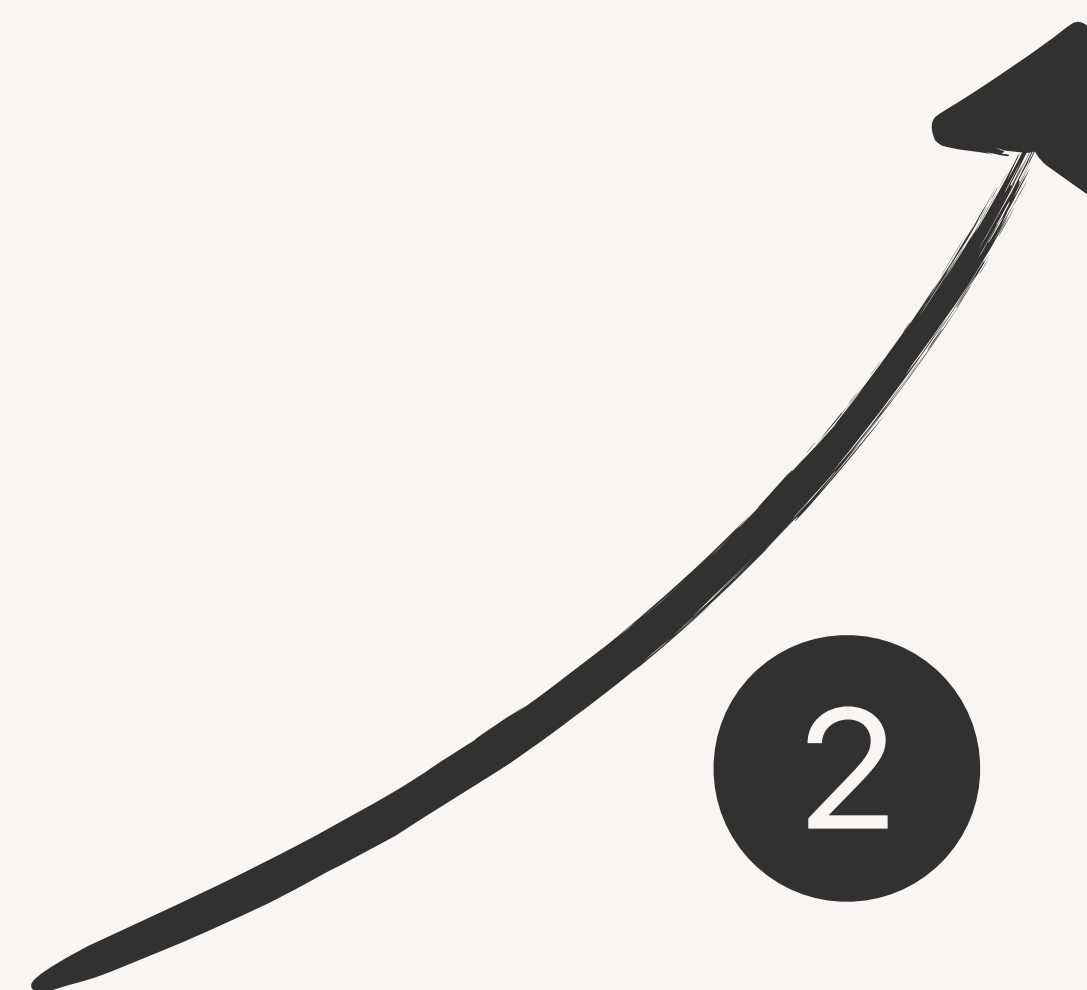


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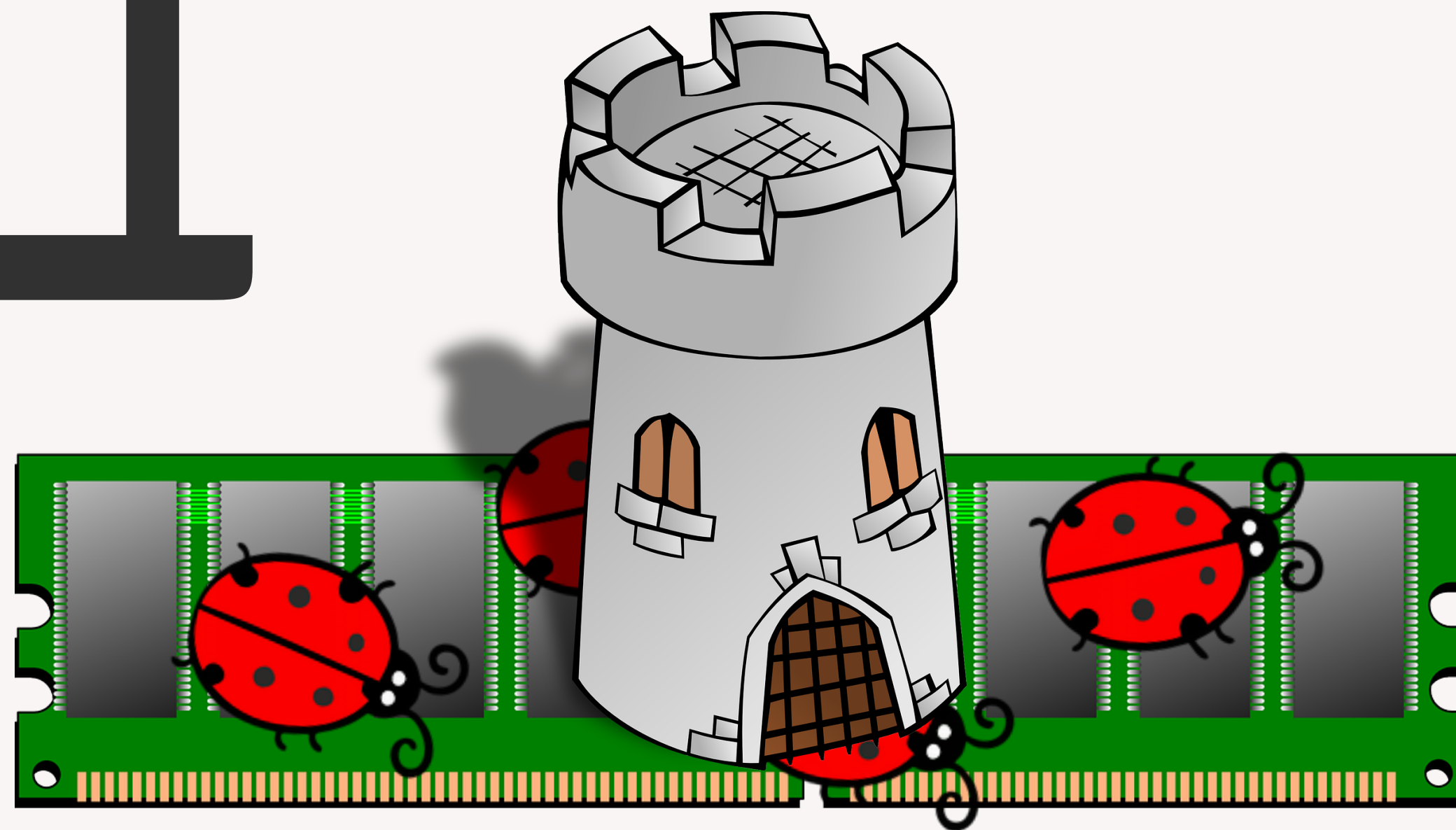
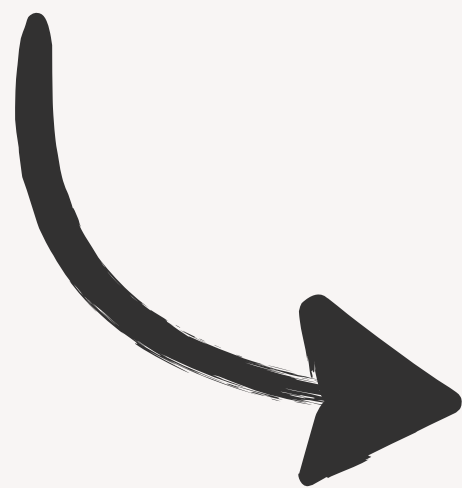


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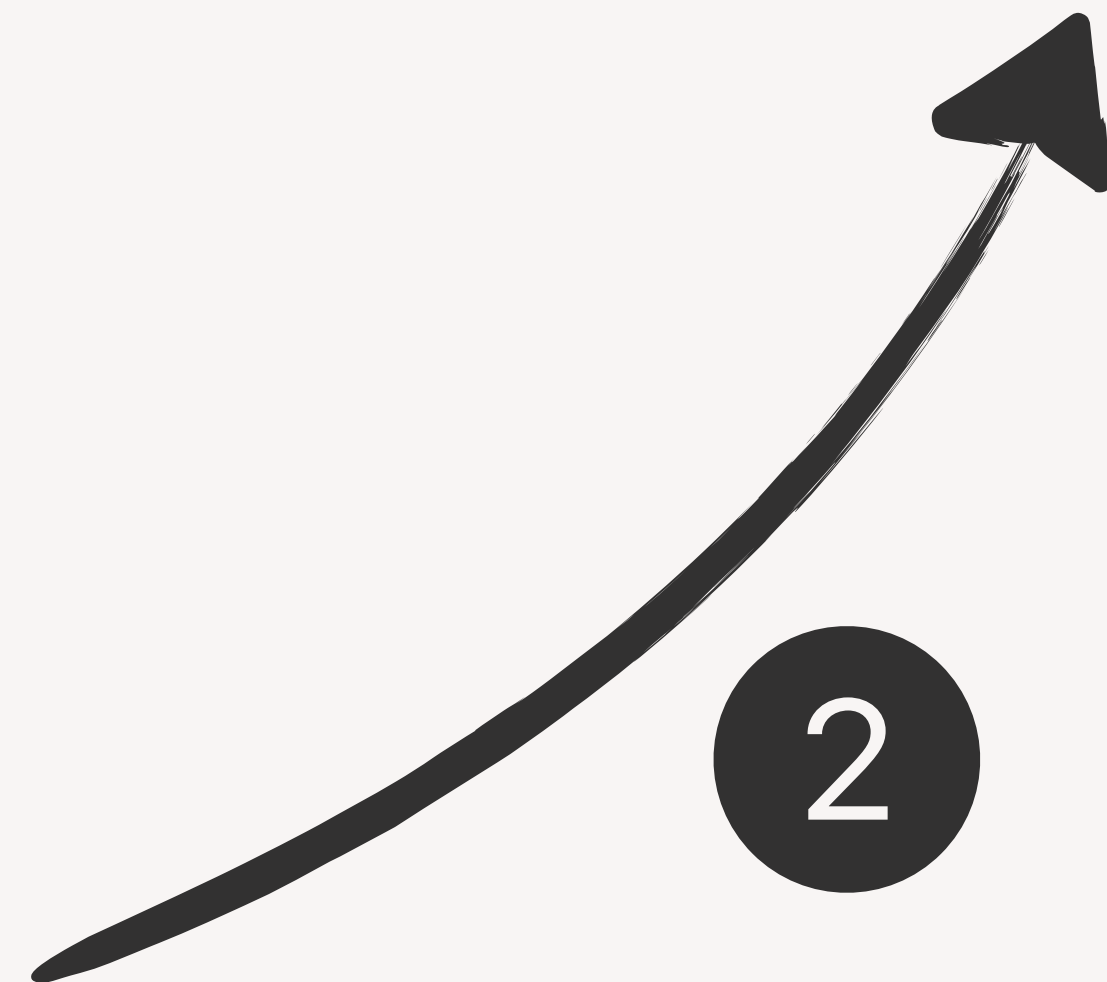




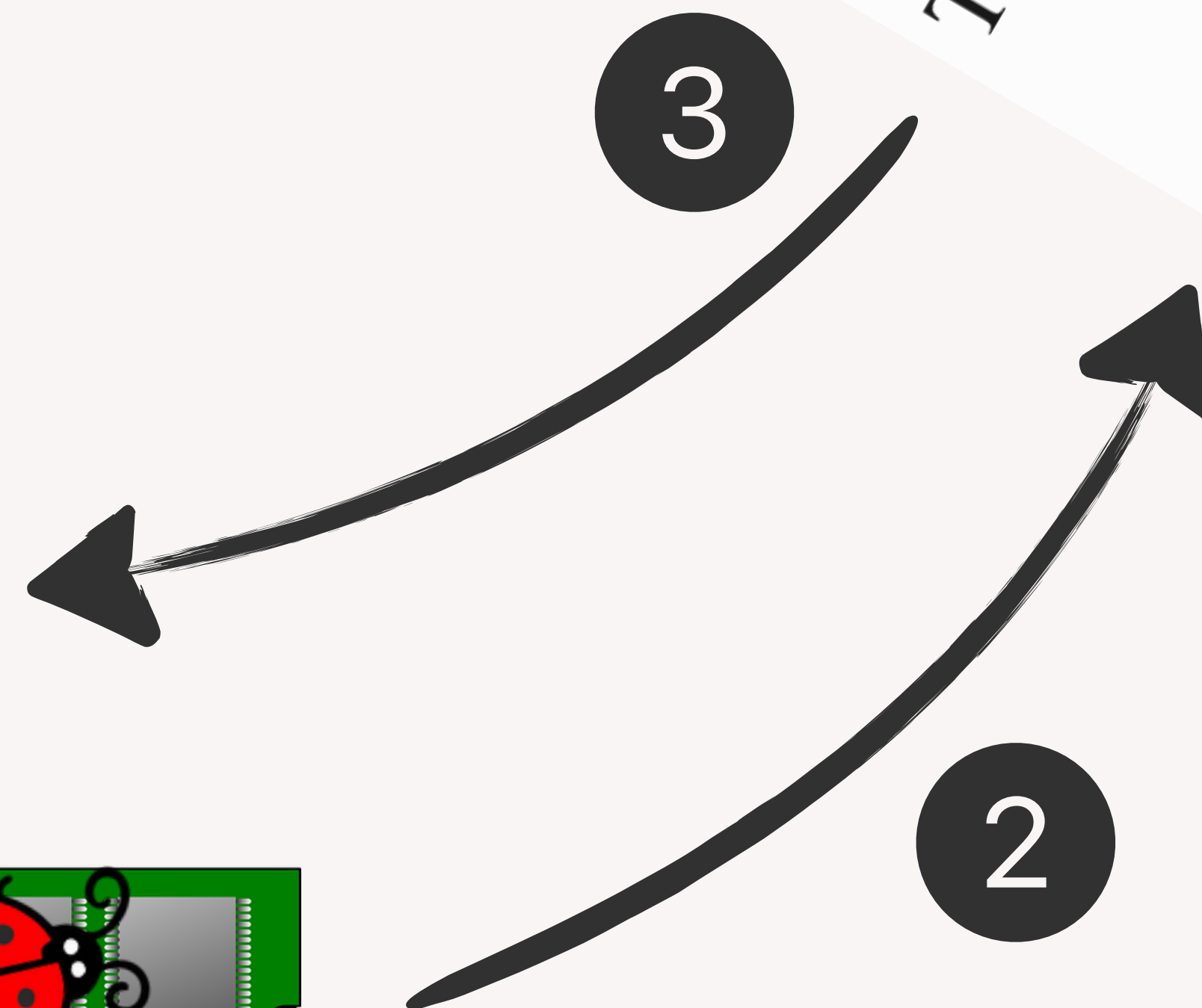
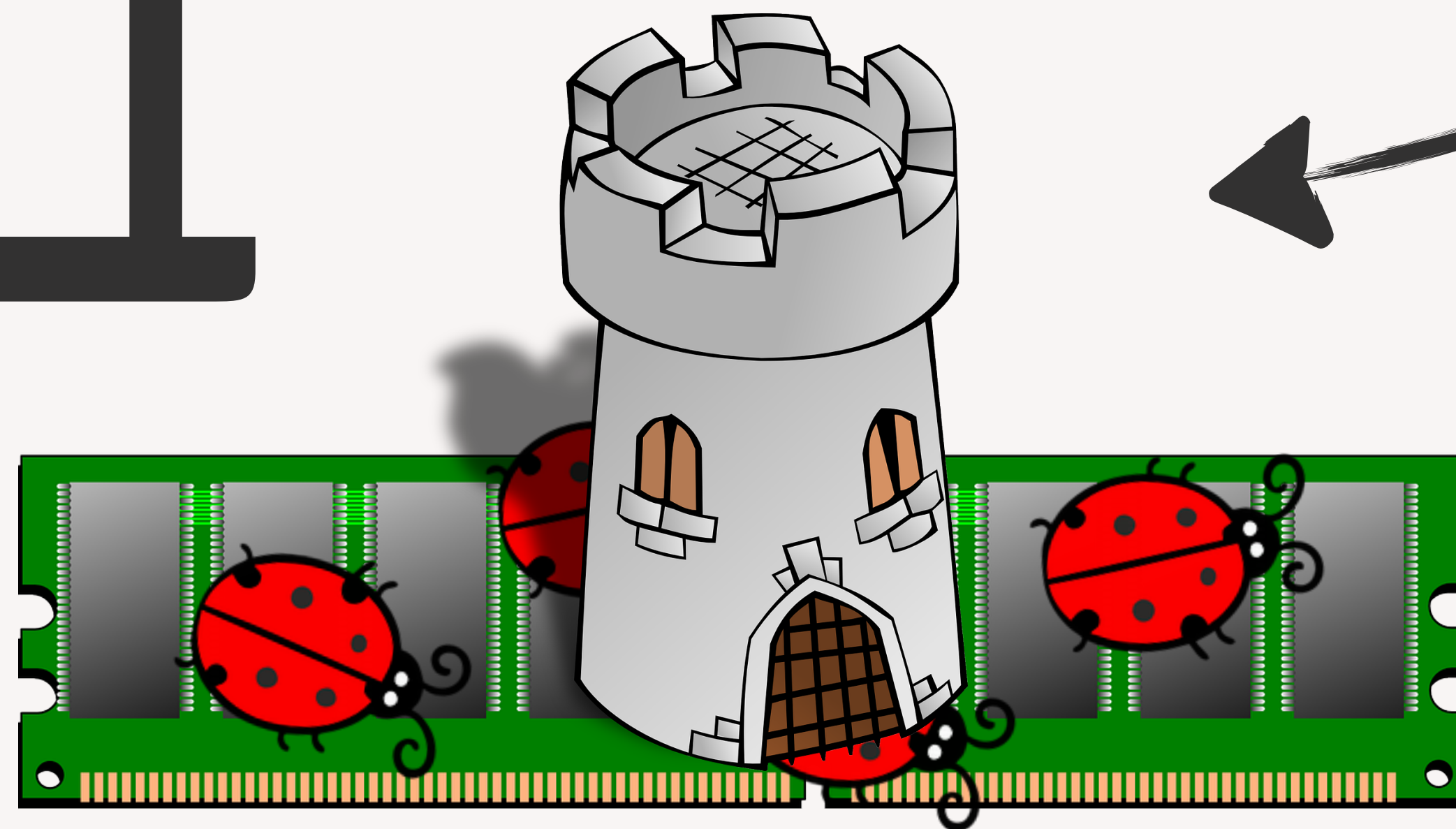
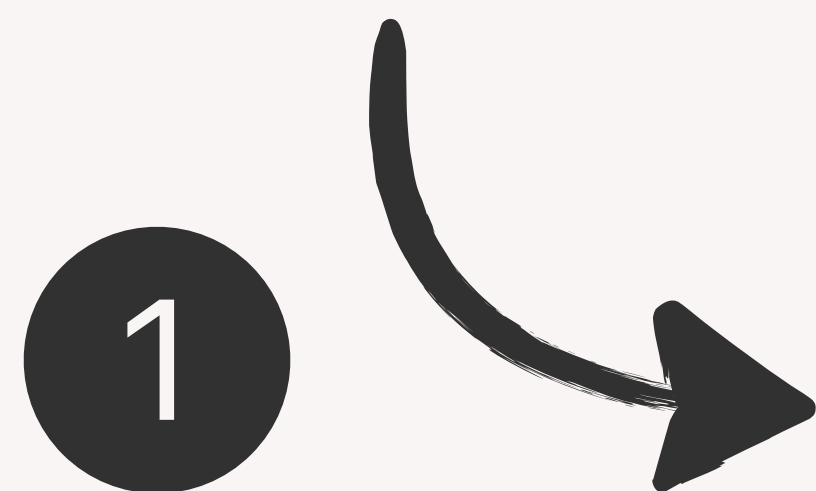
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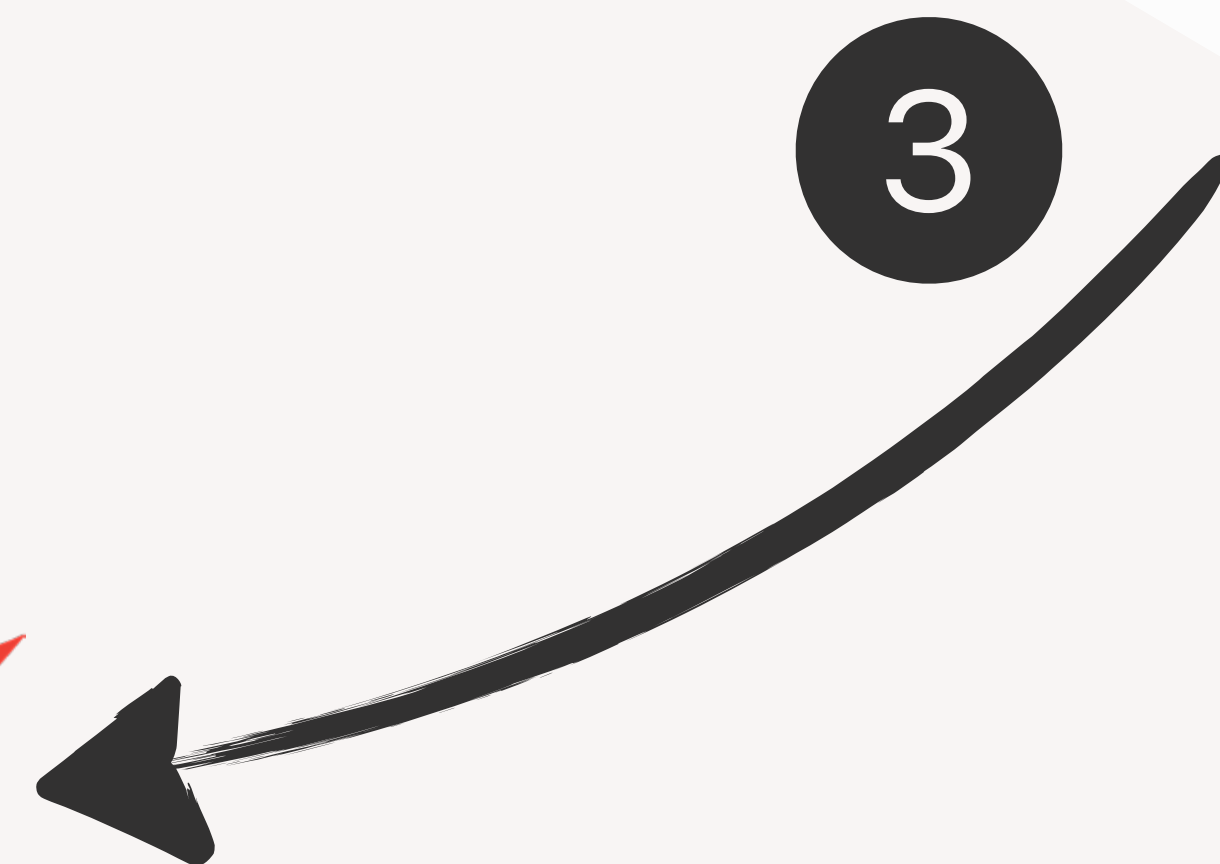
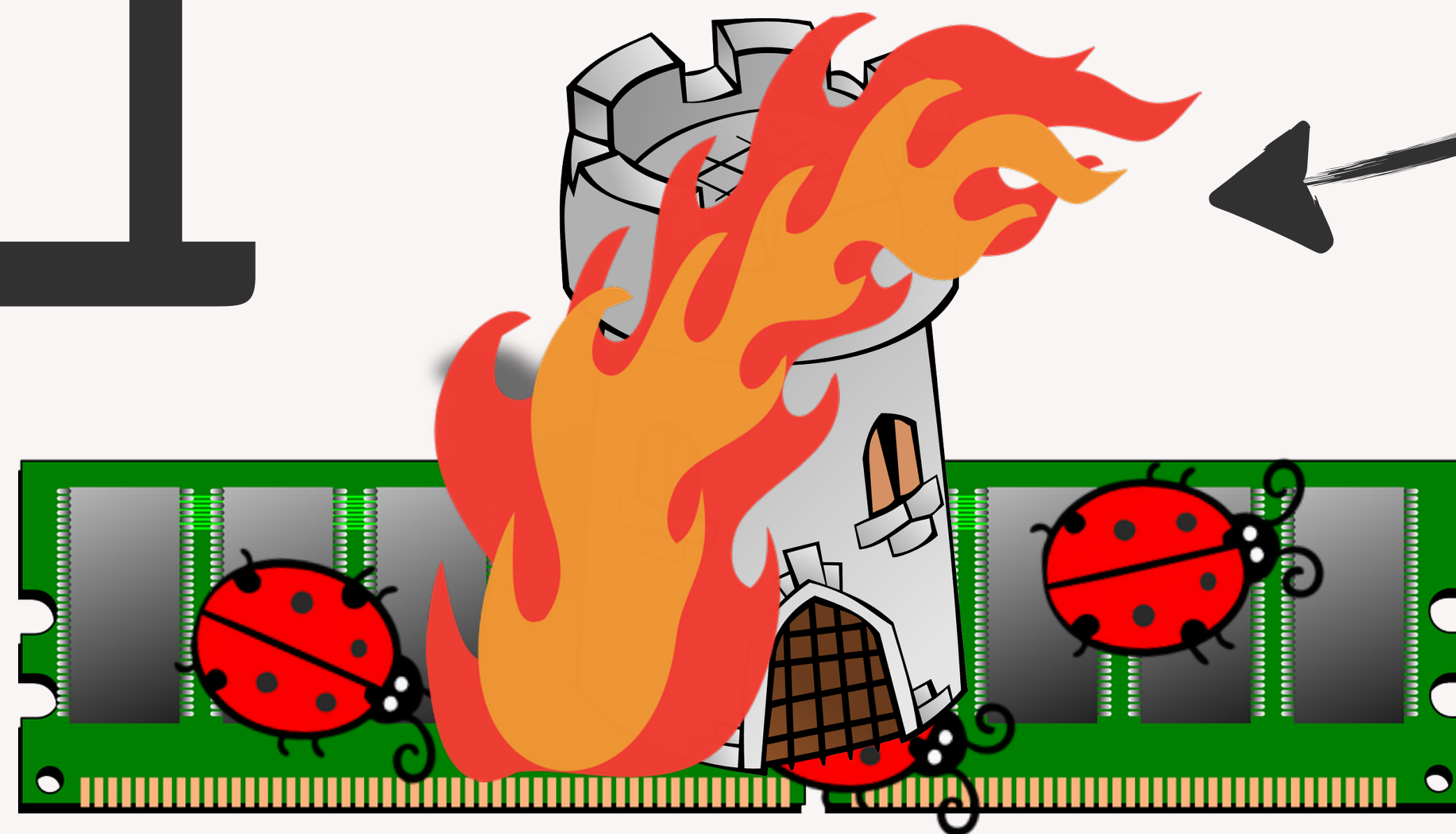
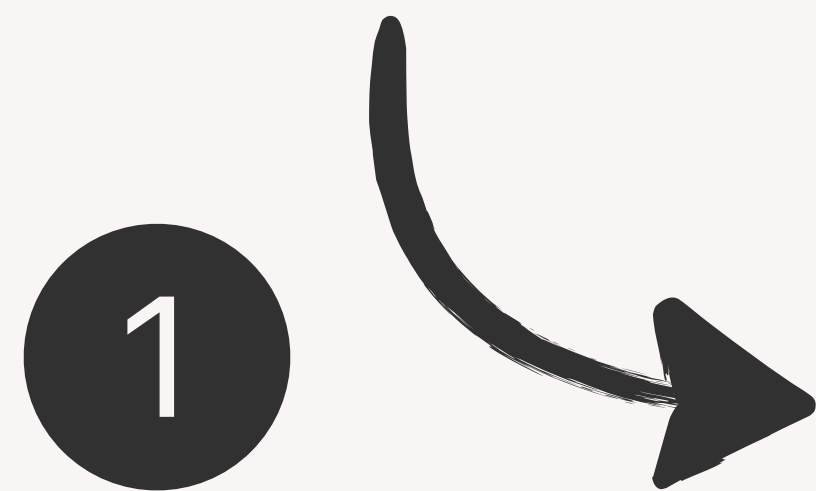
2



2020 IEEE Symposium
**TRRespass: Exploiting
Target Ro**
Pietro Frigo*† Emanuele
Onur Mutlu[§] C
*Vrije Universiteit



2020 IEEE Symposi
**TRRespass: Exploiting
Target Ro**
Pietro Frigo*† Emanuele
Onur Mutlu[§] C
*Vrije Universiteit



2020 IEEE Symposi

TRRespass: Exploiting

Target Ro

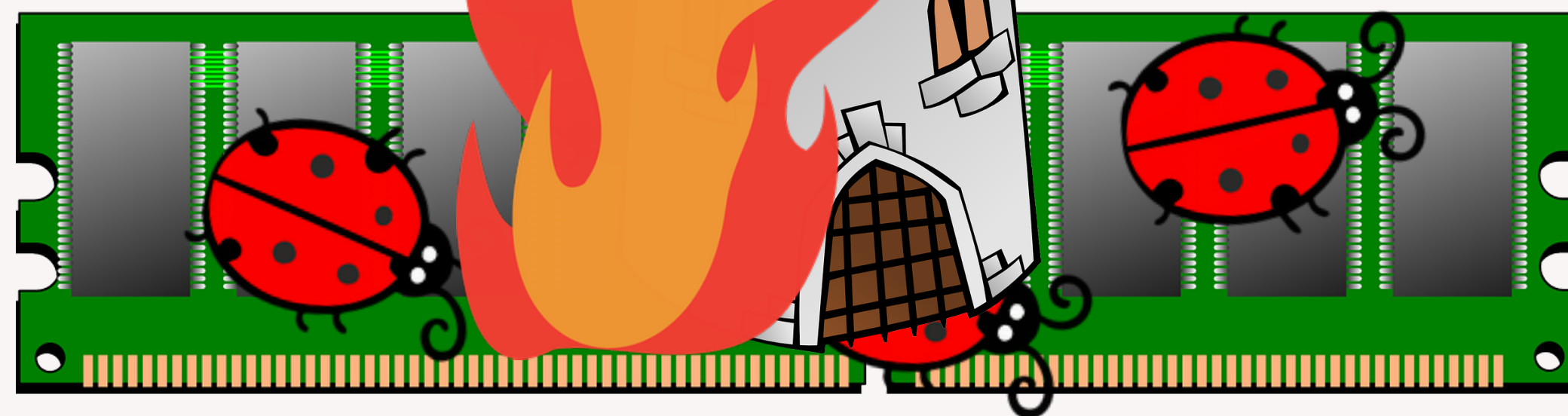
Pietro Frigo*† Emanuele

Onur Mutlu[§] C

*Vrije Universiteit



1



2



4

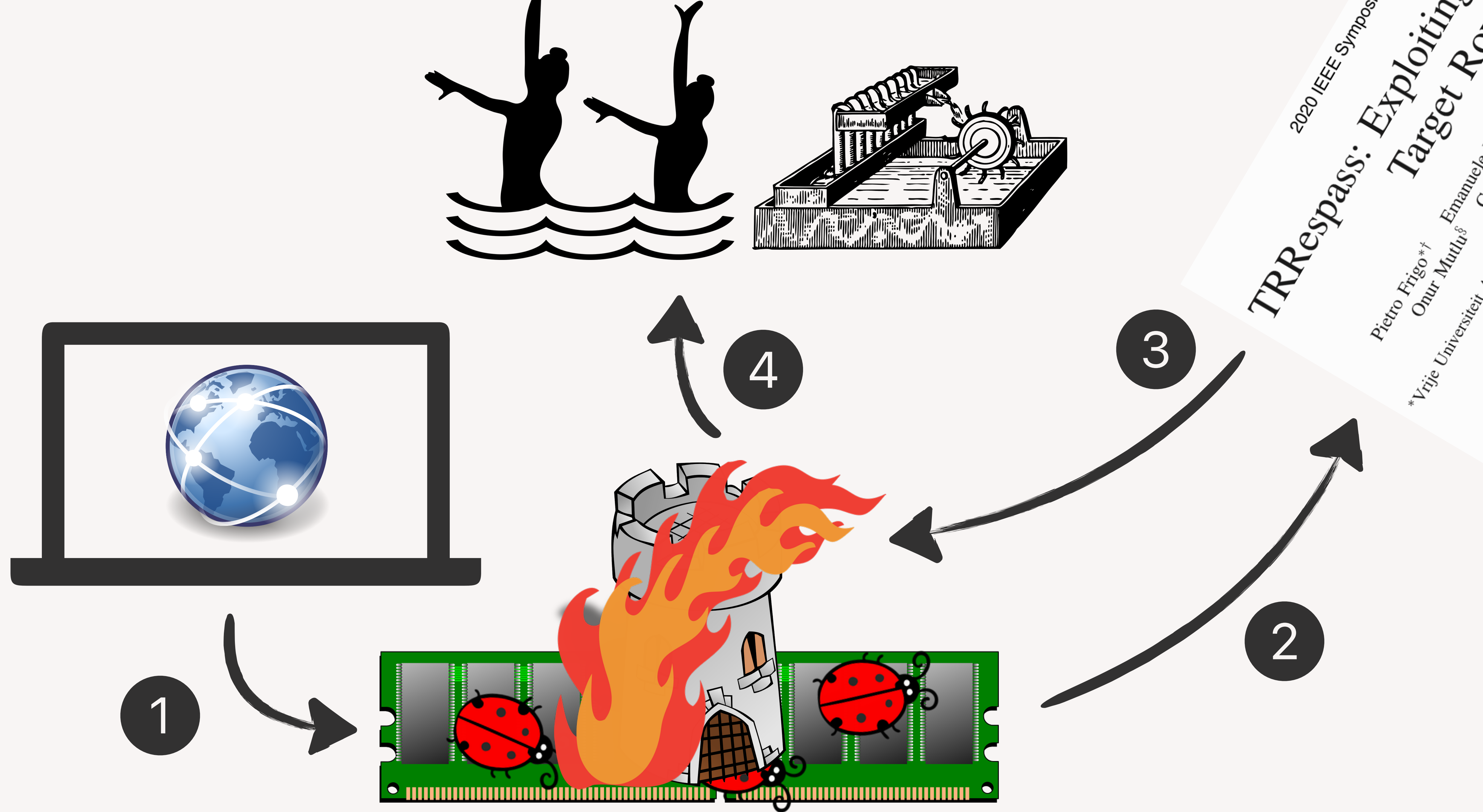


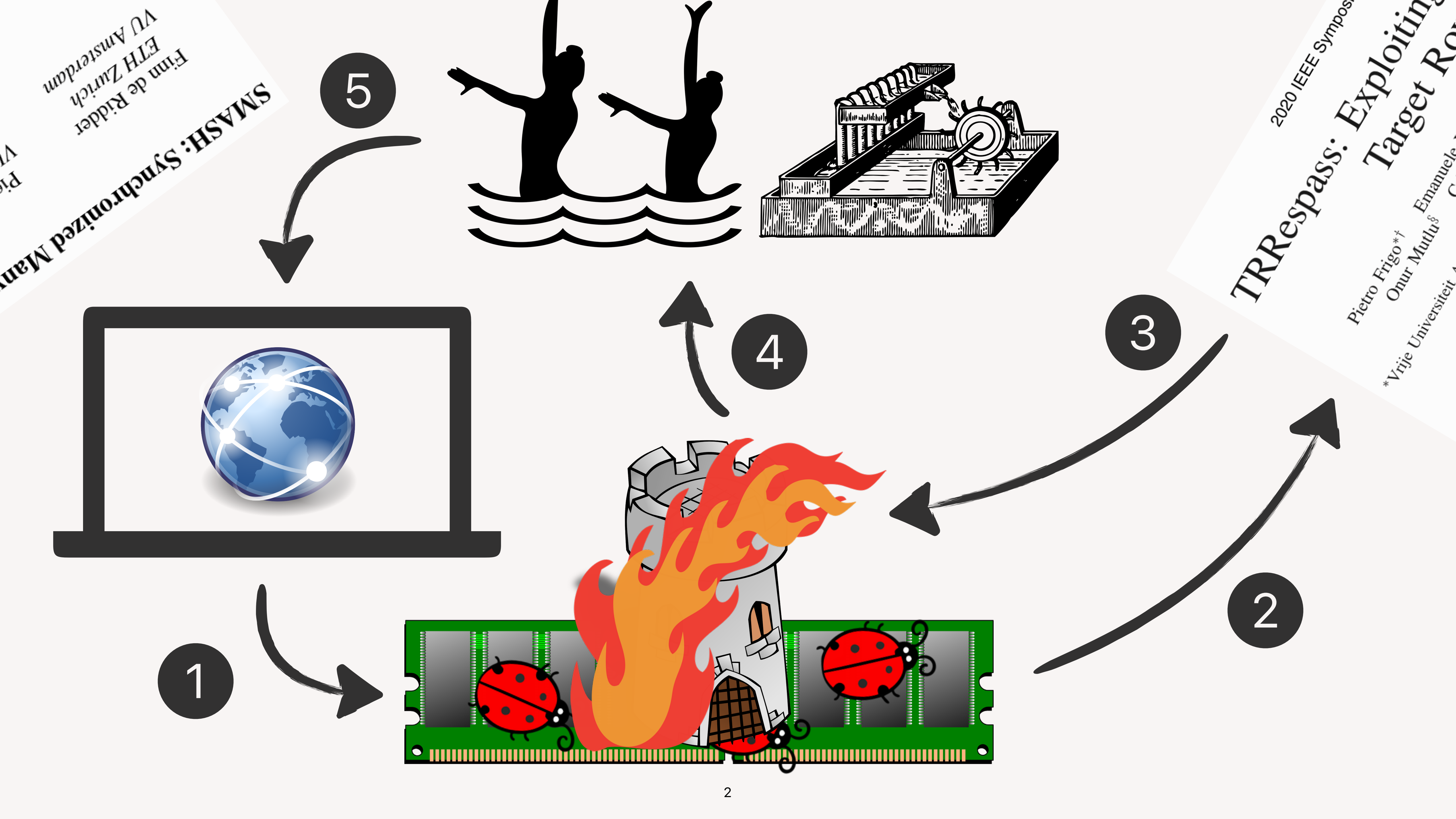
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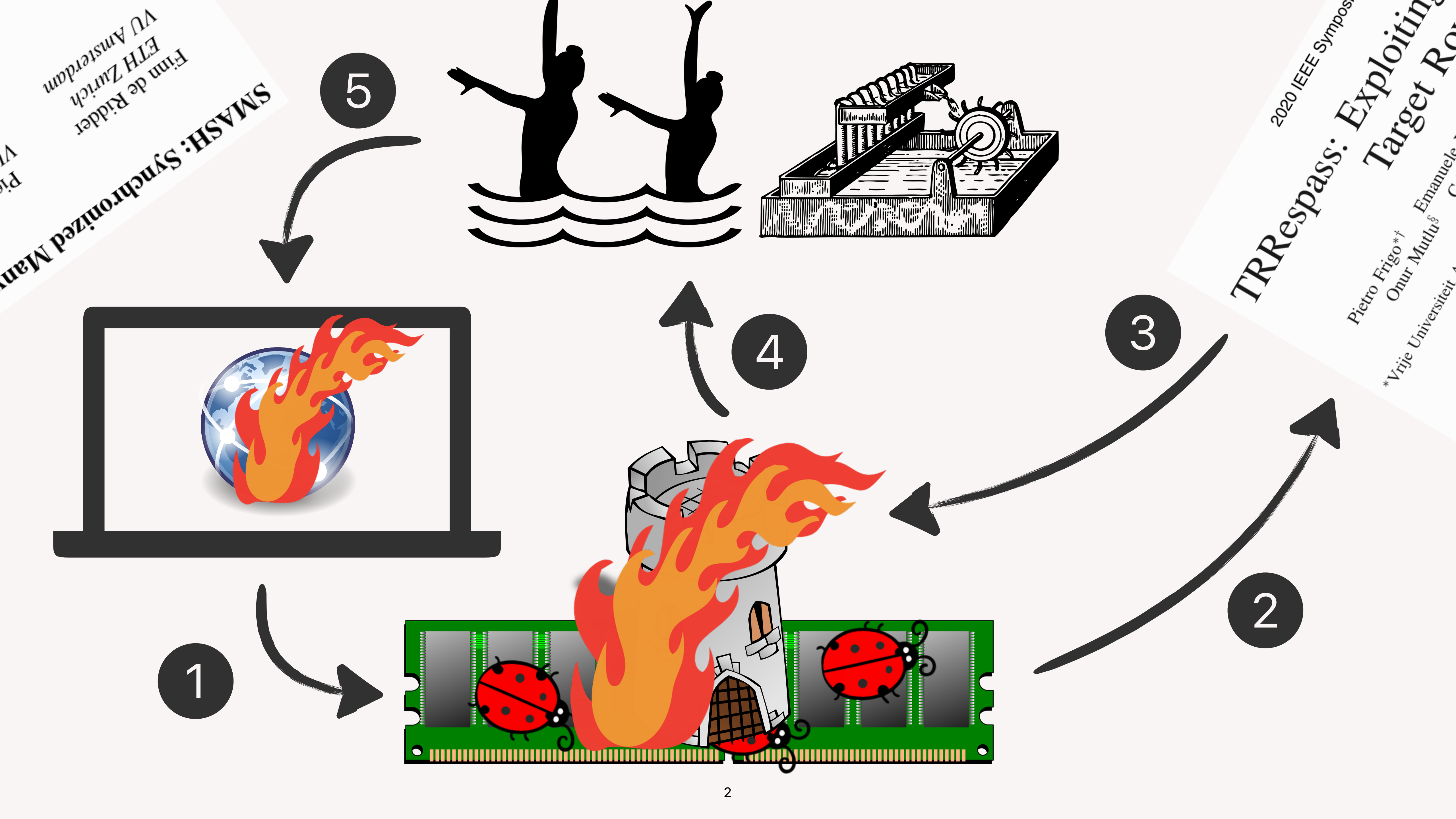
2020 IEEE Symposi
**TRRespass: Exploiting
Target Ro**
Pietro Frigo*† Emanuele
Onur Mutlu§
*Vrije Universiteit





ETH Zurich
Finn de Ridder
SMASH: Synchronized Manipulation

2020 IEEE Symposium on Security and Privacy
TRRespass: Exploiting Target Row



SMASH: Synchronized Manipulation

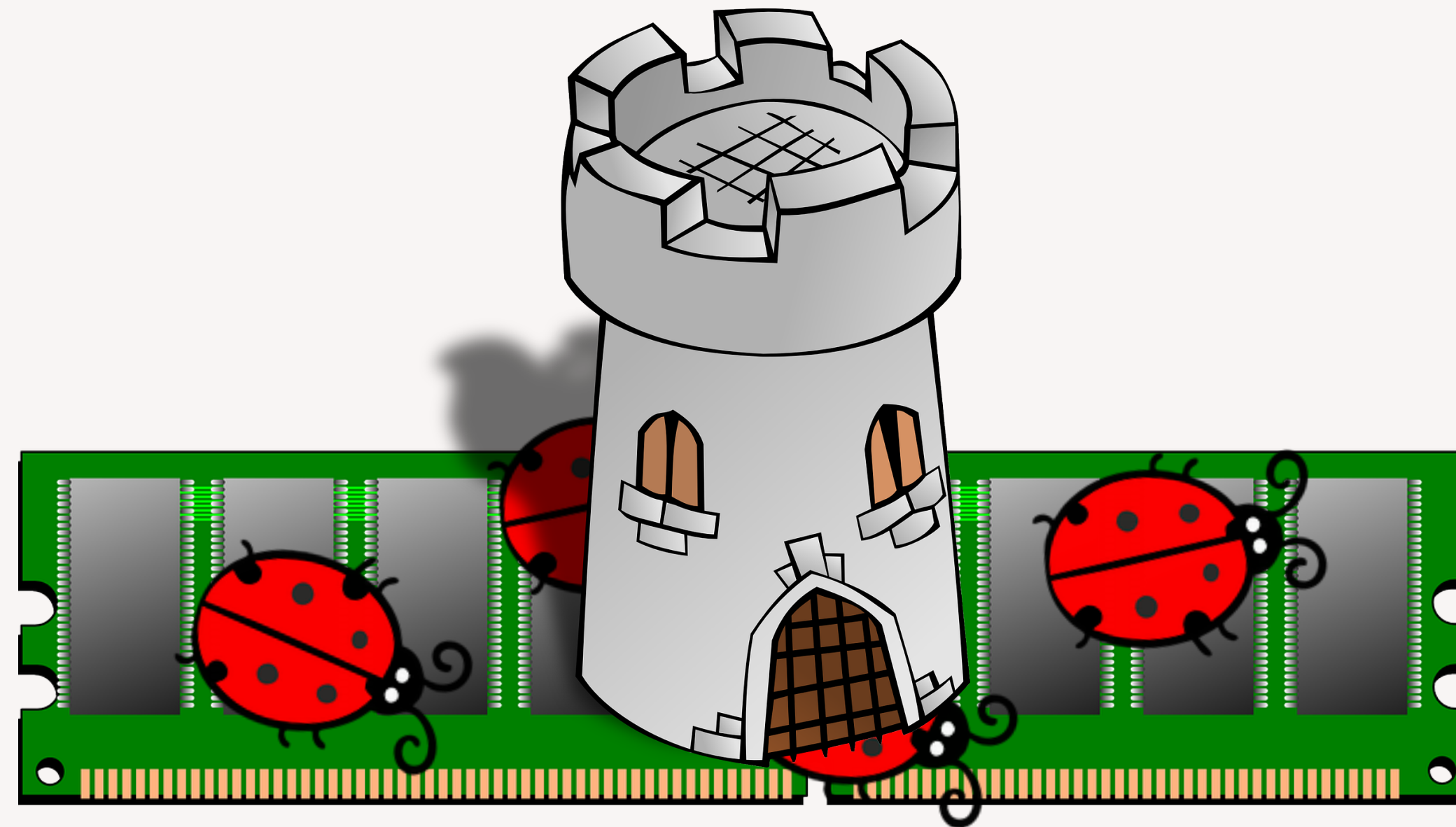
Finn de Ridder
ETH Zurich
VU Amsterdam

TRRespass: Exploiting Target Resource

2020 IEEE Symposium
Pietro Frigo*† Emanuele
Onur Mutlu*† Emanuele
*Vrije Universiteit

Rowhammer Bugs and TRR

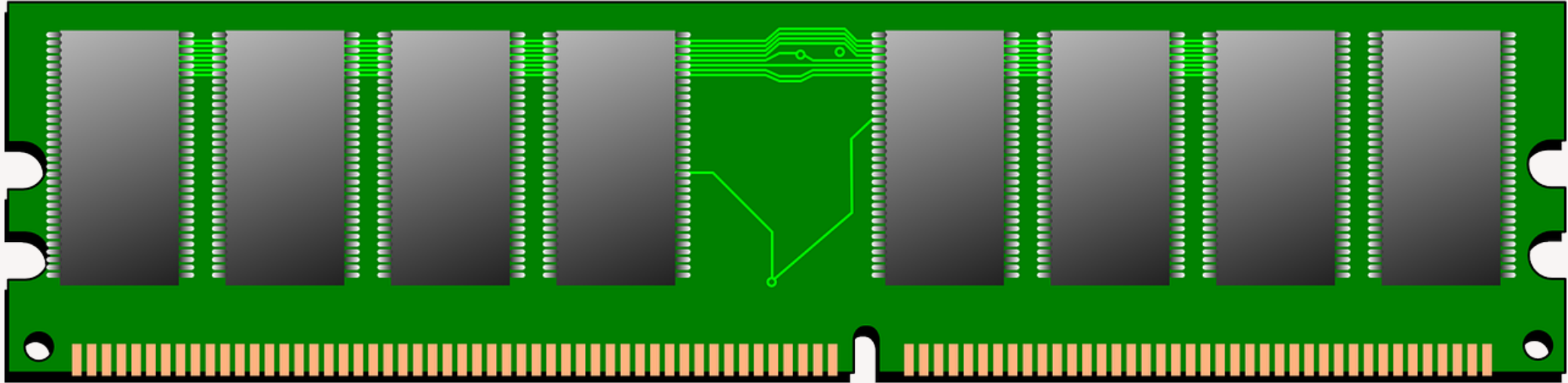
Part I



Rowhammer

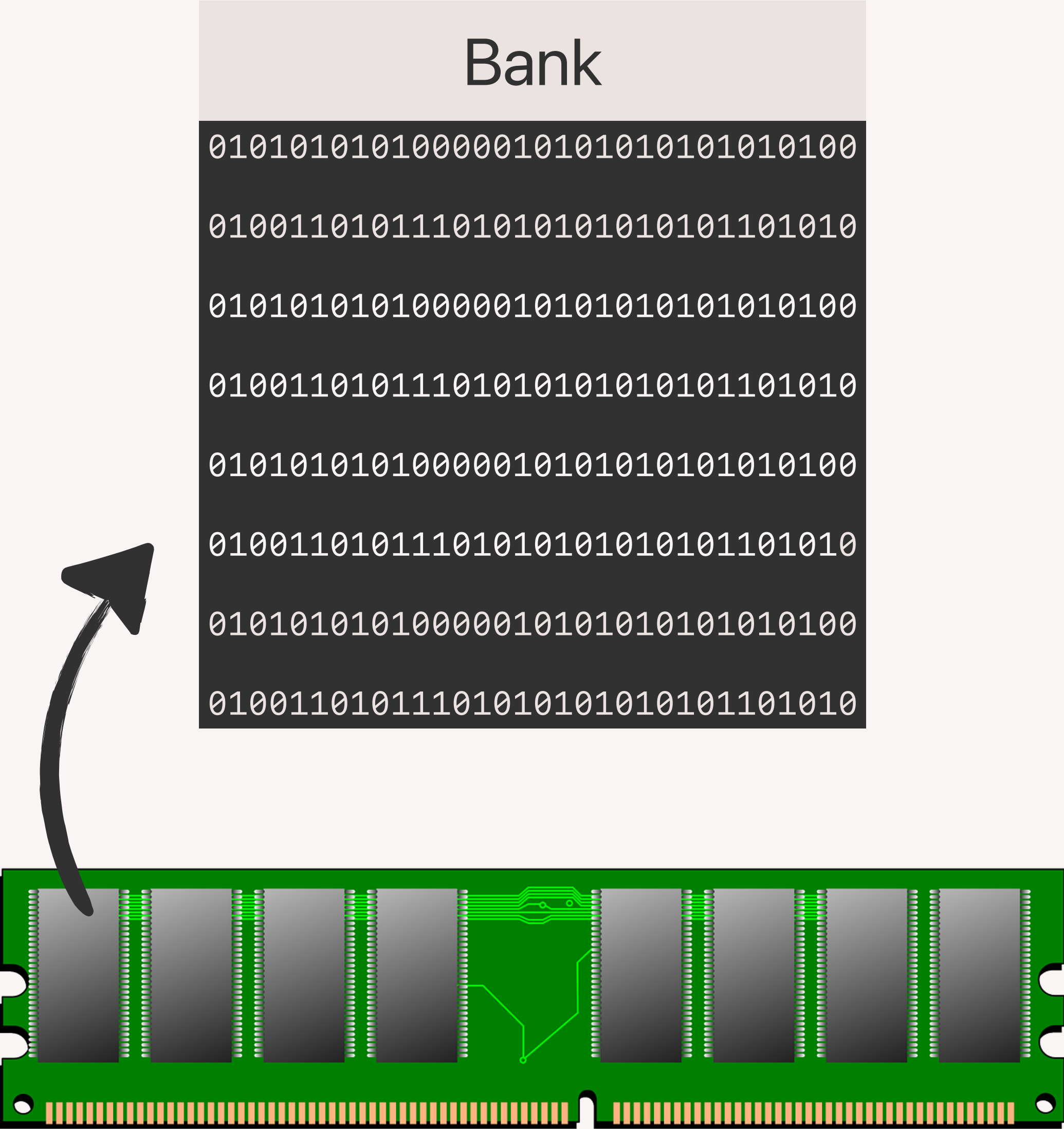
Bugs

Bank
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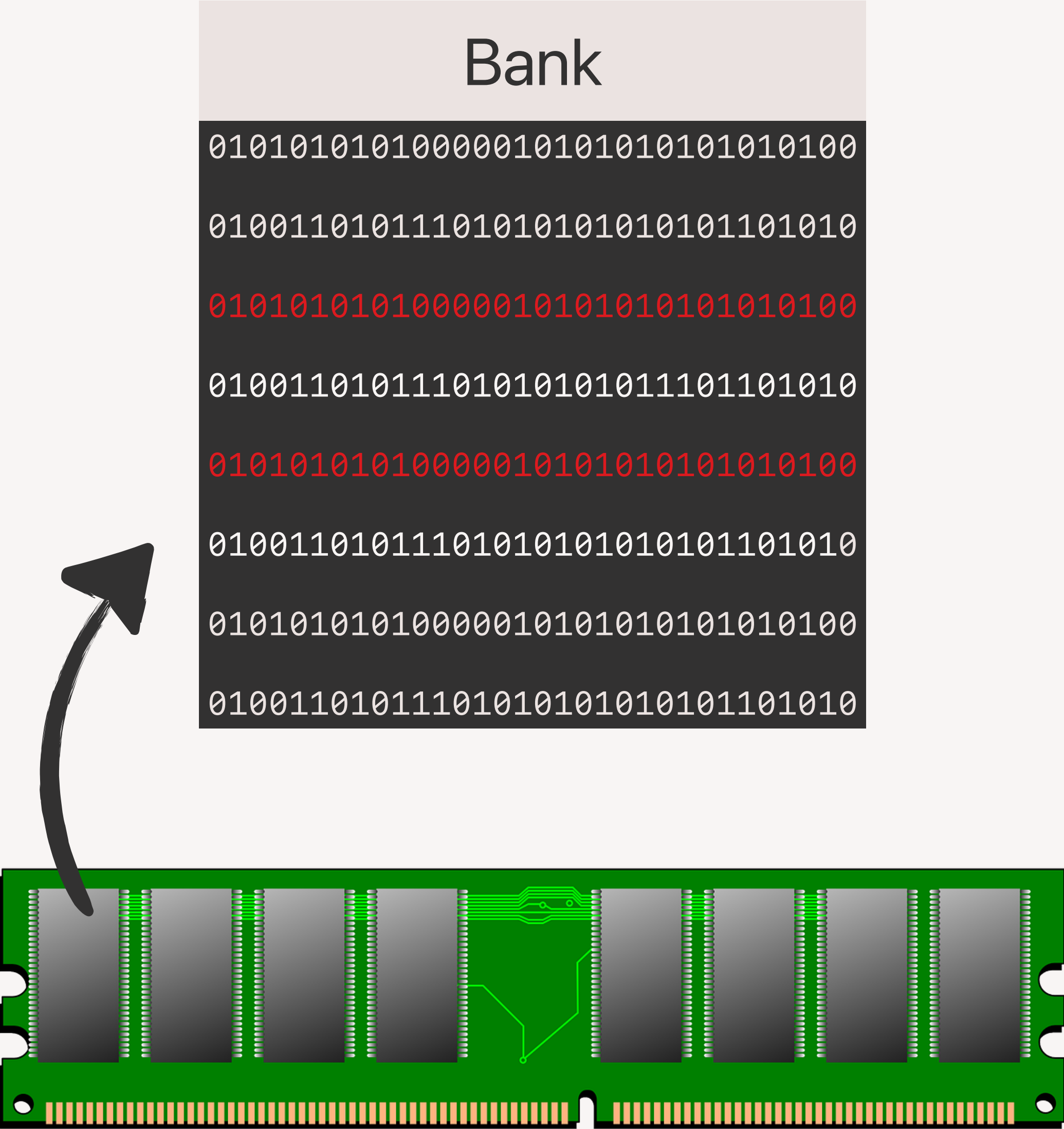
Rowhammer

Bugs



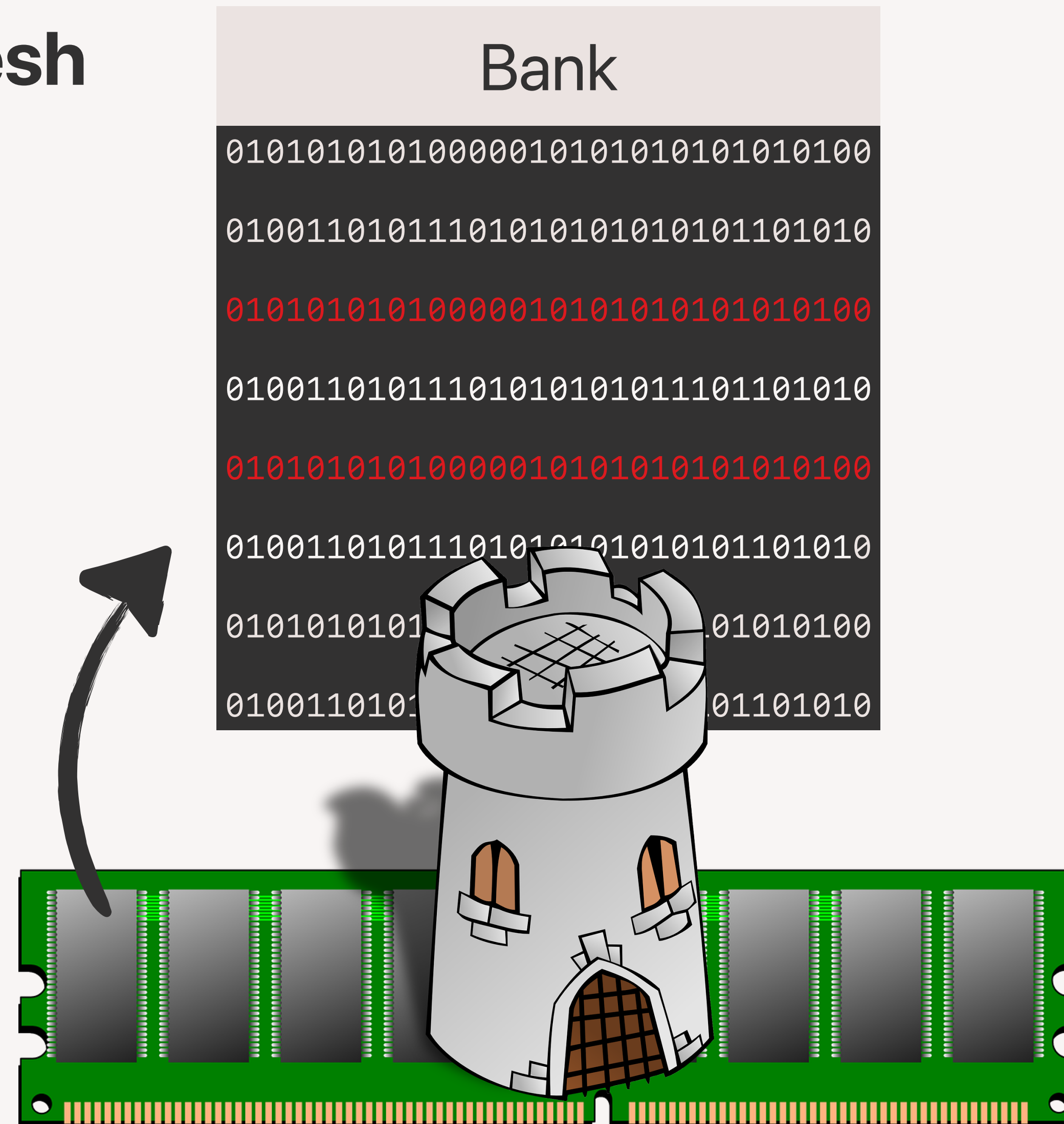
Rowhammer

Bugs



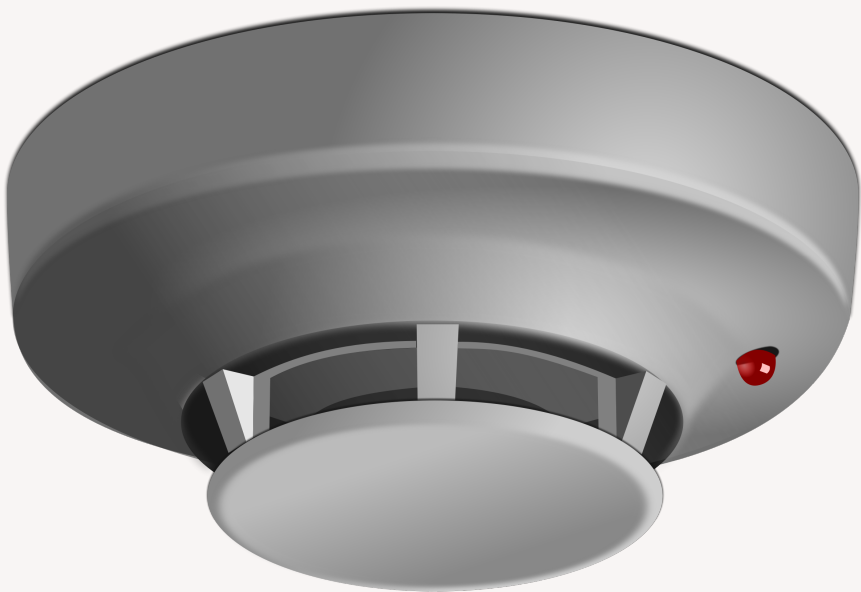
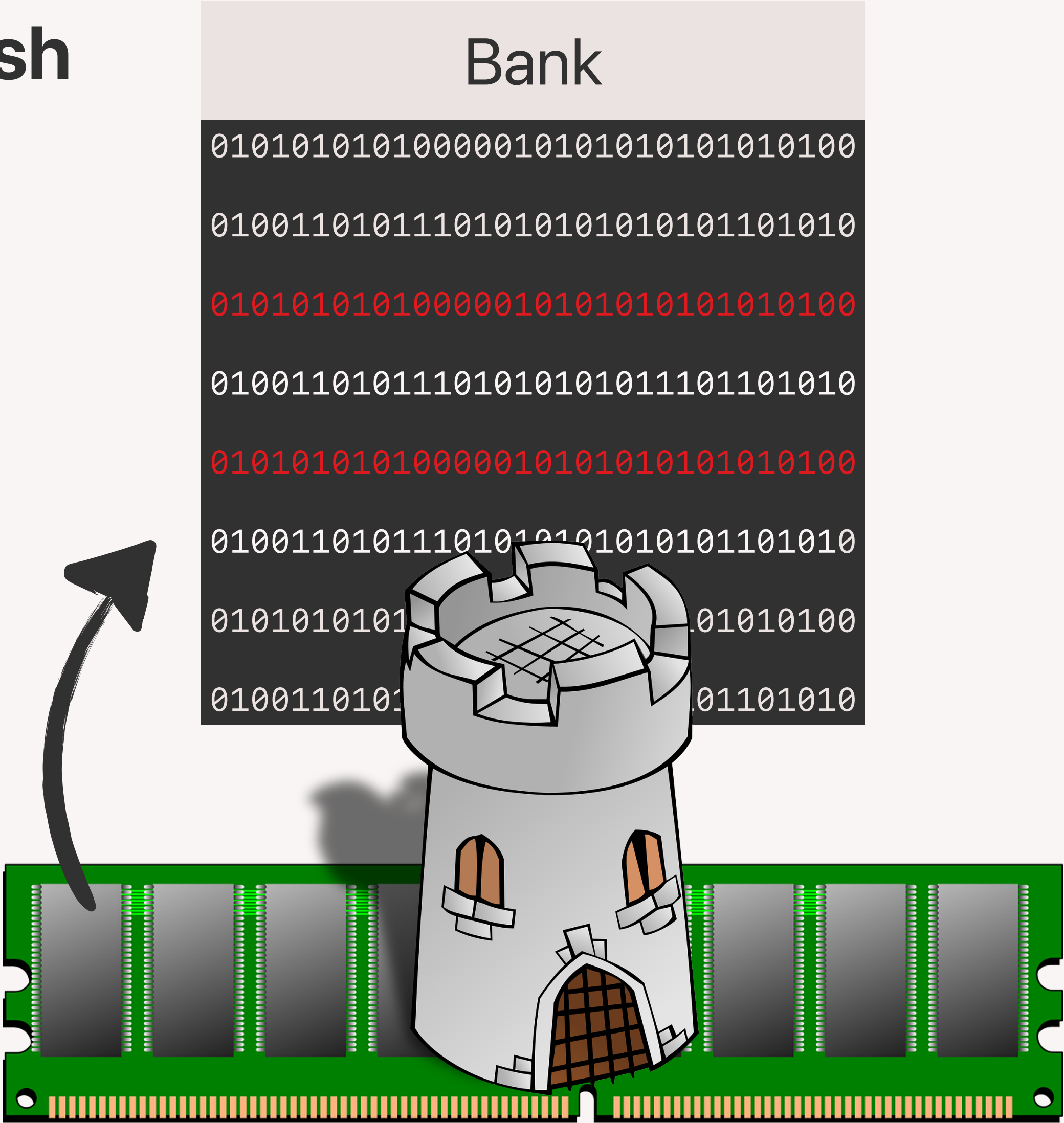
TRR

Target Row Refresh



TRR

Target Row Refresh



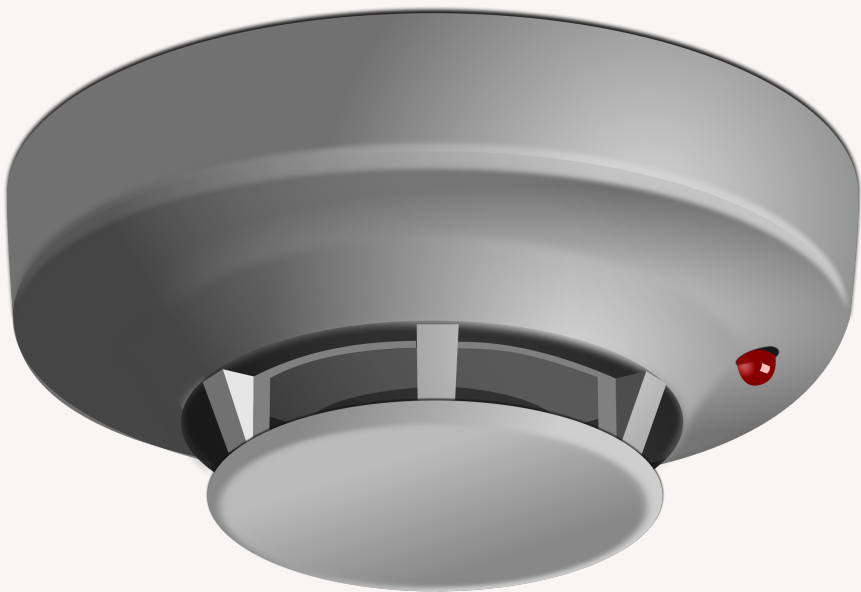
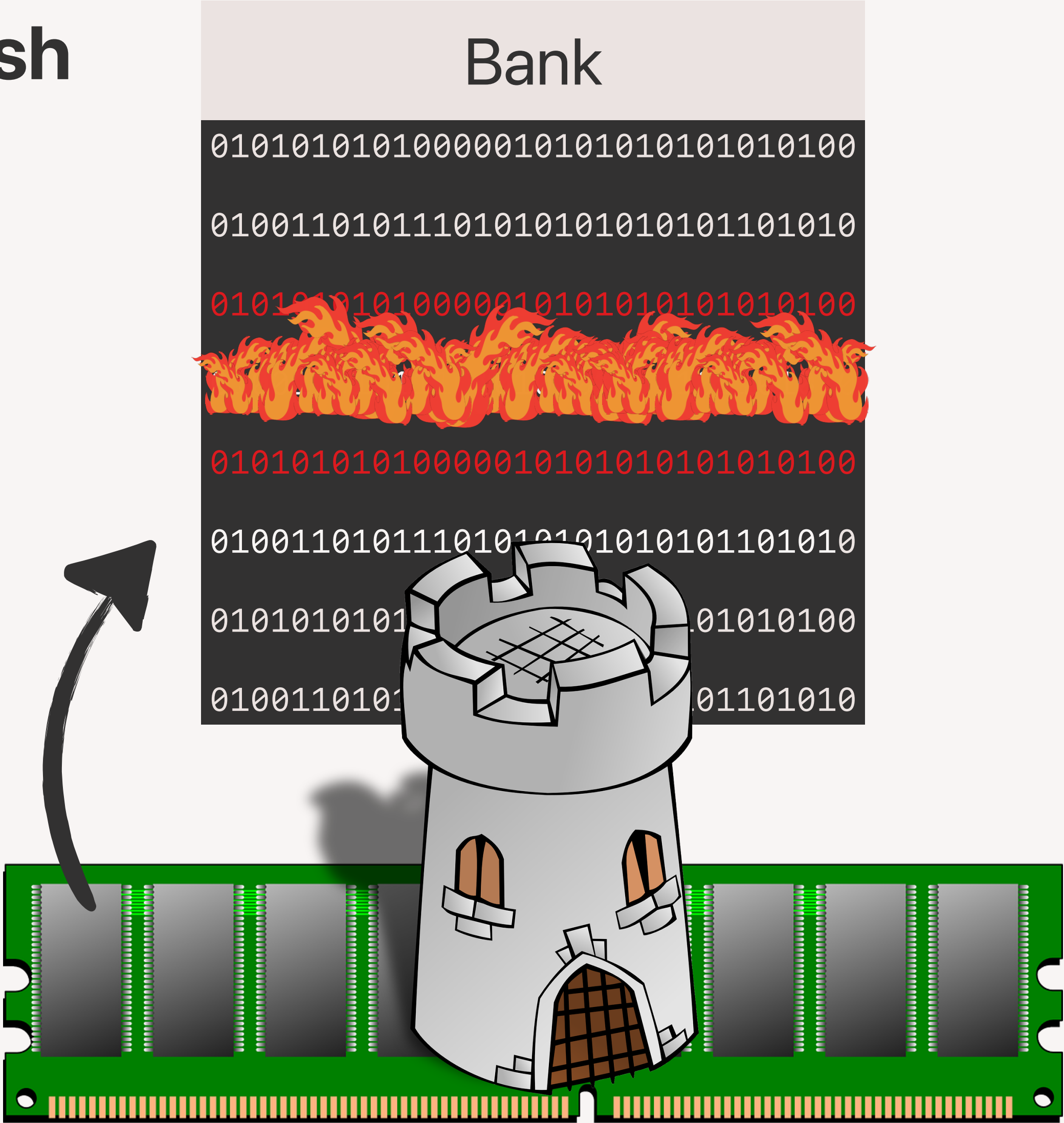
Sampler



Inhibitor

TRR

Target Row Refresh



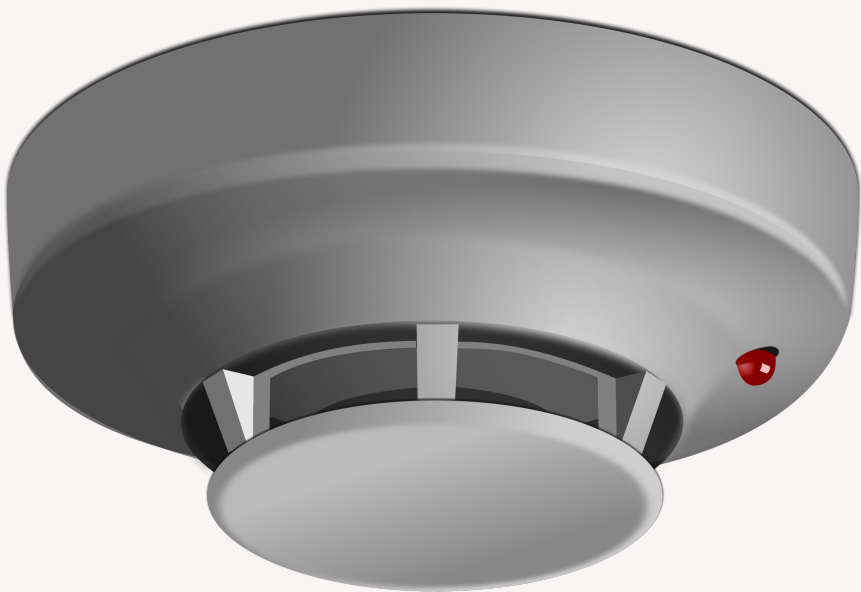
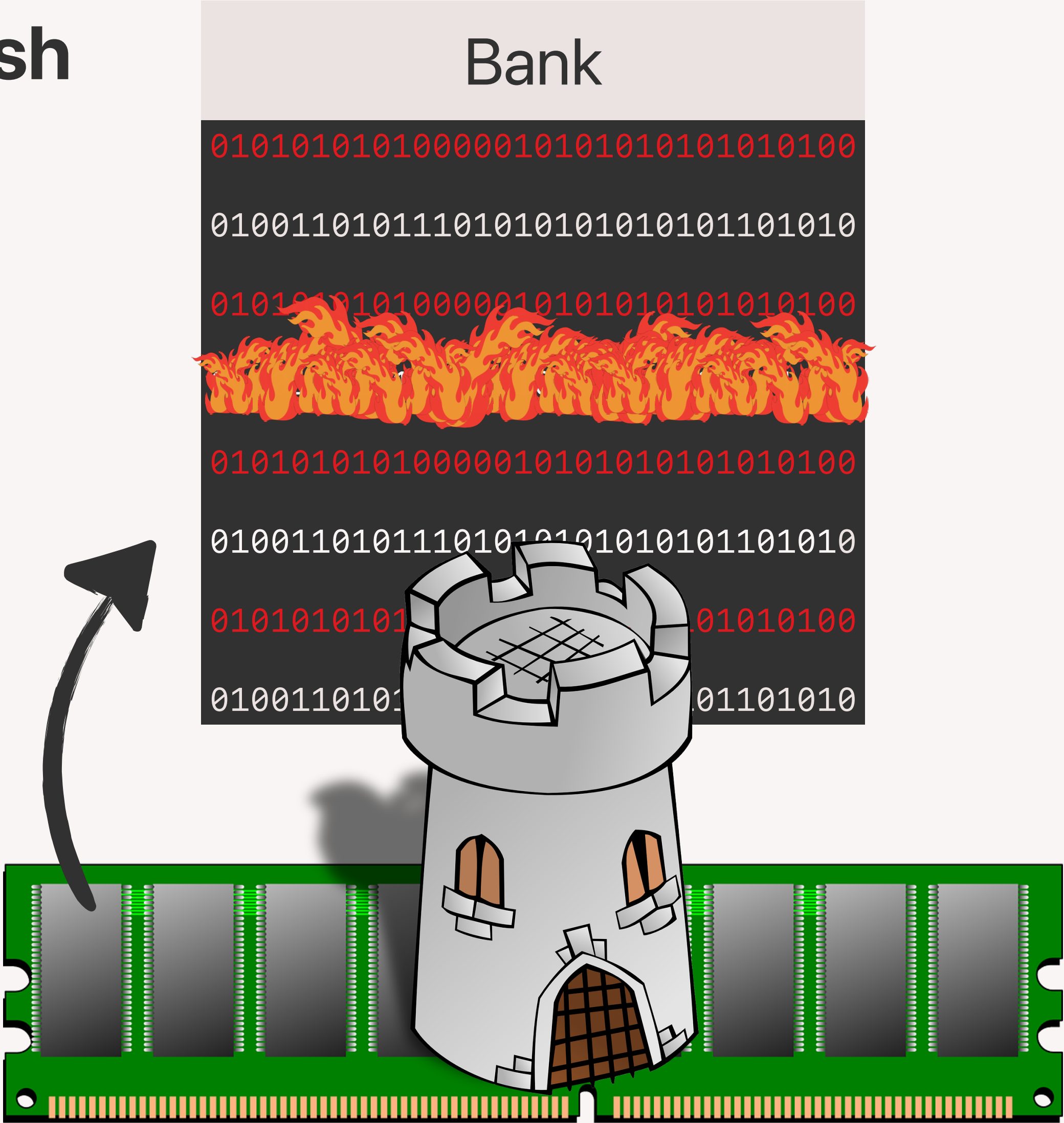
Sampler



Inhibitor

TRR

Target Row Refresh



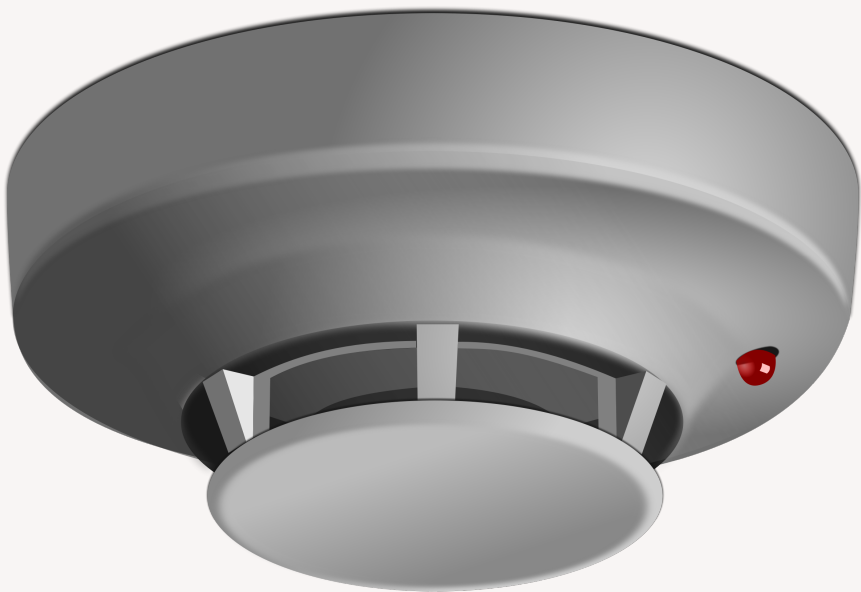
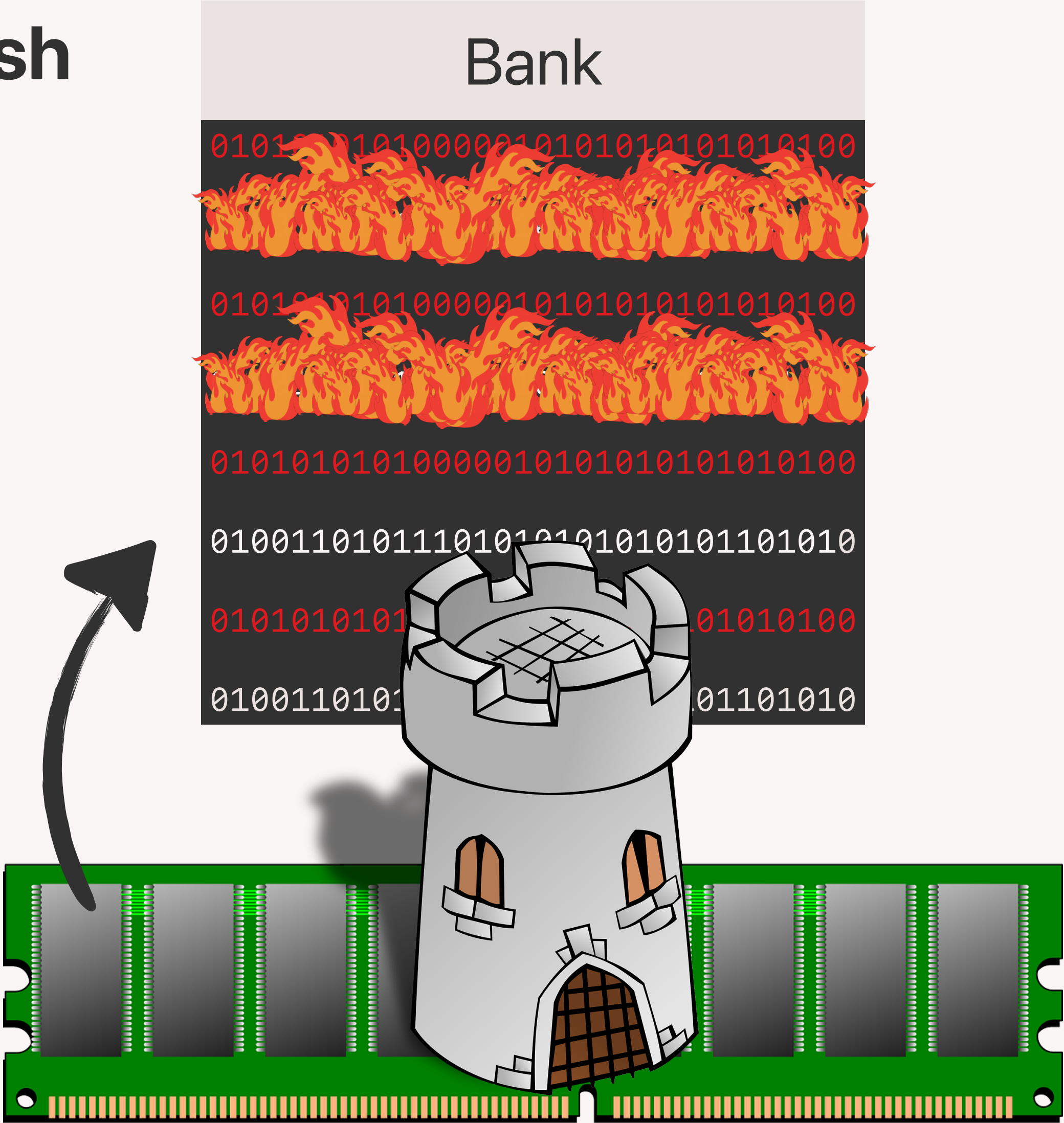
Sampler



Inhibitor

TRR

Target Row Refresh



Sampler



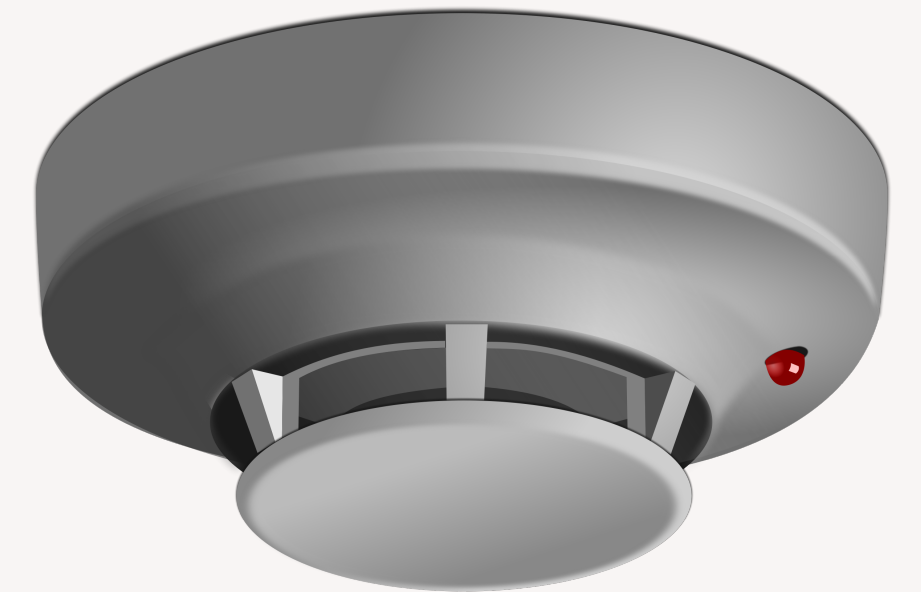
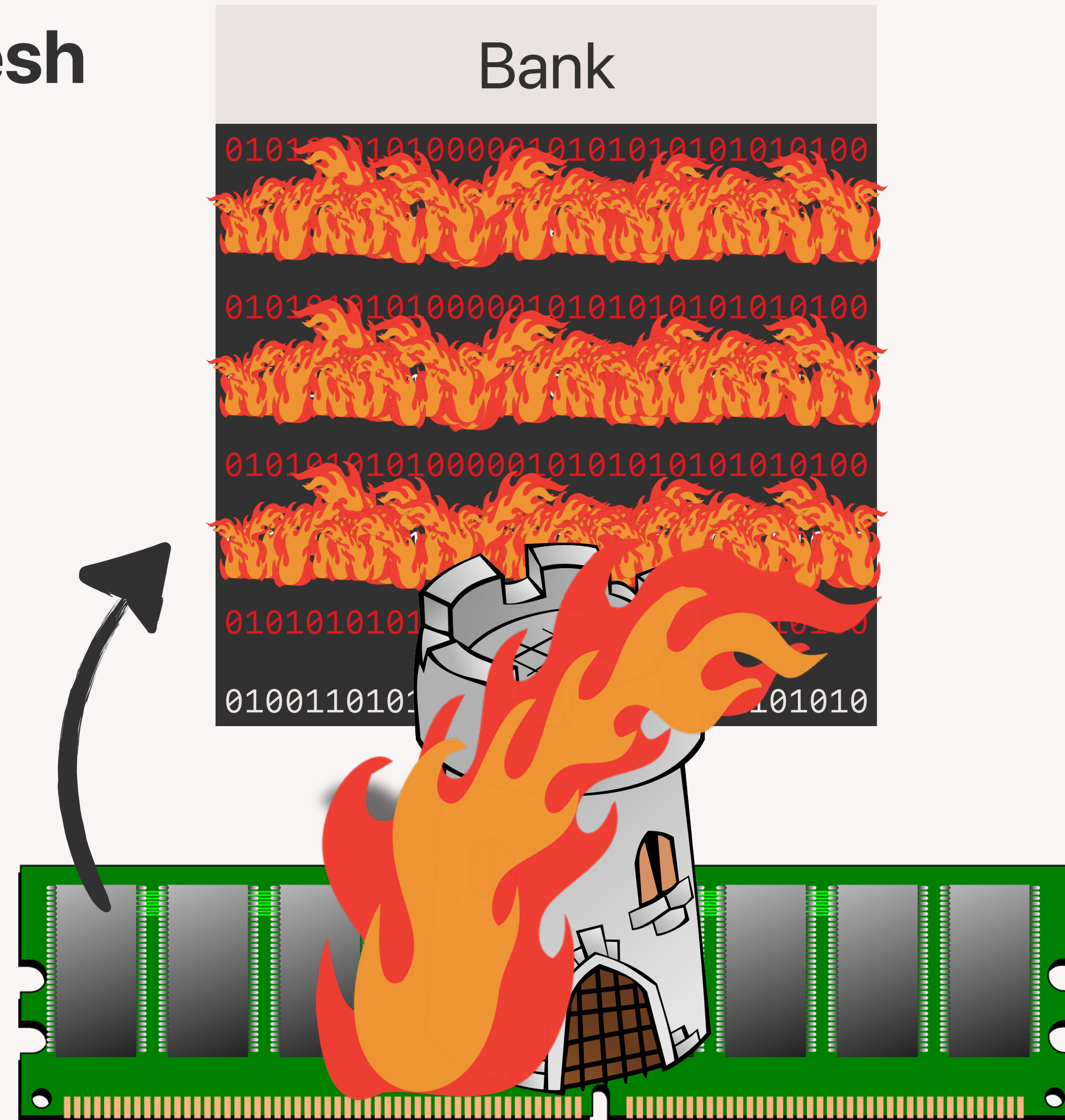
Inhibitor

Target Row Refresh



TRR

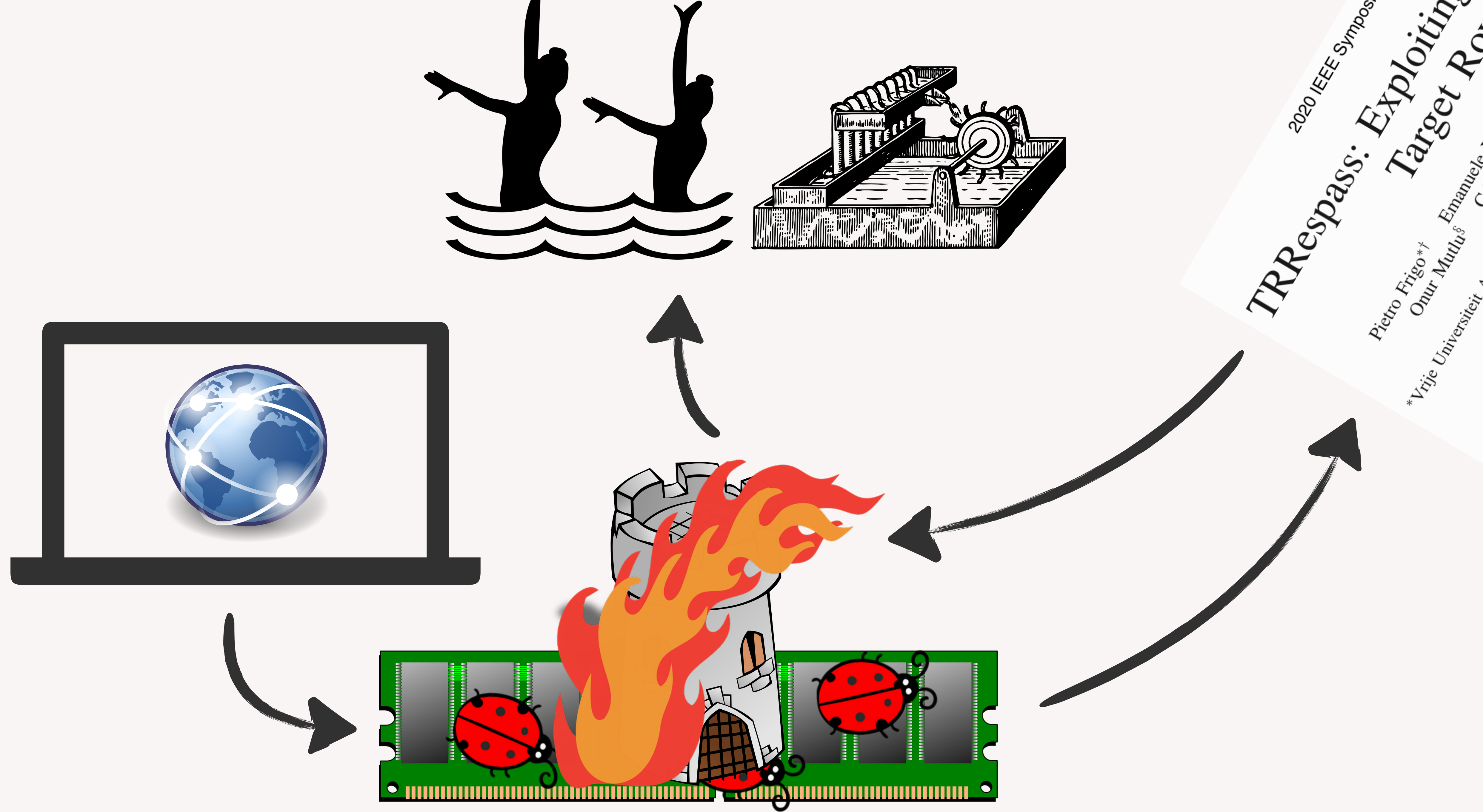
Target Row Refresh



Sampler

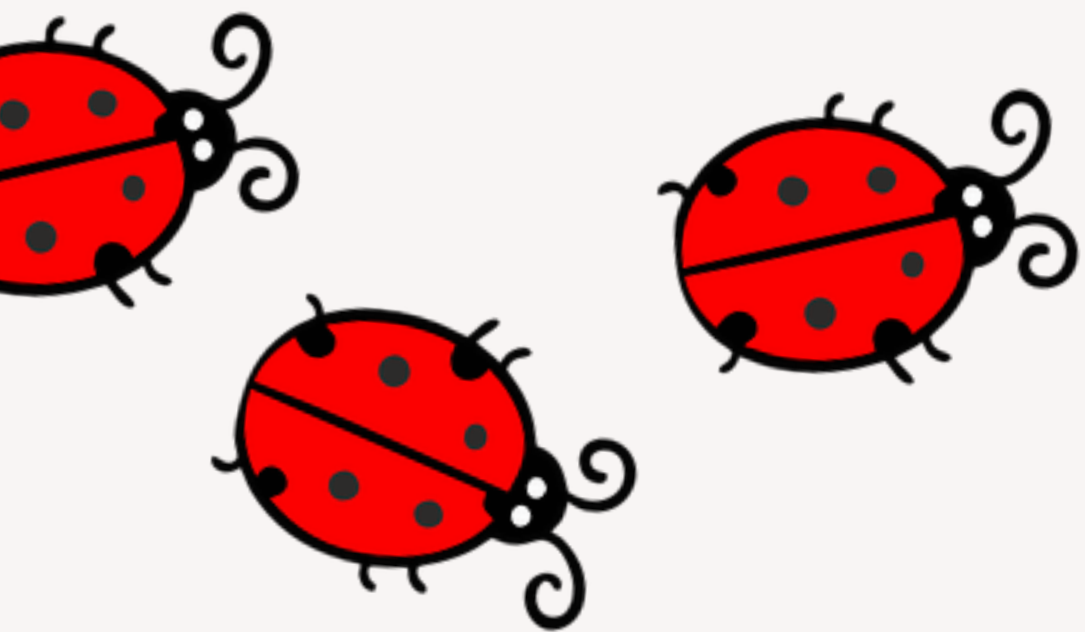


Inhibitor

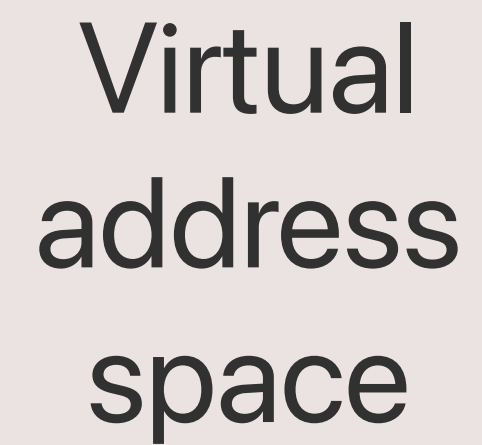


Rowhammer From JavaScript

Part II

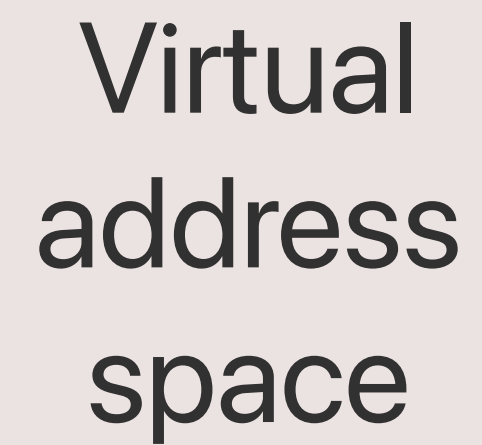


Complicated



01001101011101010101
01010101010000010101
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Complicated



Physical address space

Bank

01001101011101010101

Rowhammer From JavaScript

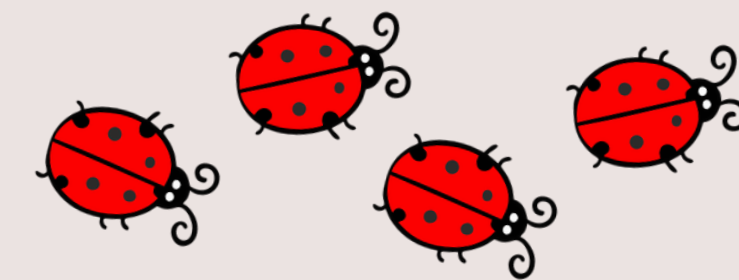
Complicated



Virtual address space

Physical address space

"DRAM"
address
space



Bank

010101010100000101010101

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Complicated

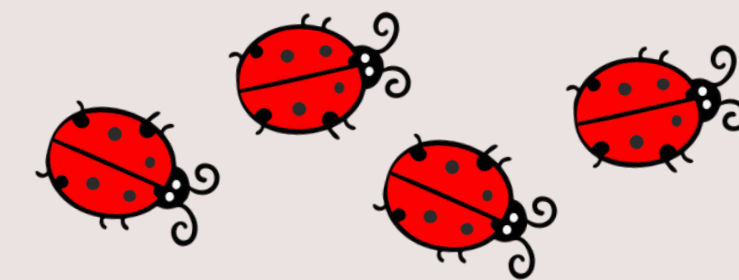


Virtual address space



Physical address space

"DRAM"
address
space



Bank

[illegible]

Complicated



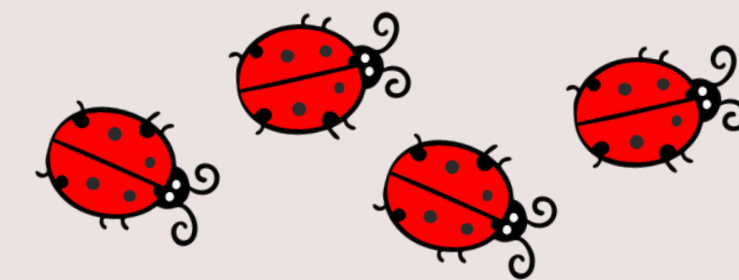
Virtual address space



Physical address space



"DRAM"
address
space



Bank

010101010100000101010101

010011010111010101010101

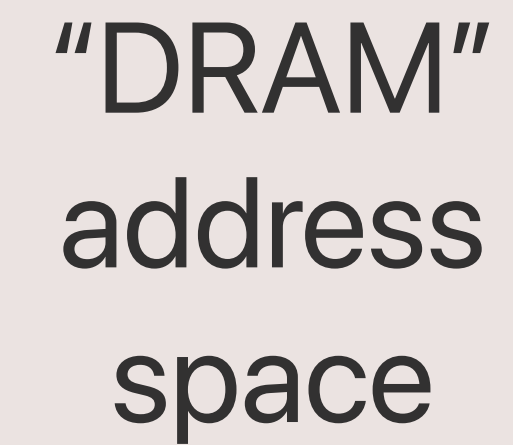
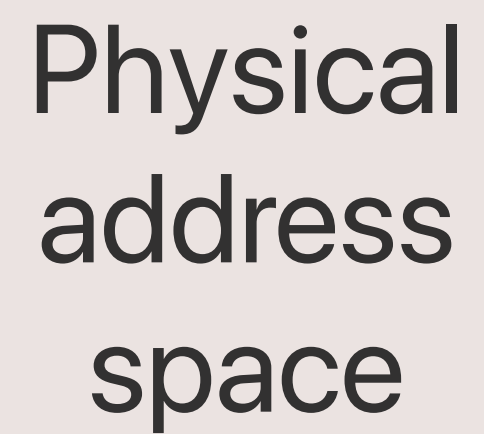
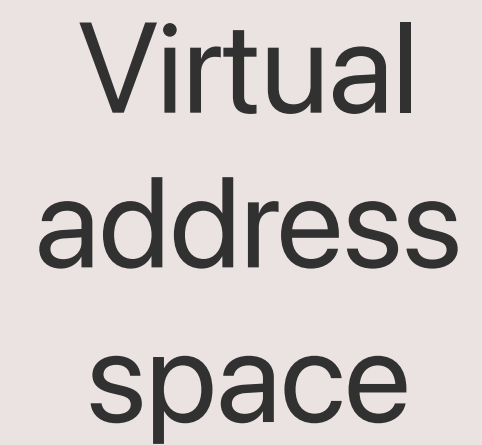
010101010101000001010101

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Complicated



Bank

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01010101010000010101

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Many-sided to Many Double-sided Rowhammer

Removing constraints

Bank
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010011010111010101010101101010
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Many-sided to Many Double-sided Rowhammer

Removing constraints

Bank
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4-sided
Requires a large chunk

Many-sided to Many Double-sided Rowhammer

Removing constraints

Bank
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01010101010000010101010101010100
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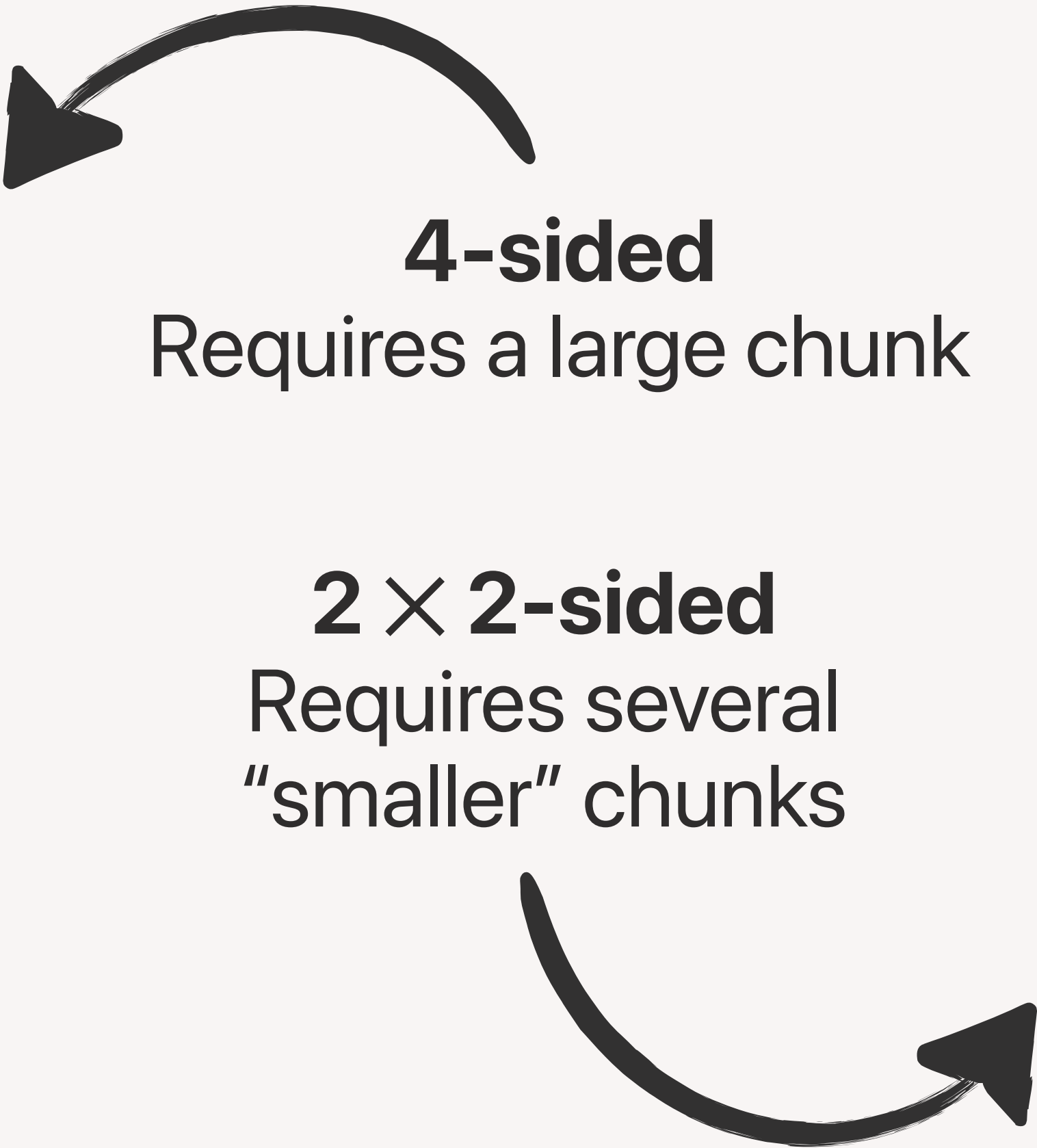
4-sided
Requires a large chunk

Bank
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Many-sided to Many Double-sided Rowhammer

Removing constraints

Bank
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Bank
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010011010111010101010101101010
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010011010111010101010101101010
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Many-sided to Many Double-sided Rowhammer

Removing constraints

Bank
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01010101010000010101010101010100
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010011010111010101010101101010
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010011010111010101010101101010

2 × 2-sided
Requires several
"smaller" chunks



Bank
01010101010000010101010101010100
010011010111010101010101101010
01010101010000010101010101010100
010011010111010101010101101010
01010101010000010101010101010100
010011010111010101010101101010
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Many-sided to Many Double-sided Rowhammer

Removing constraints

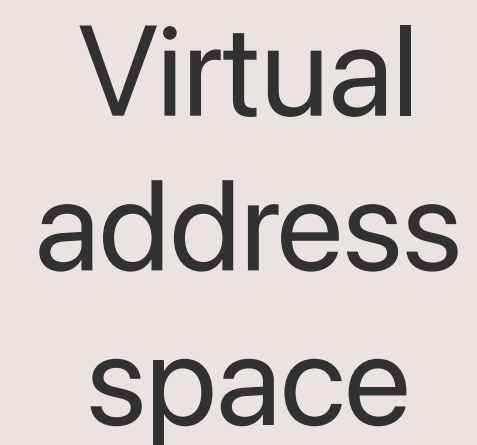
Bank
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010011010111010101010101101010
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010011010111010101010101101010
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Fits inside a huge page!

2 × 2-sided
Requires several
"smaller" chunks

Bank
01010101010000010101010101010100
010011010111010101010101101010
01010101010000010101010101010100
010011010111010101010101101010
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010011010111010101010101101010
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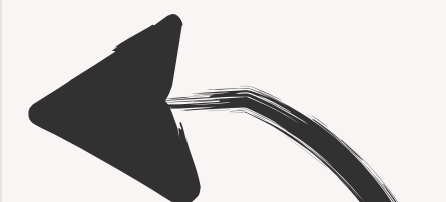
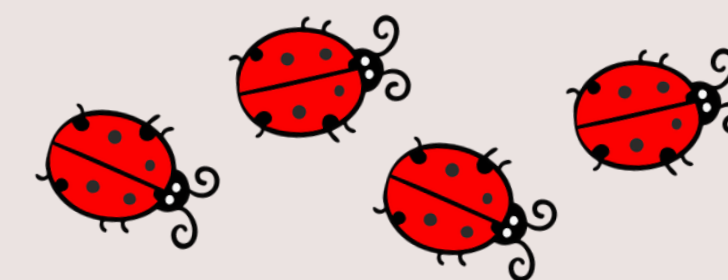
Complicated



Physical address space



"DRAM" address space



Caches

Bank

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01010101010000010101

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01001101011101010101

Rowhammer From JavaScript

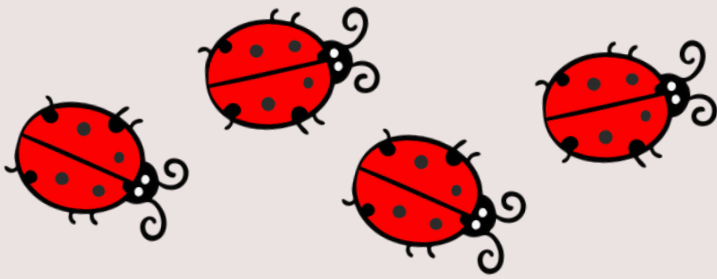
Complicated



Virtual
address
space

Physical
address
space

"DRAM"
address
space



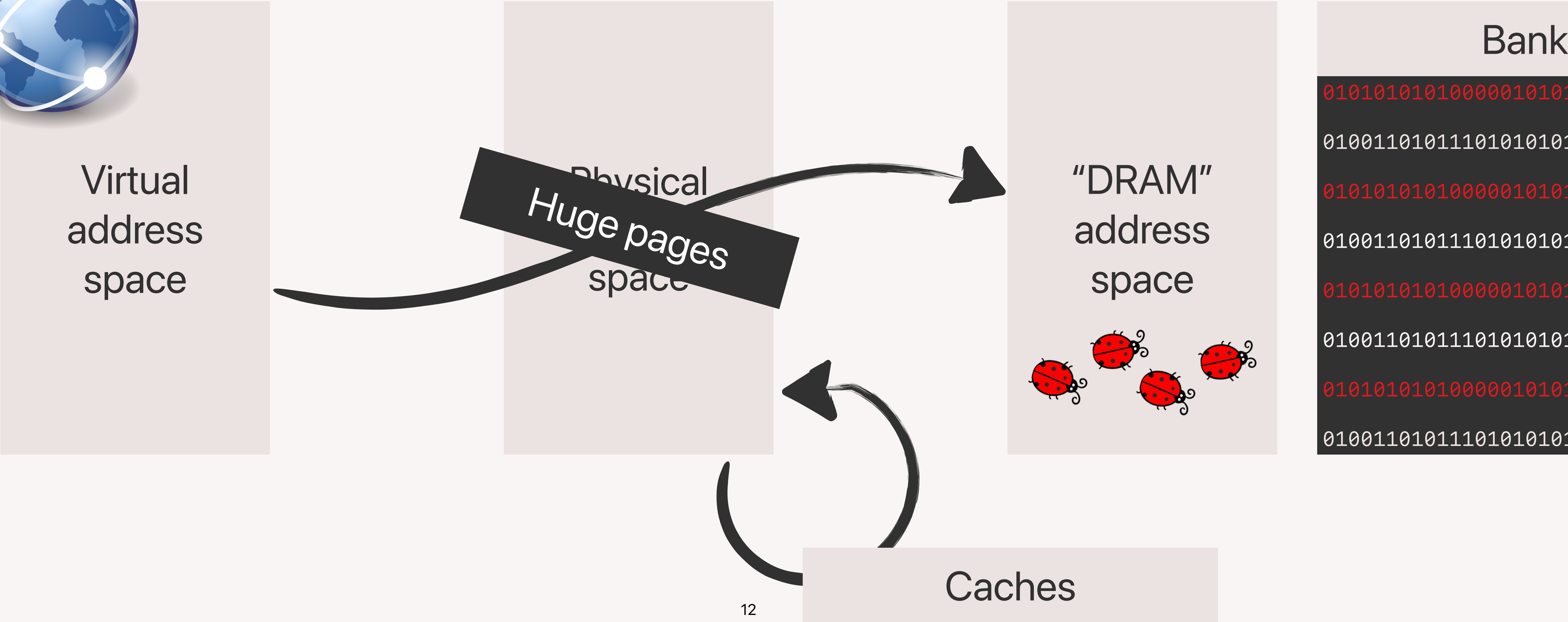
Bank

0101010101000001010101
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Caches

Rowhammer From JavaScript

Complicated



Cache Eviction

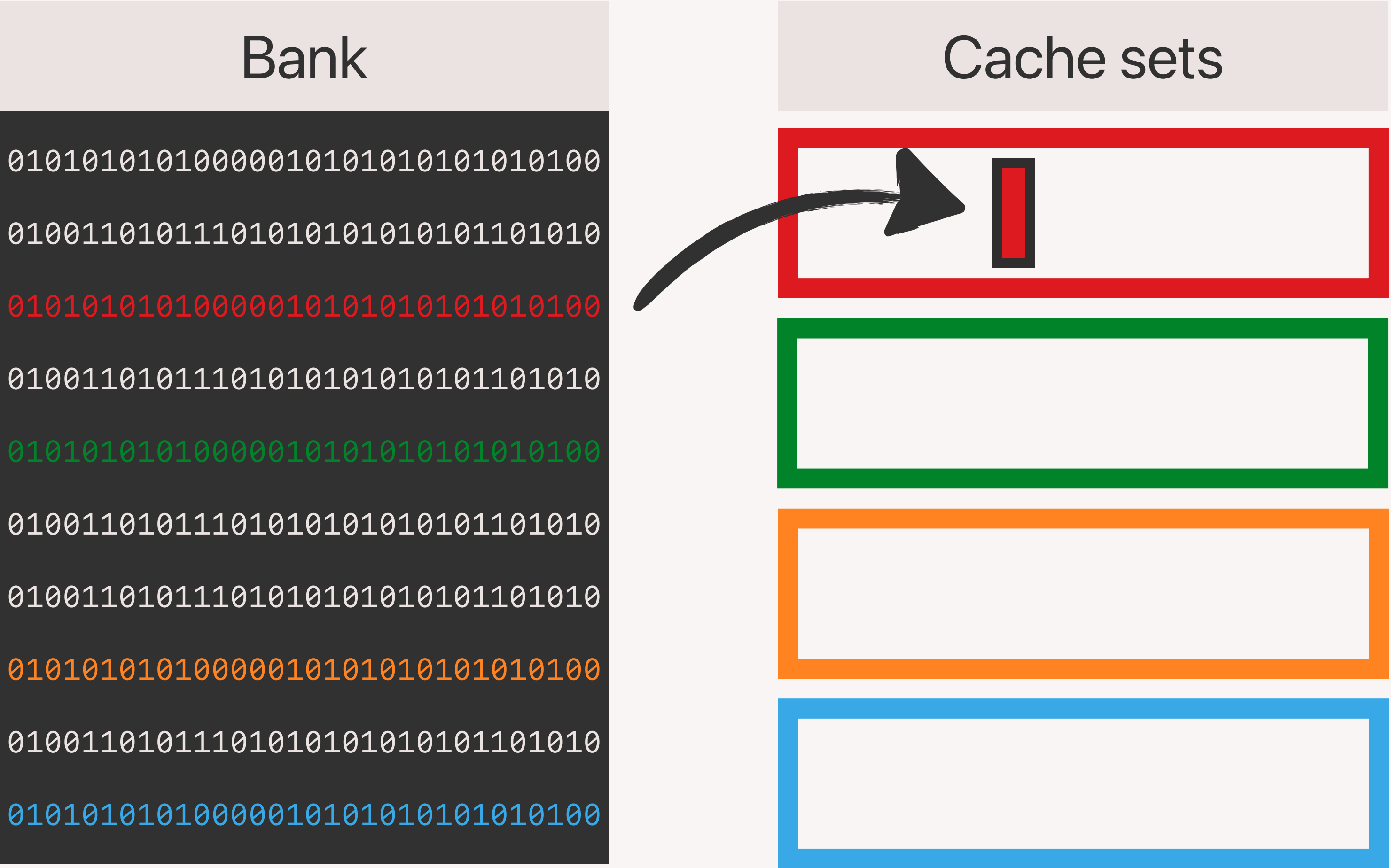
Eviction sets

Bank
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Cache sets

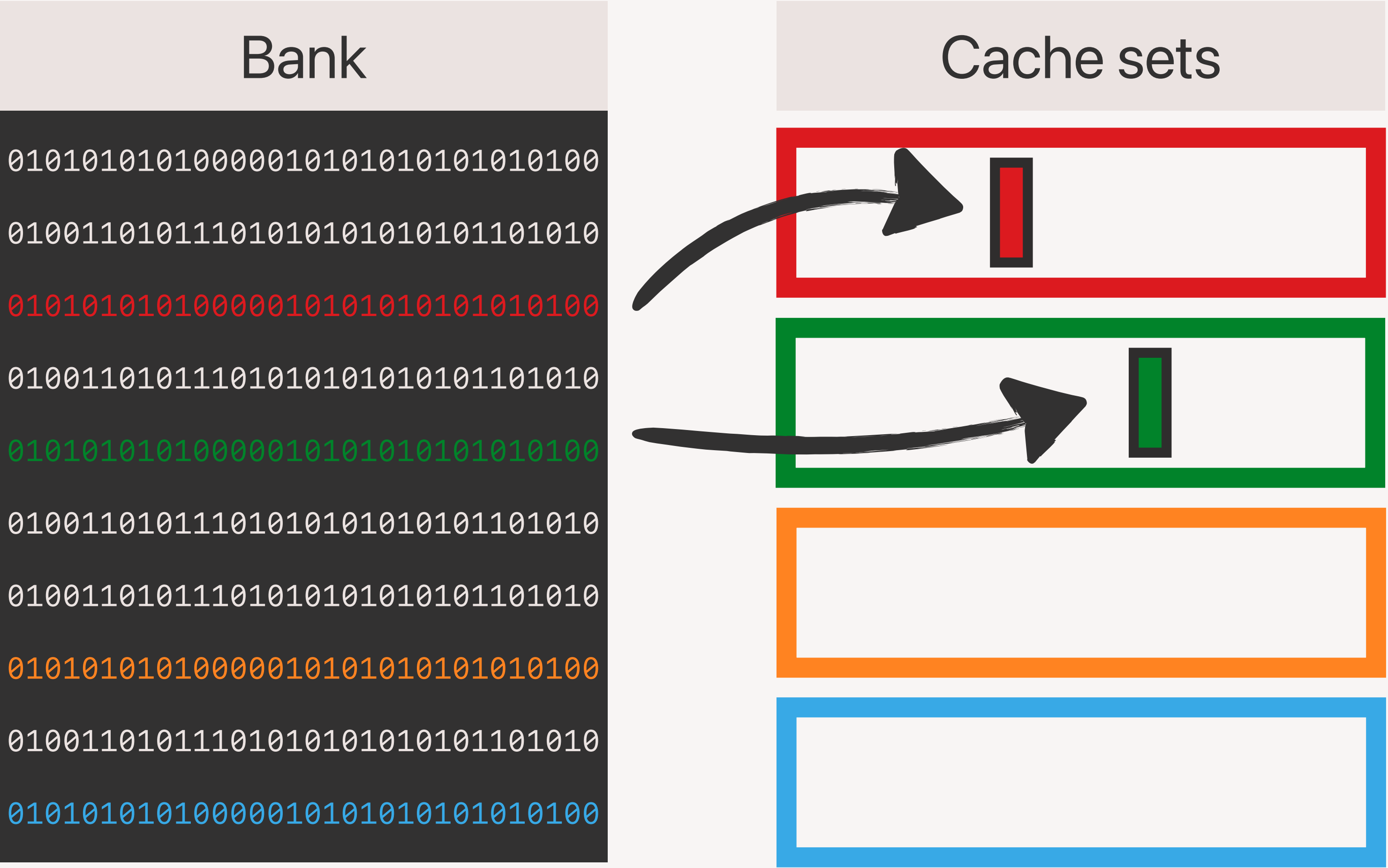
Cache Eviction

Eviction sets



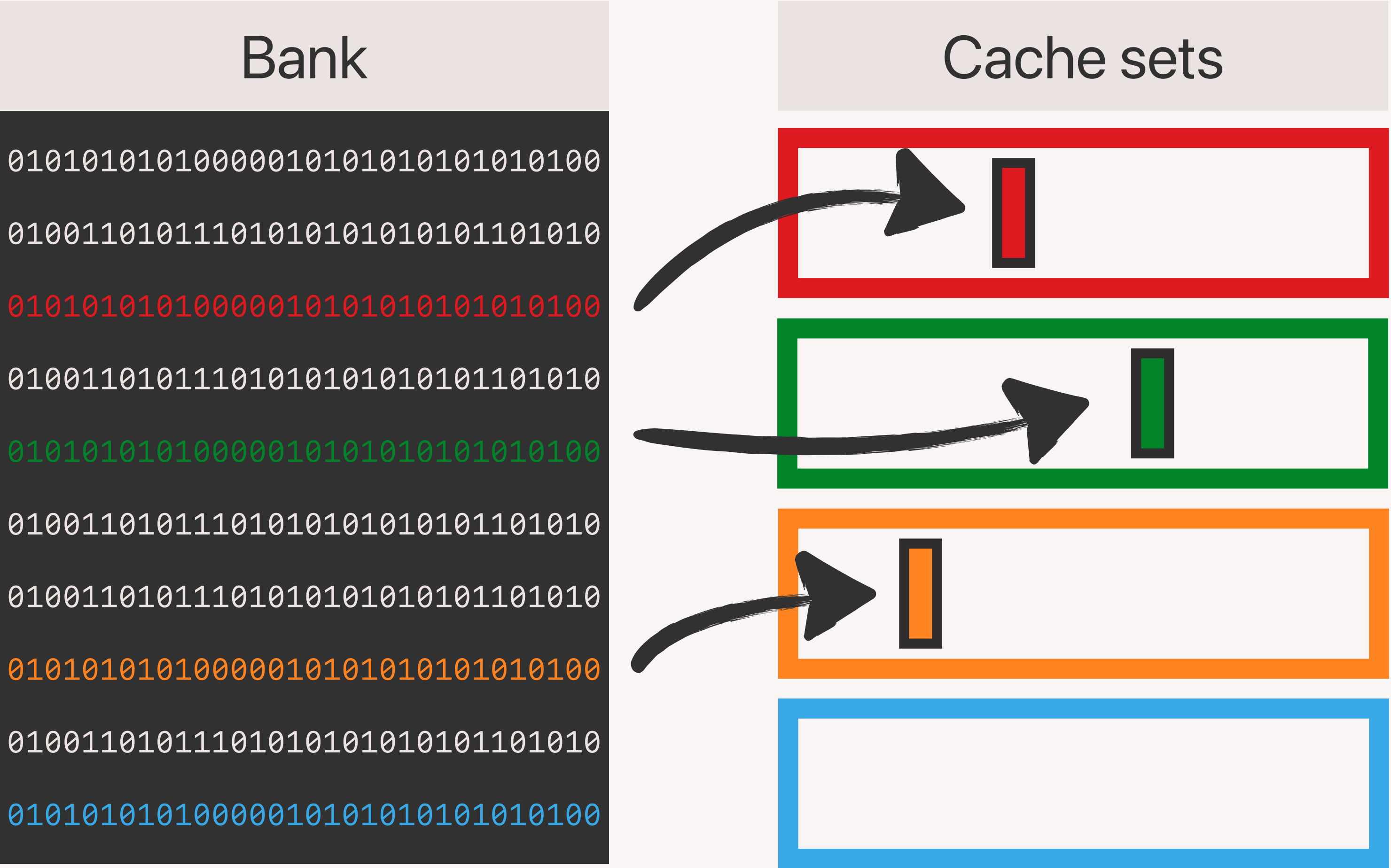
Cache Eviction

Eviction sets



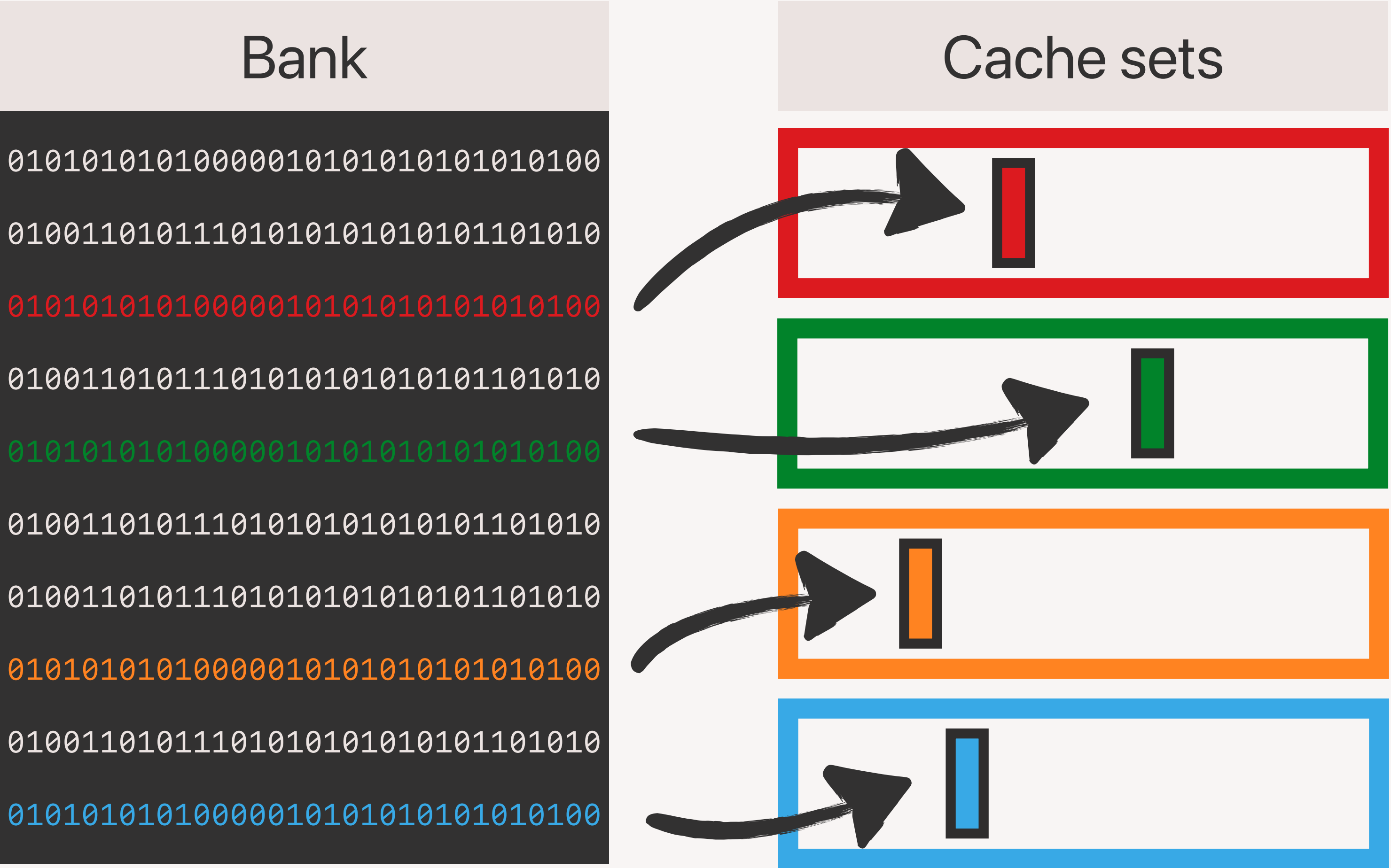
Cache Eviction

Eviction sets



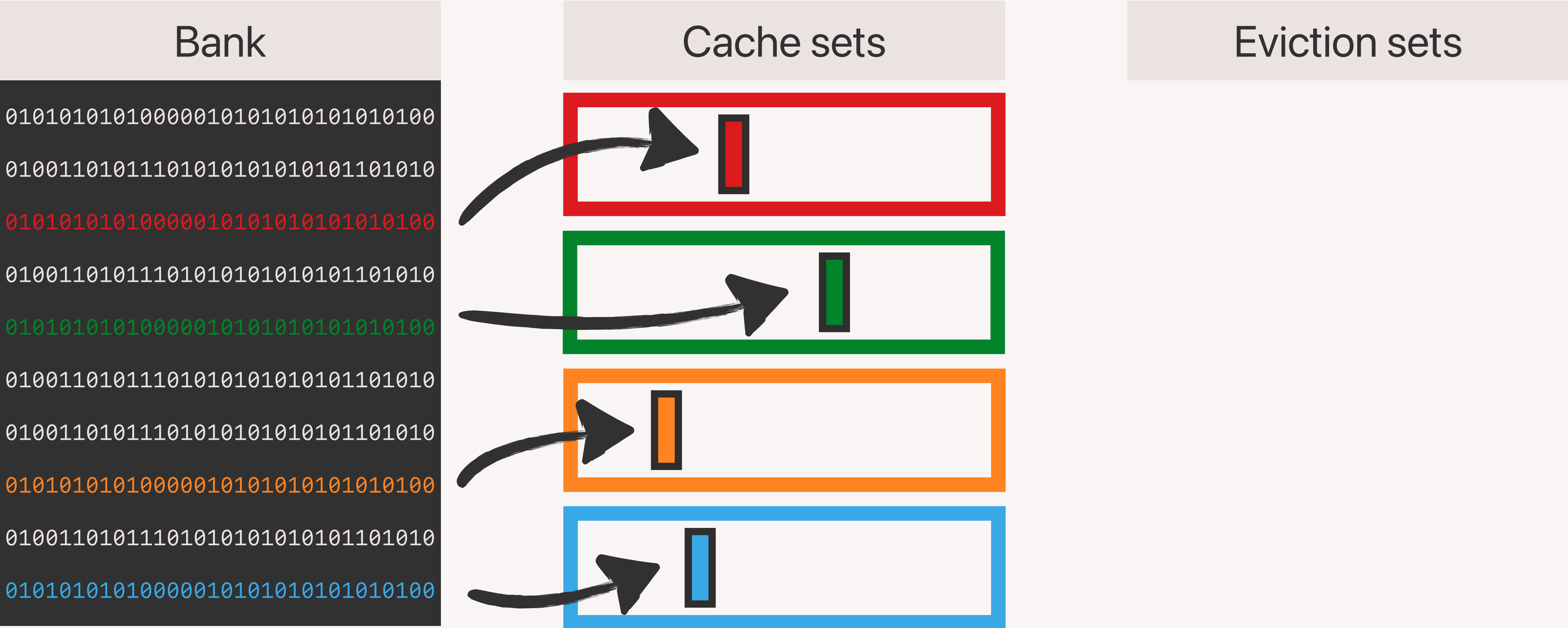
Cache Eviction

Eviction sets



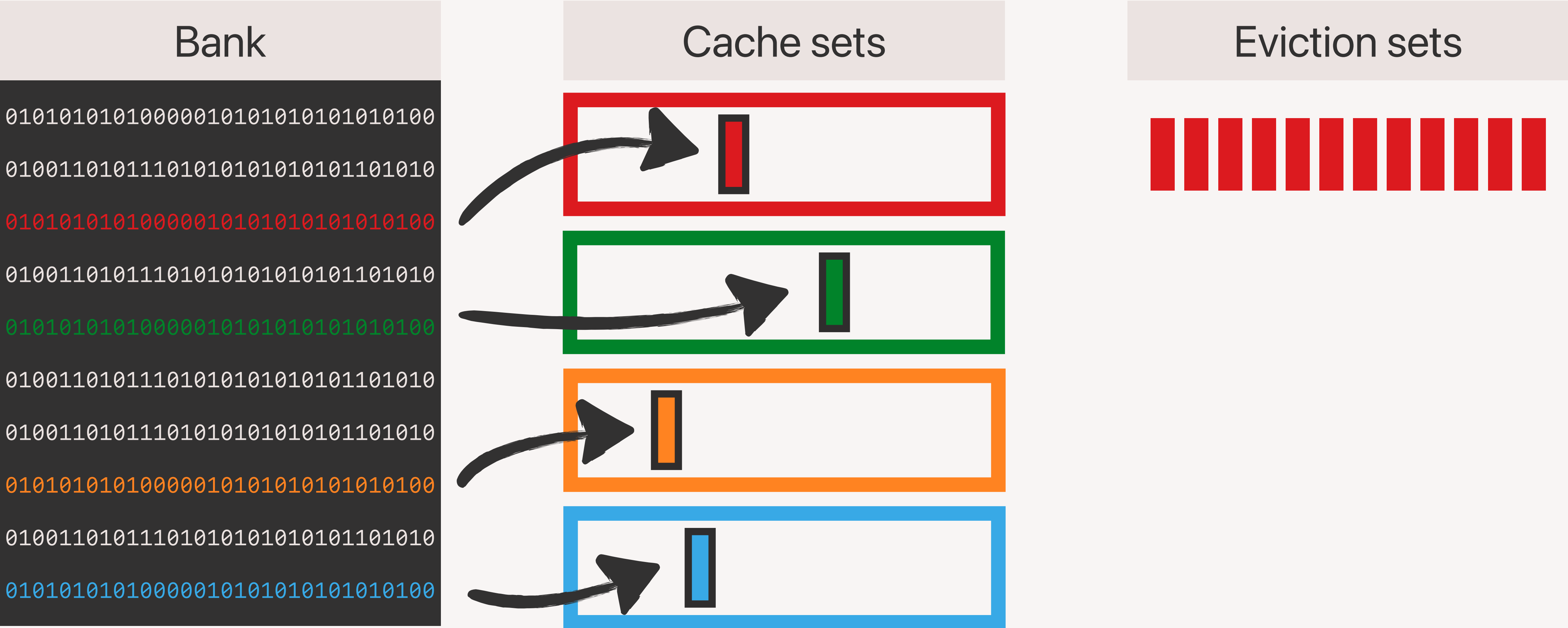
Cache Eviction

Eviction sets



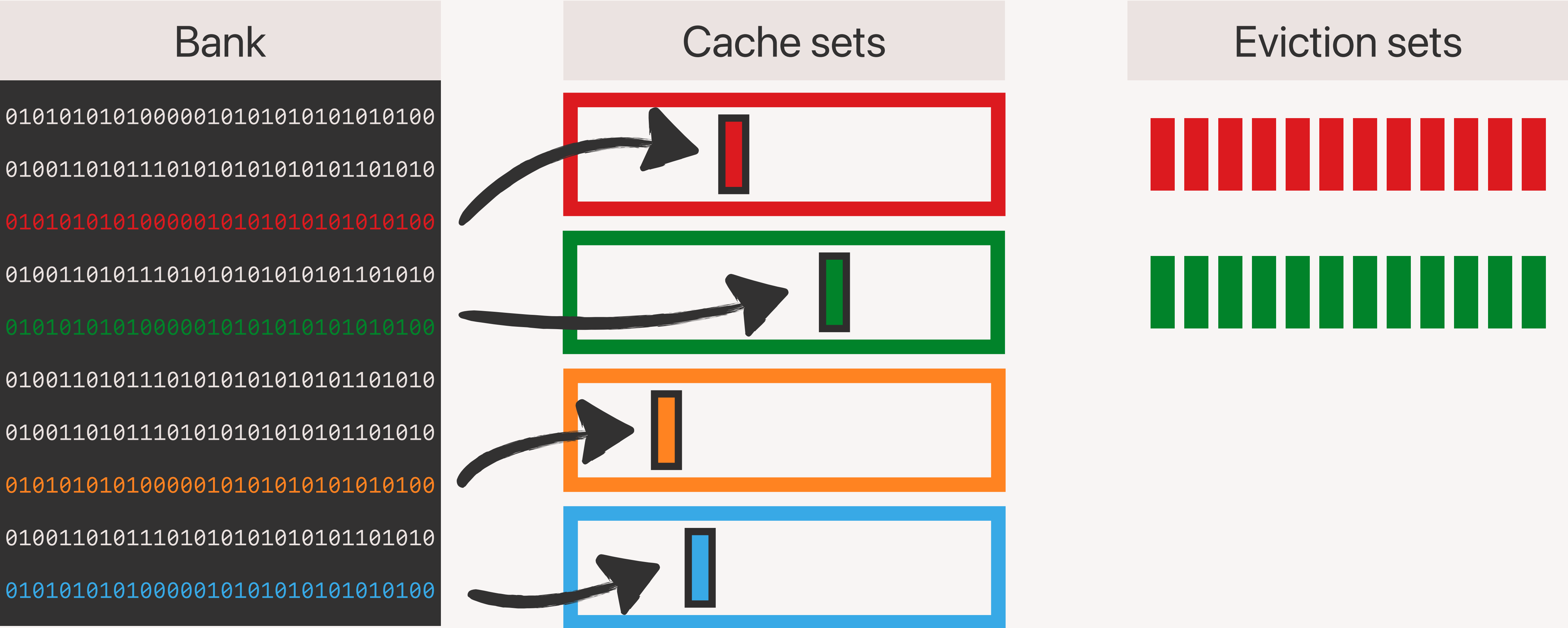
Cache Eviction

Eviction sets



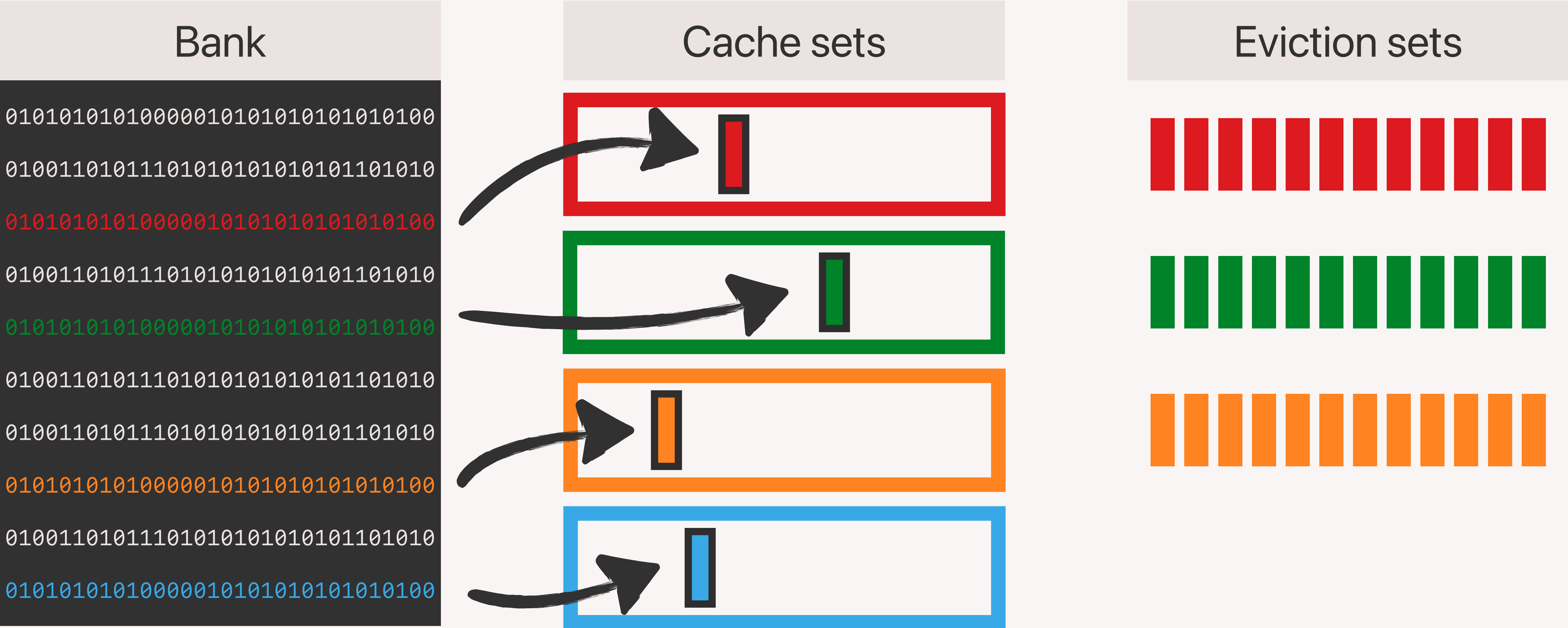
Cache Eviction

Eviction sets



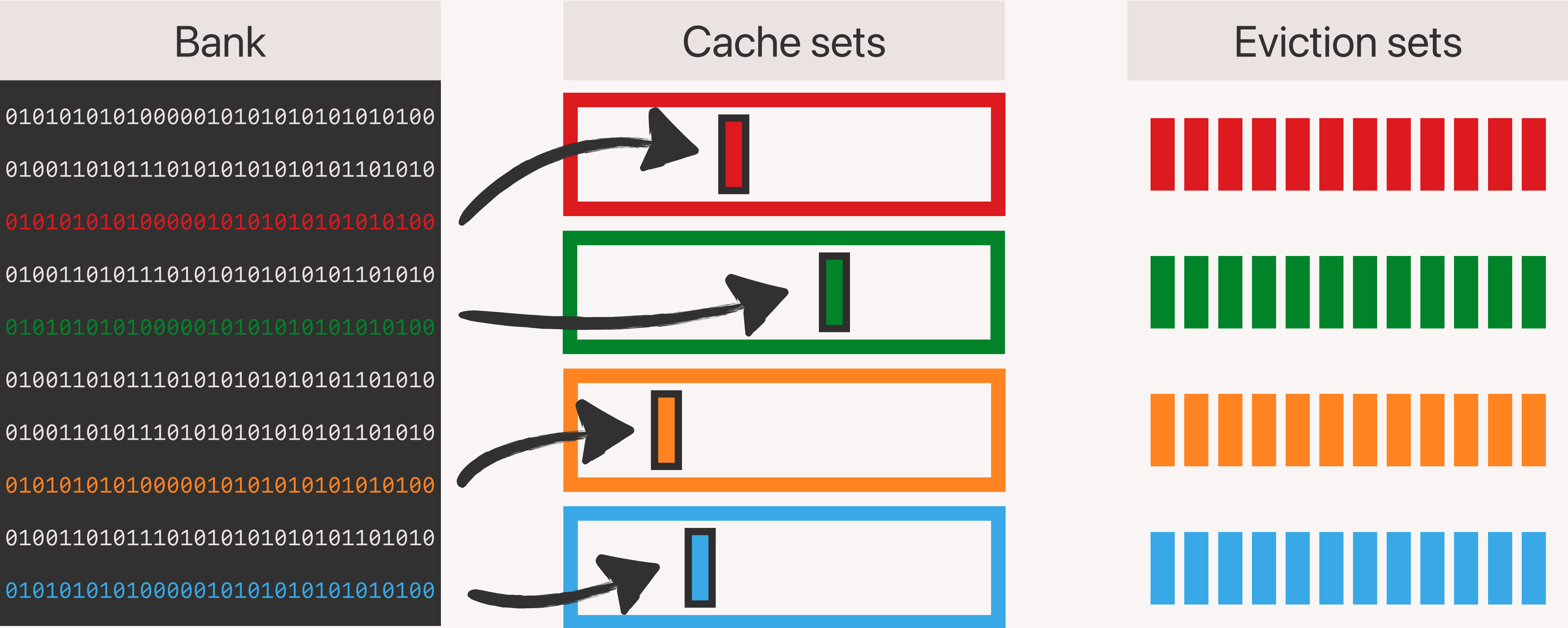
Cache Eviction

Eviction sets



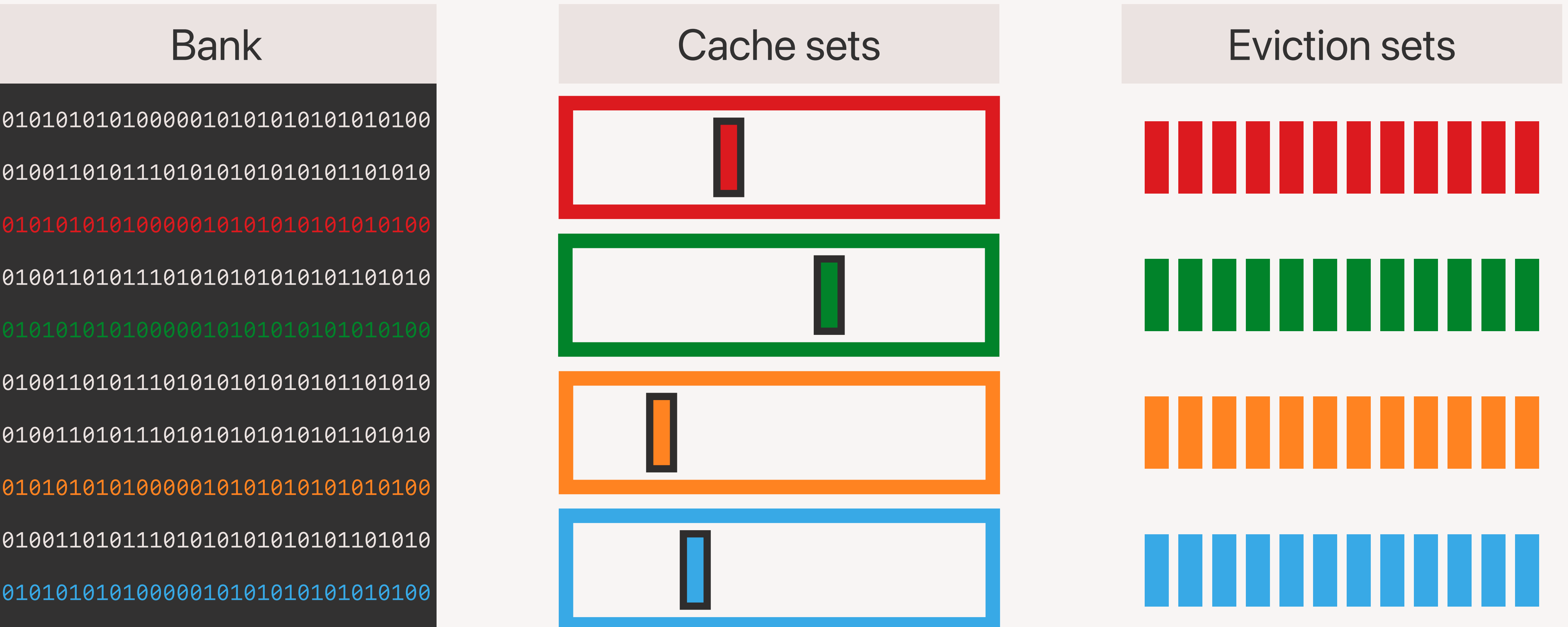
Cache Eviction

Eviction sets



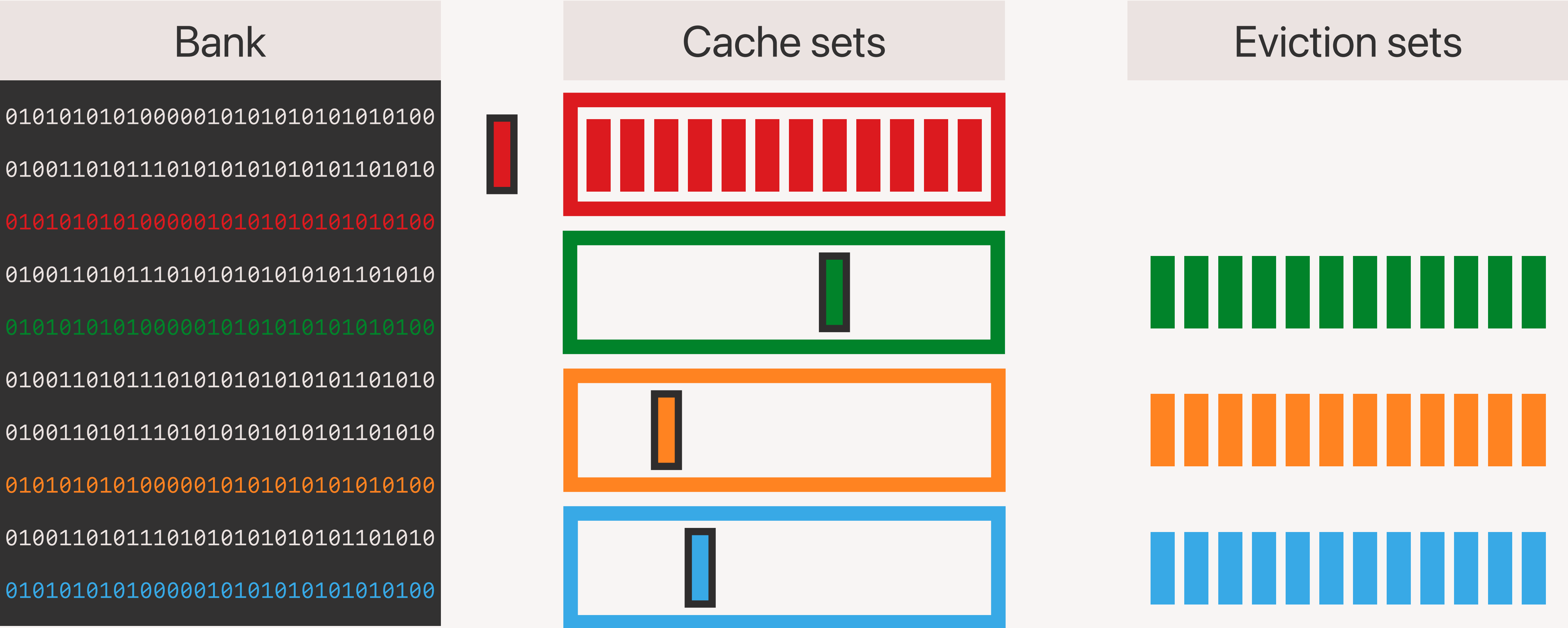
Cache Eviction

Eviction sets



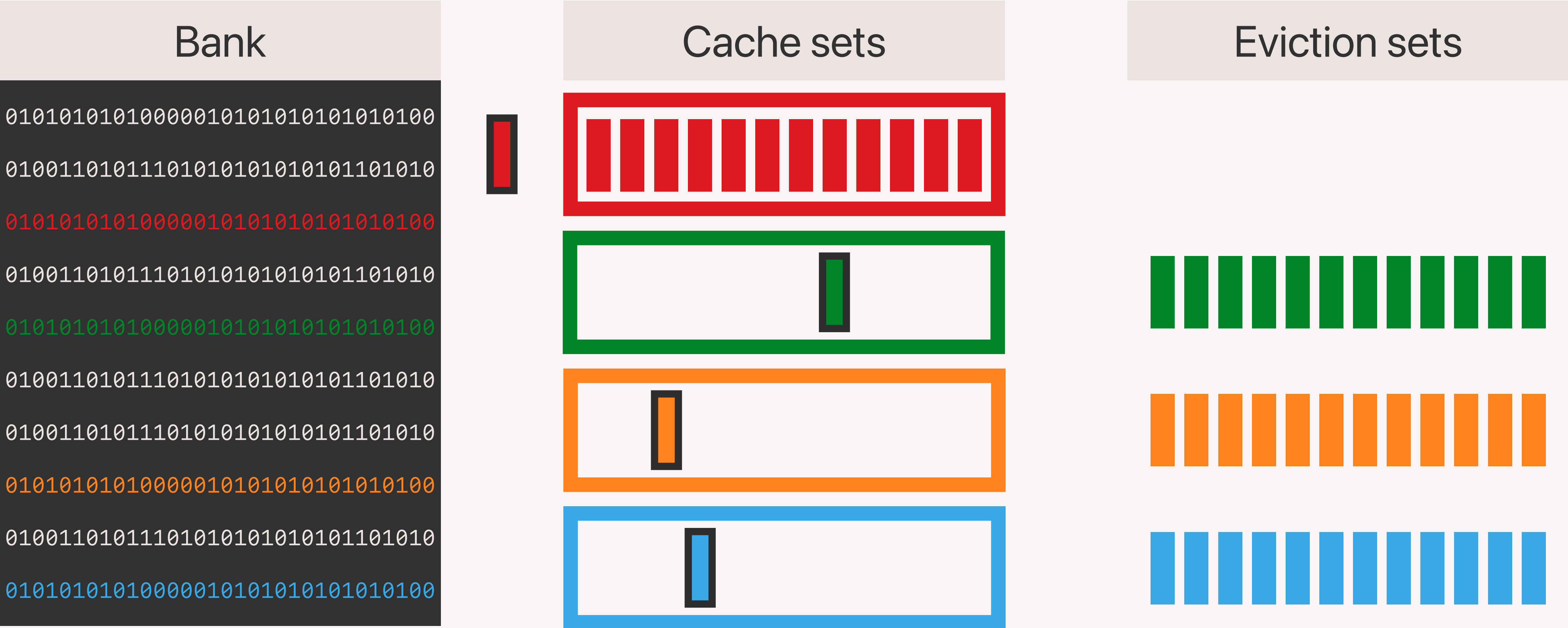
Cache Eviction

Eviction sets



Cache Eviction

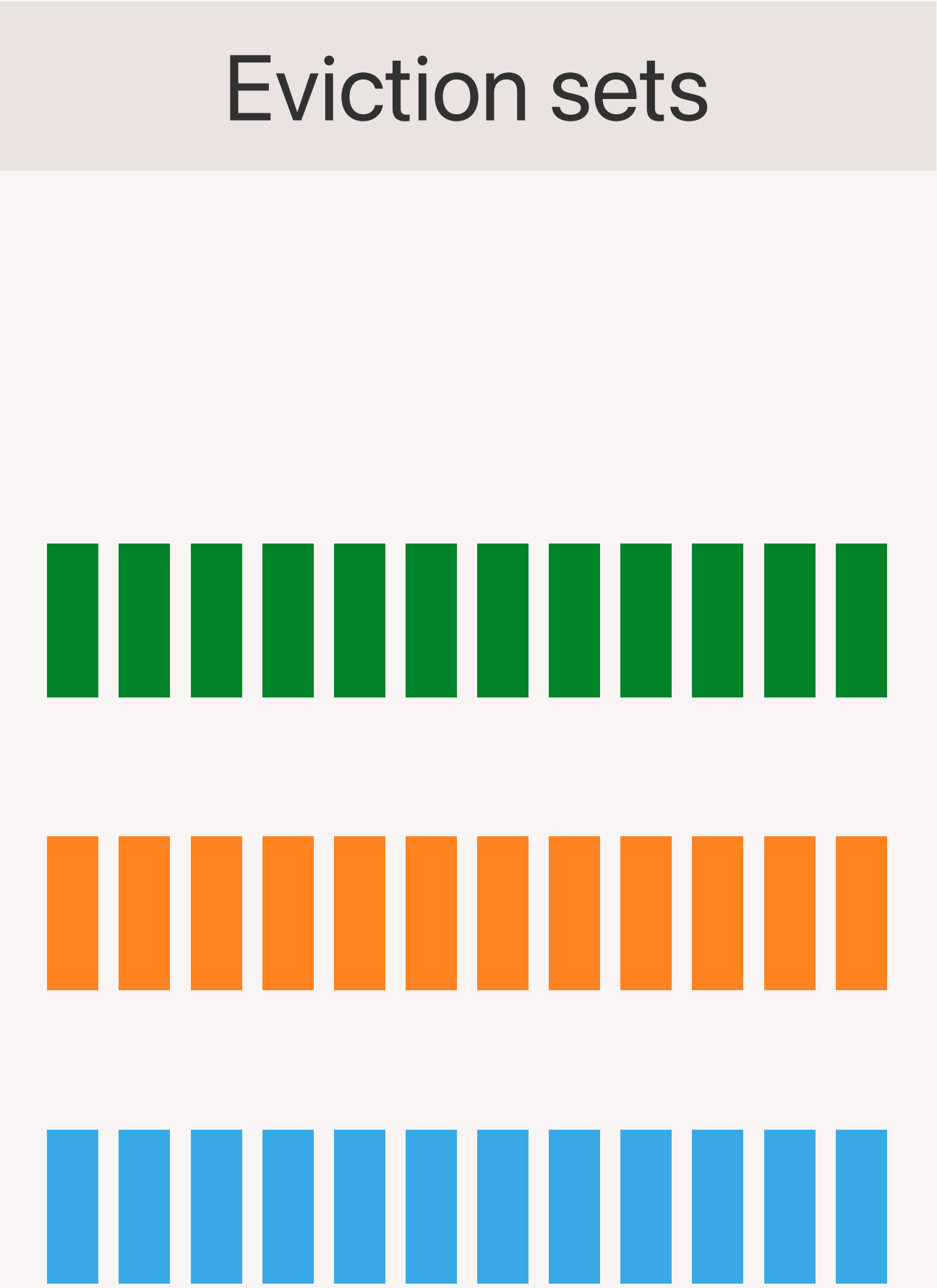
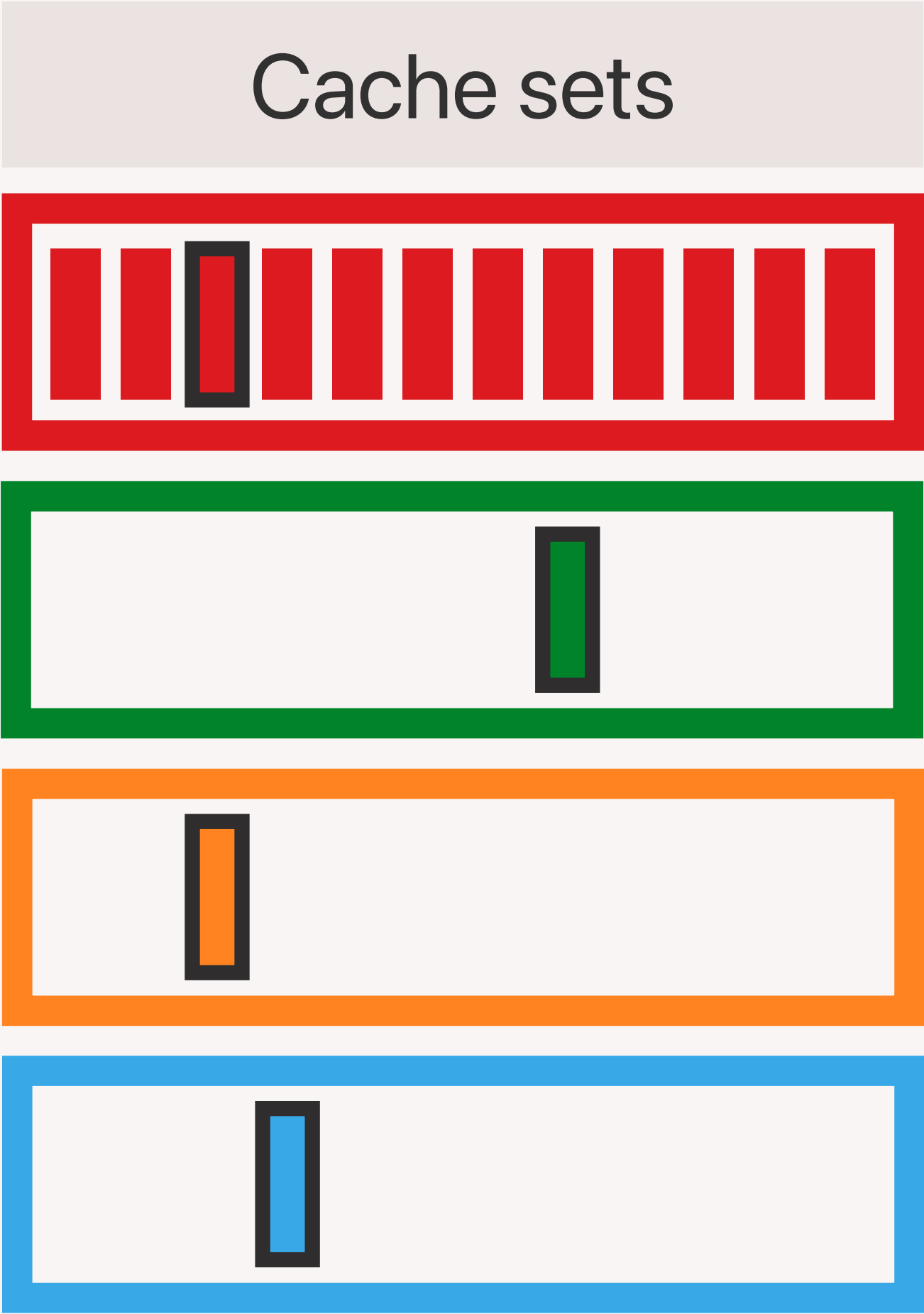
Eviction sets



Cache Eviction

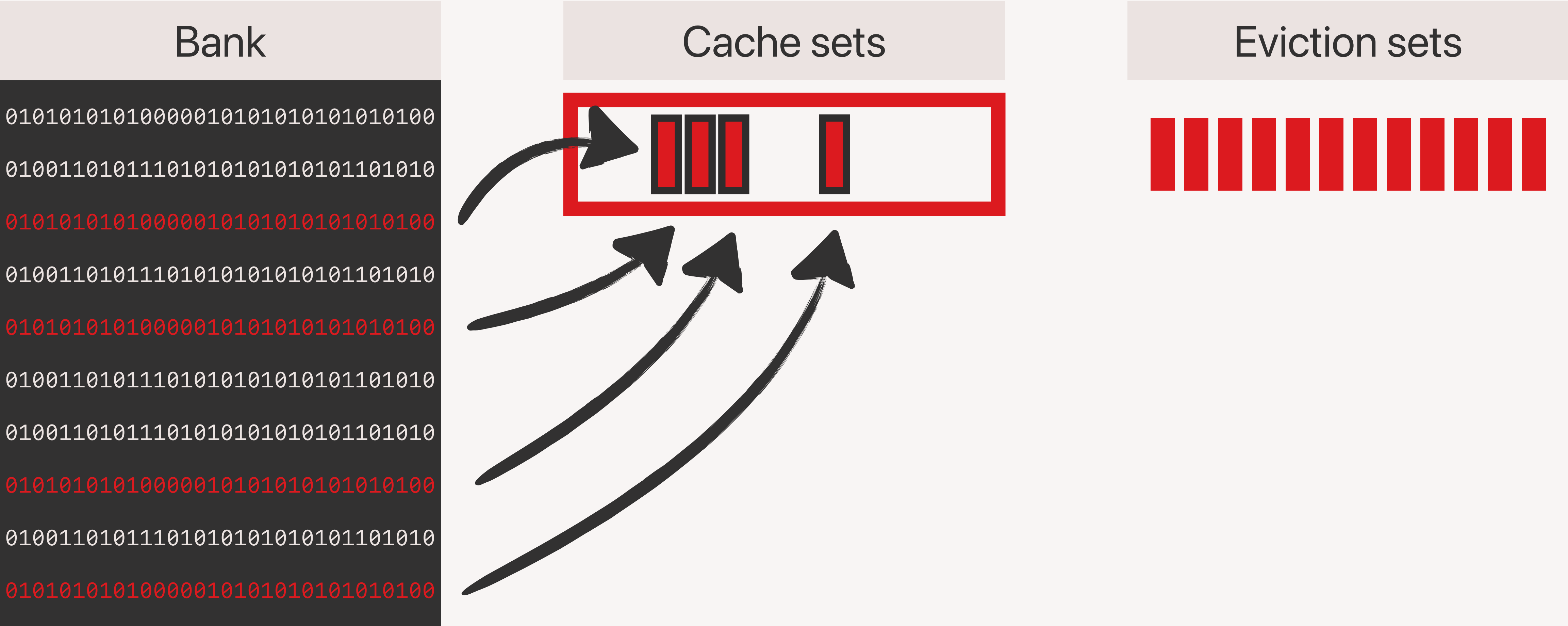
Eviction sets

Bank
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100



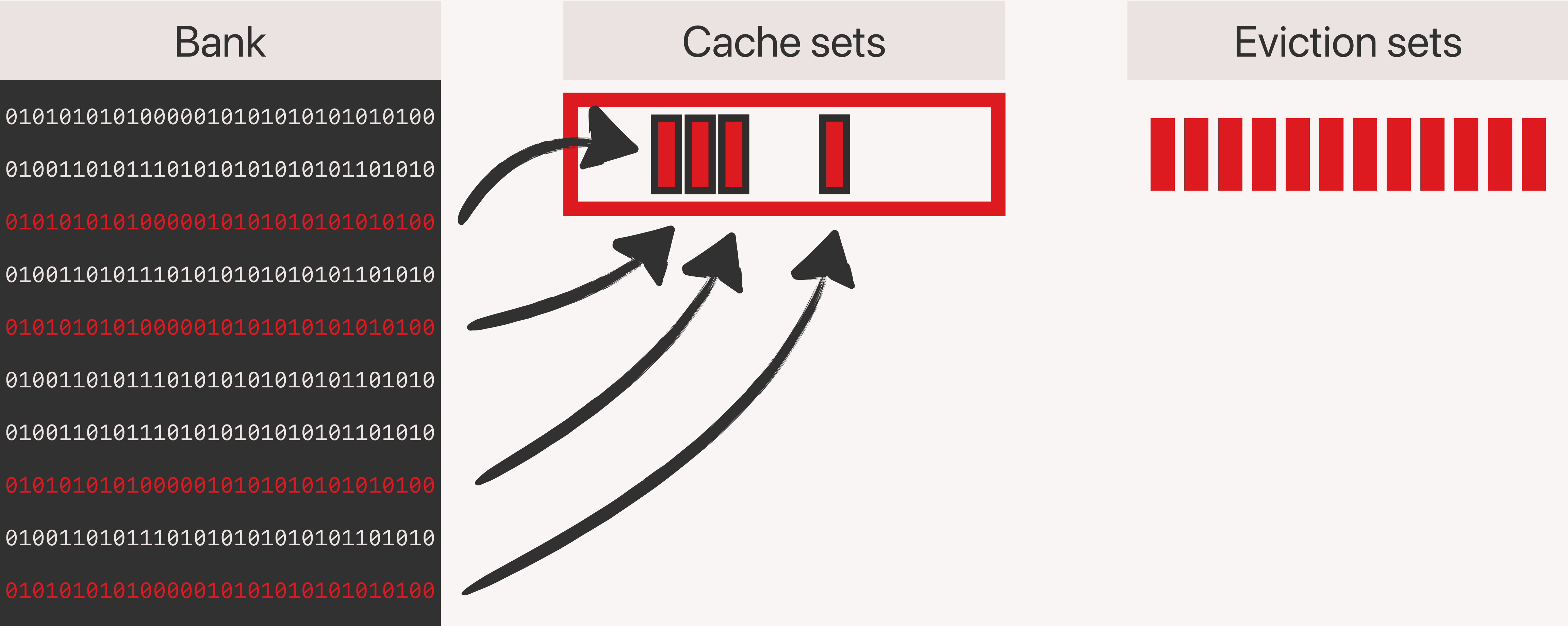
Cache Eviction

Same set?



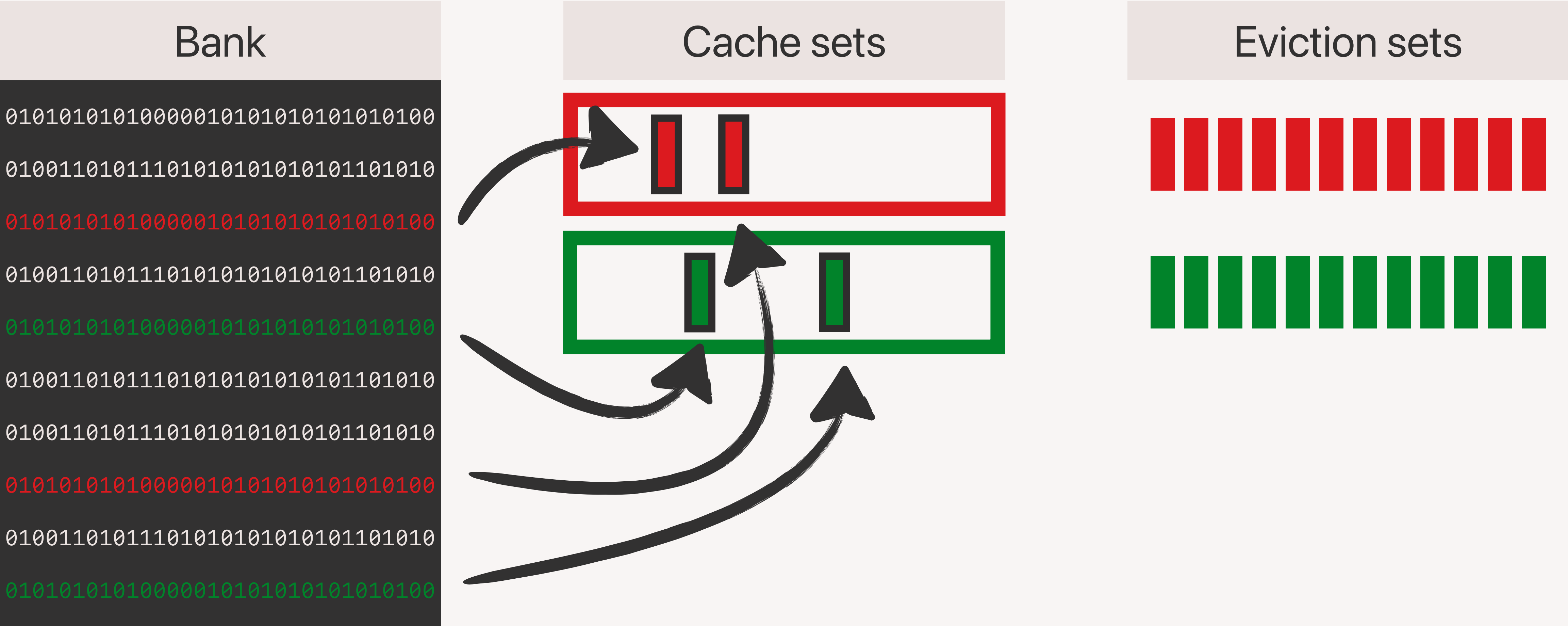
Cache Eviction

Same set? Not possible!



Cache Eviction

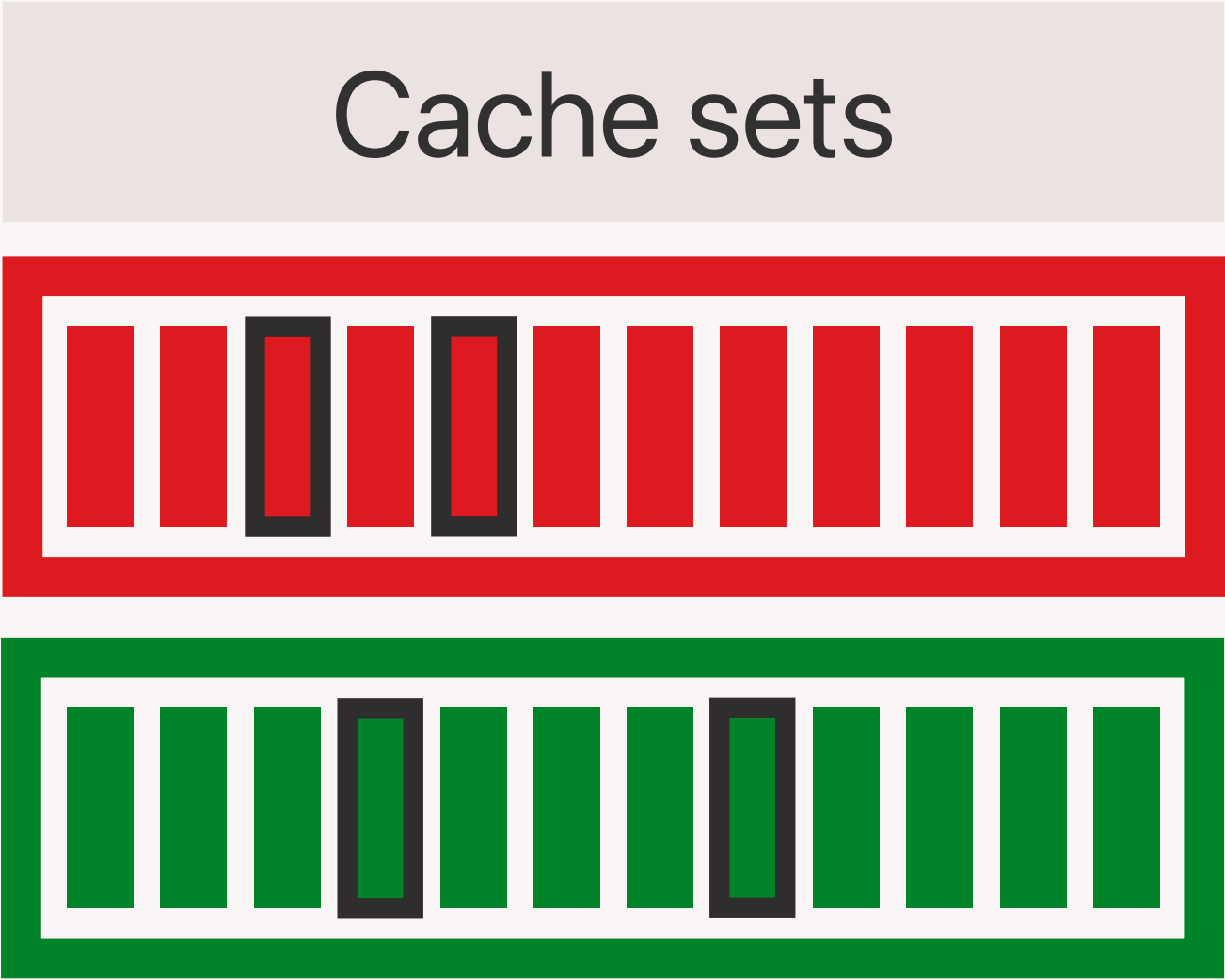
Two sets



Cache Eviction

Two sets + cache hits

Bank
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100

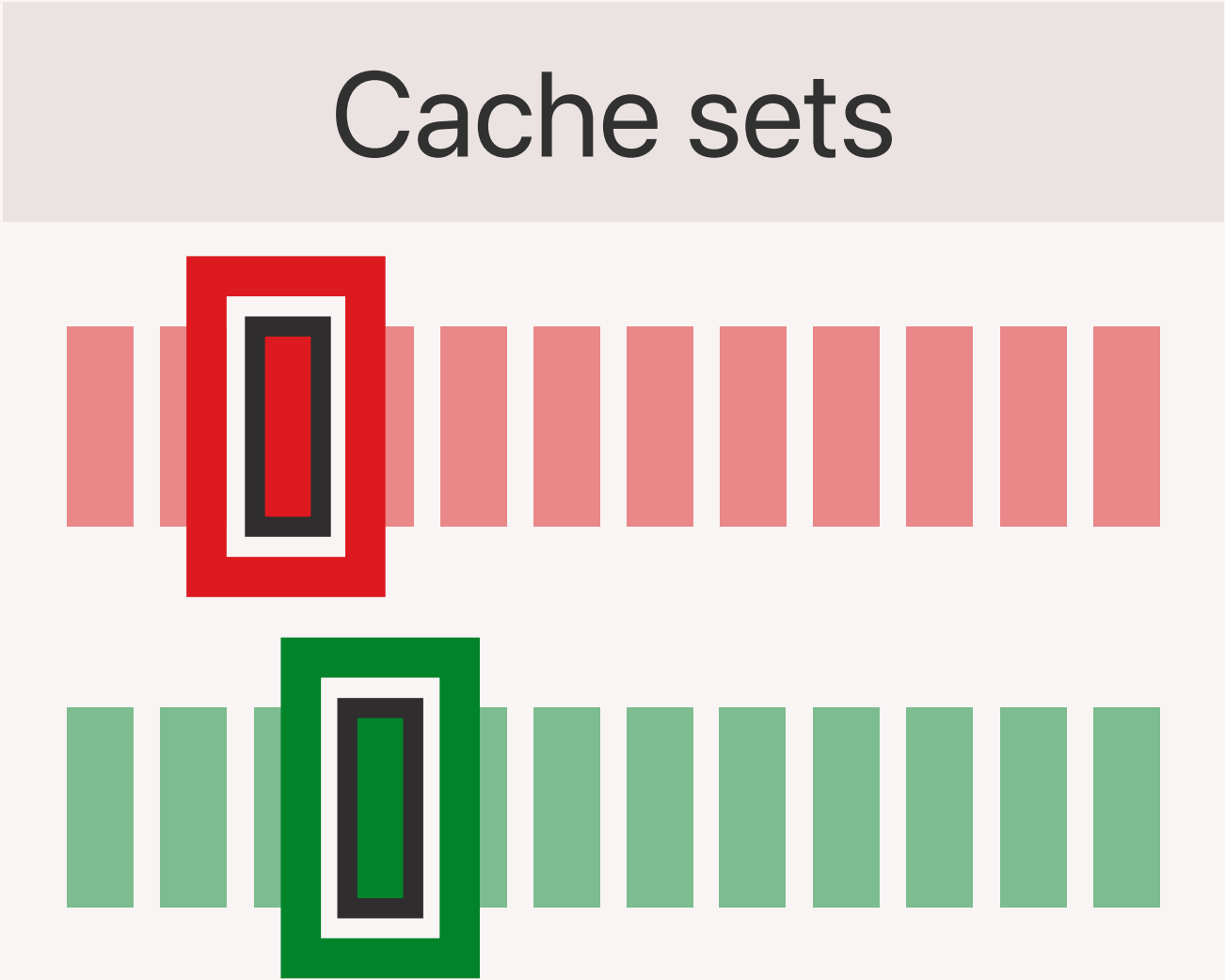


Eviction sets

Cache Eviction

Two sets + cache hits

Bank
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100

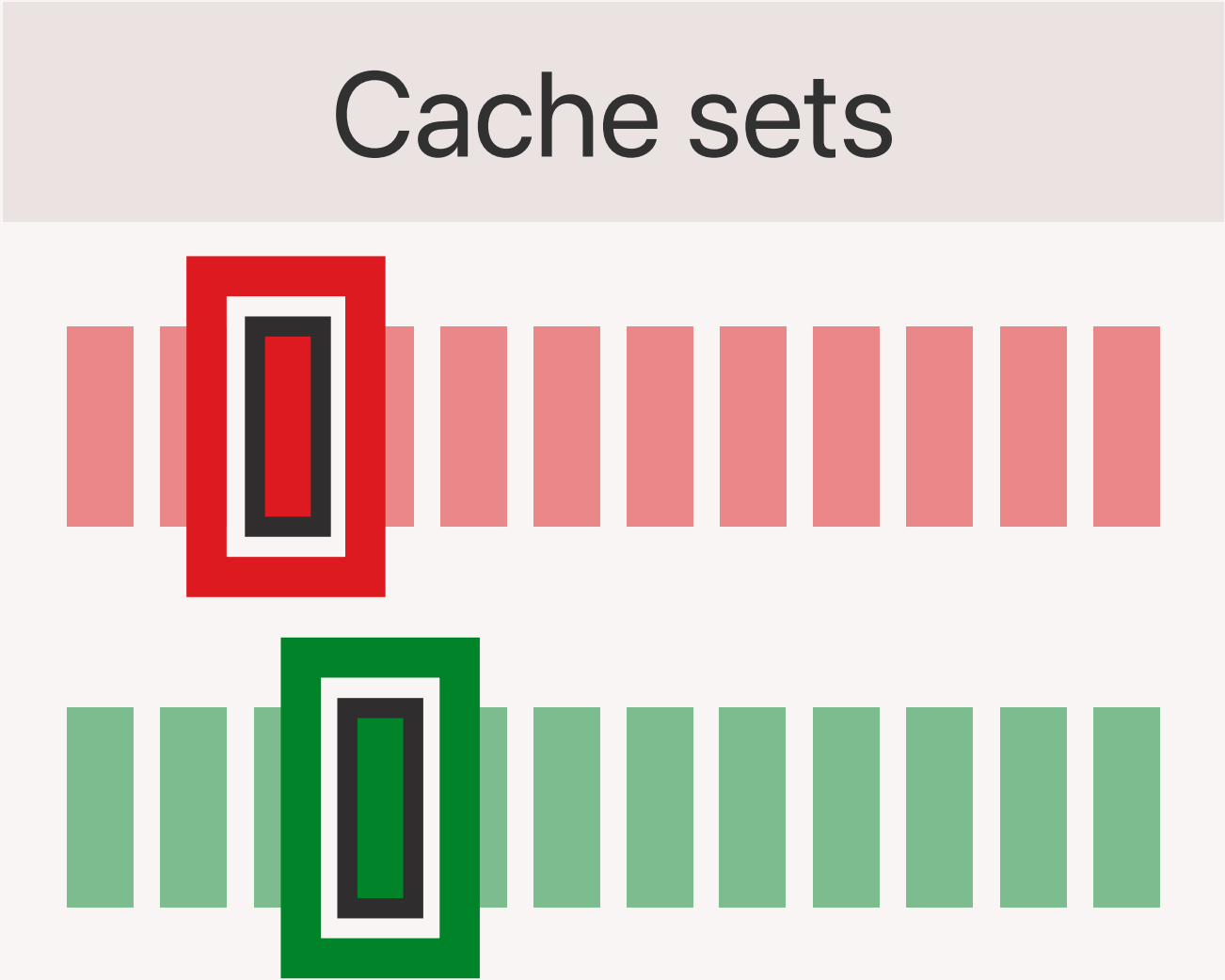


Eviction sets

Cache Eviction

Two sets + cache hits + self-eviction

Bank
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100

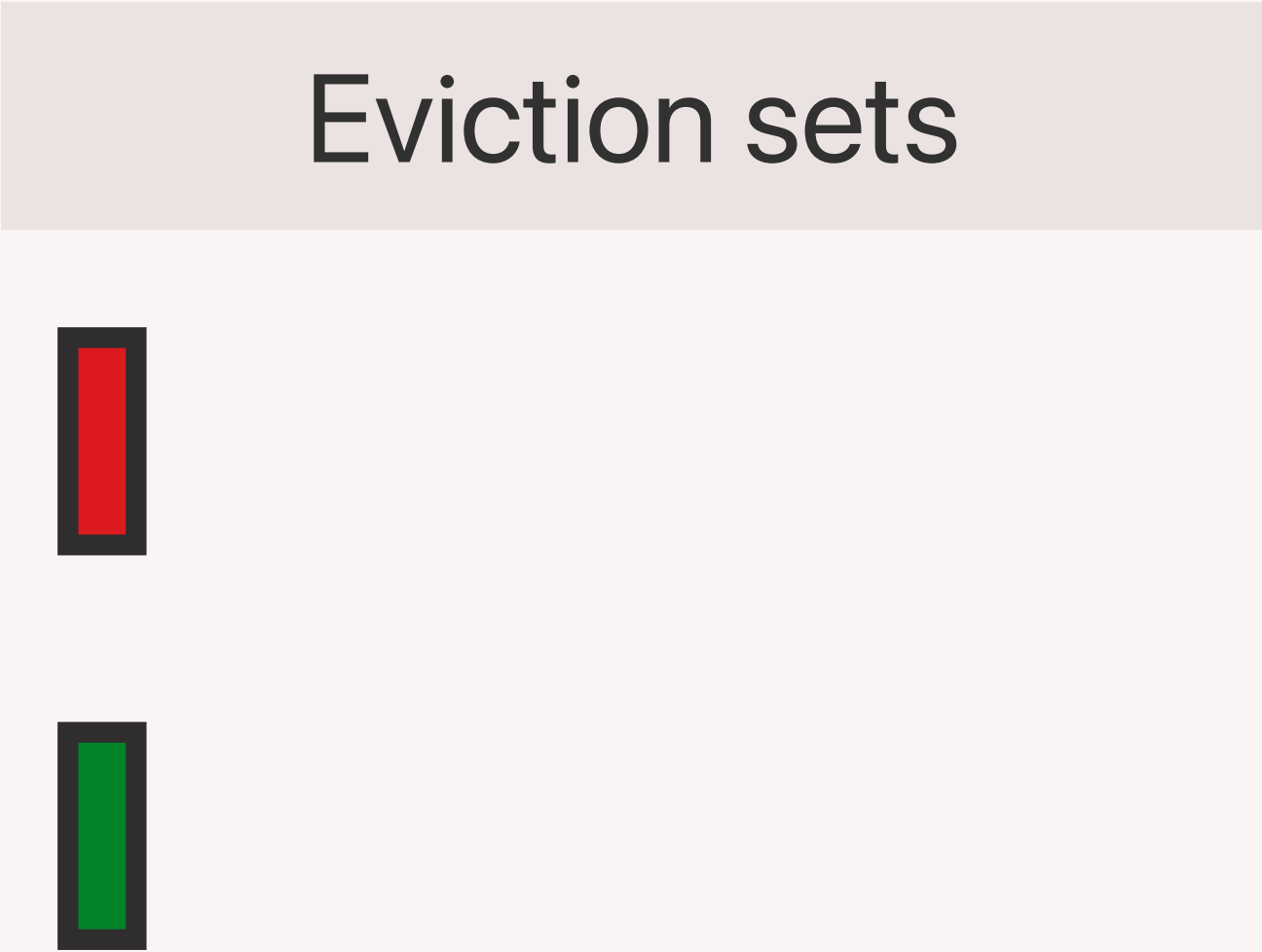
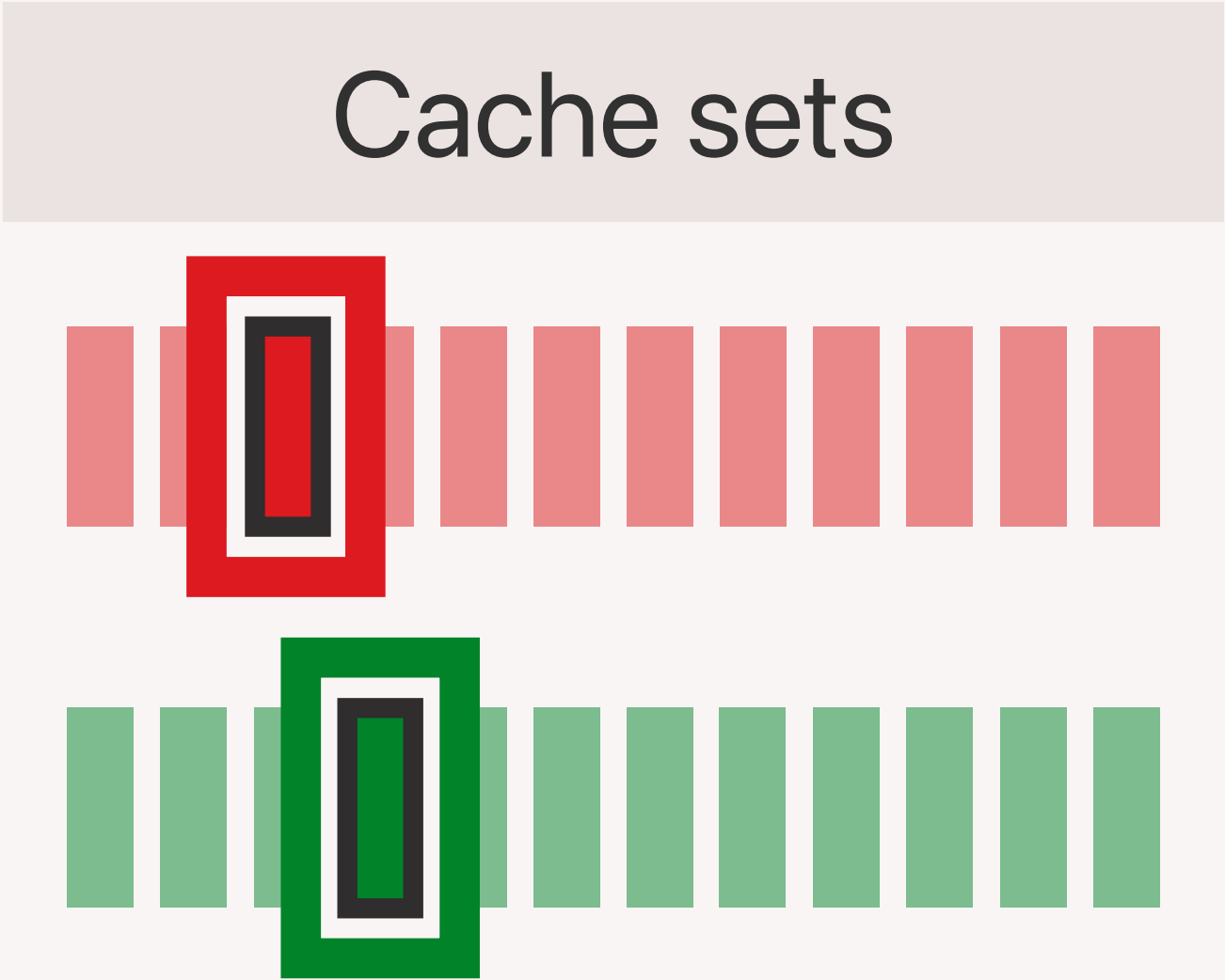


Eviction sets

Cache Eviction

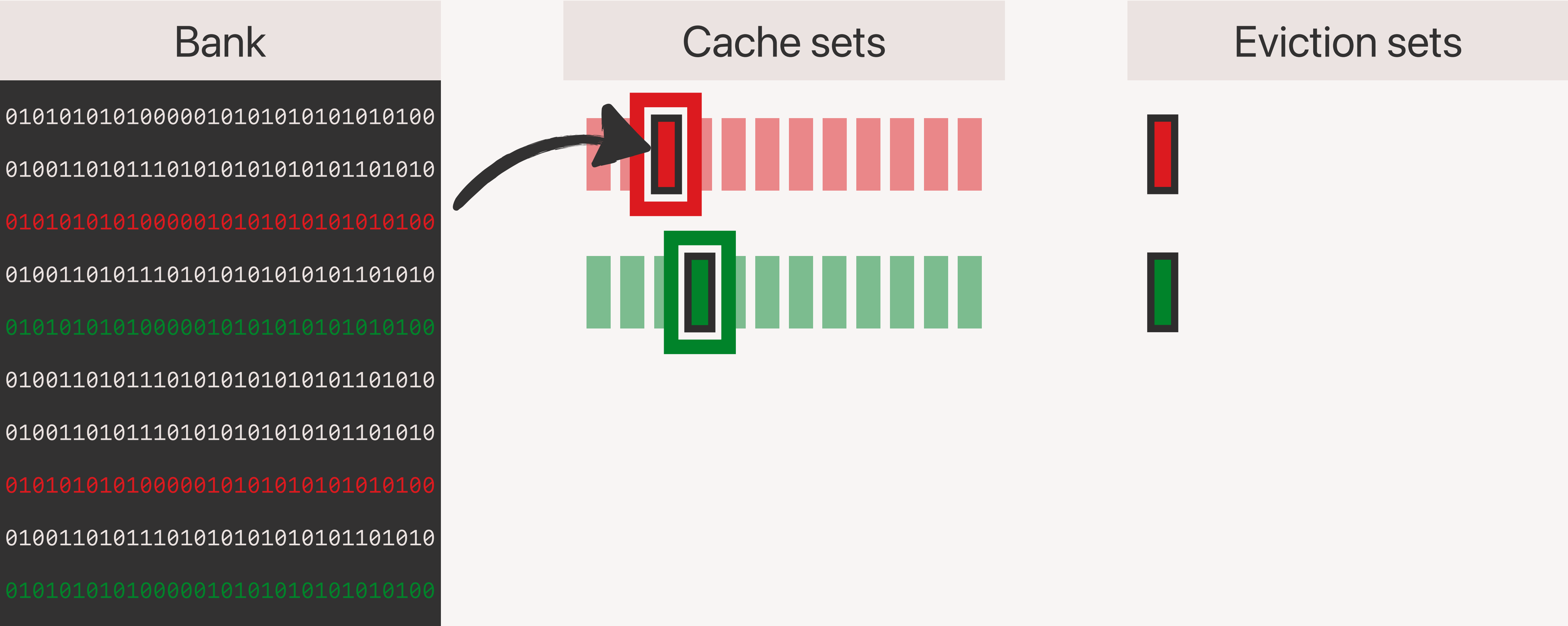
Two sets + cache hits + self-eviction

Bank
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100



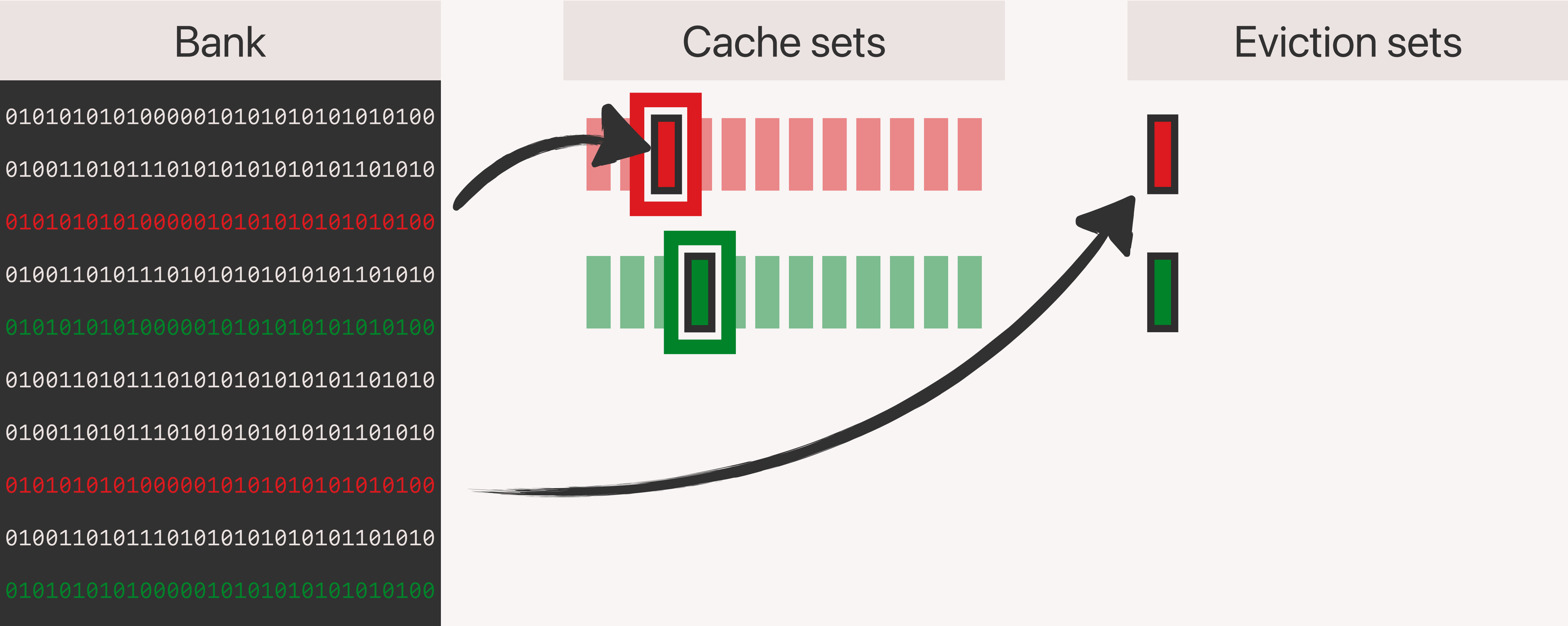
Cache Eviction

Two sets + cache hits + self-eviction



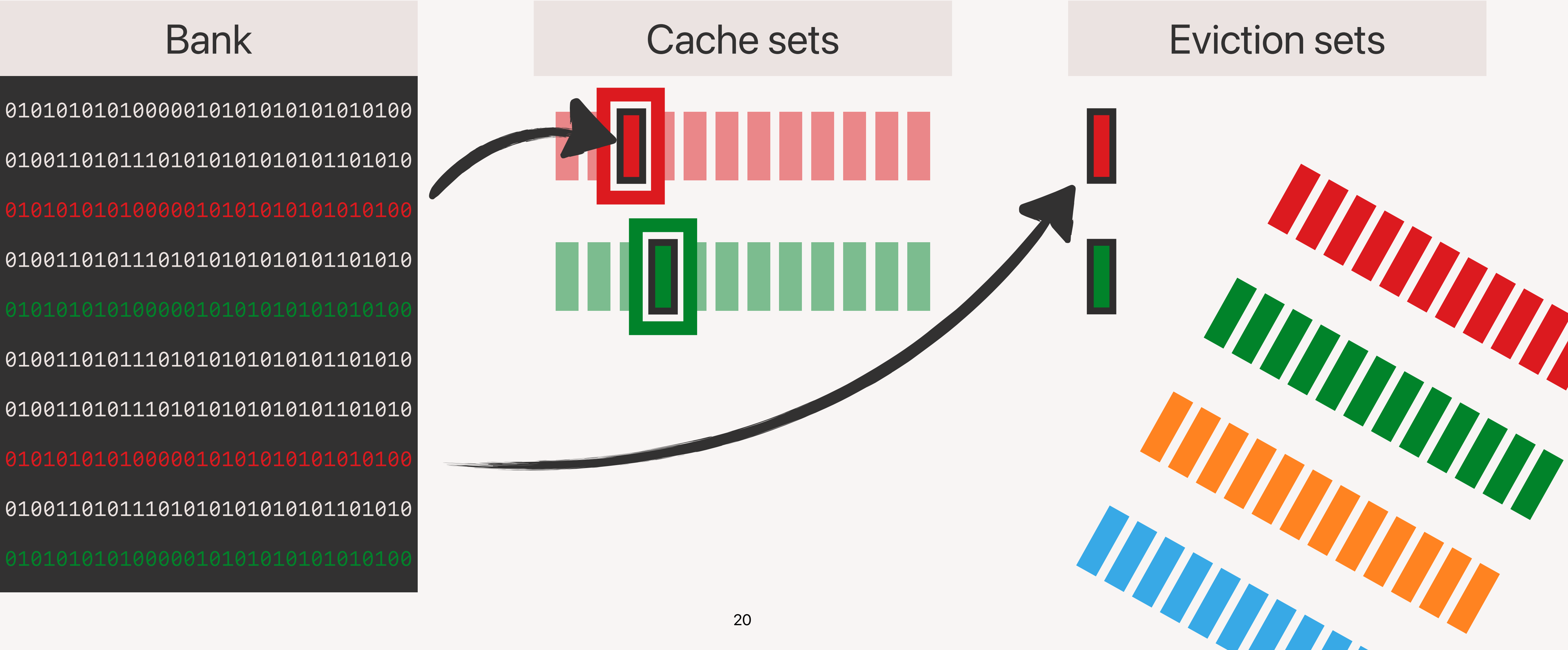
Cache Eviction

Two sets + cache hits + self-eviction



Cache Eviction

Two sets + cache hits + self-eviction



Rowhammer From JavaScript

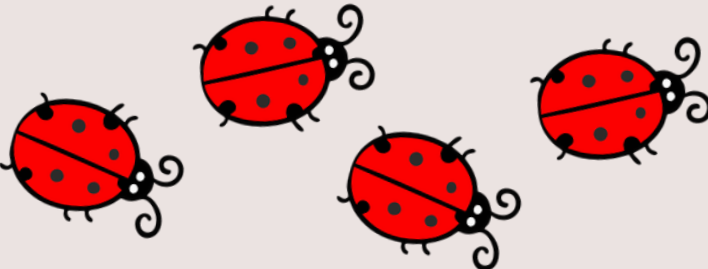
Complicated



Virtual
address
space

Physical
space
Huge pages

"DRAM"
address
space



Bank

0101010101000001010101
0100110101110101010101
0101010101000001010101
0100110101110101010101
0101010101000001010101
0100110101110101010101
0101010101000001010101
0100110101110101010101

Caches

Rowhammer From JavaScript

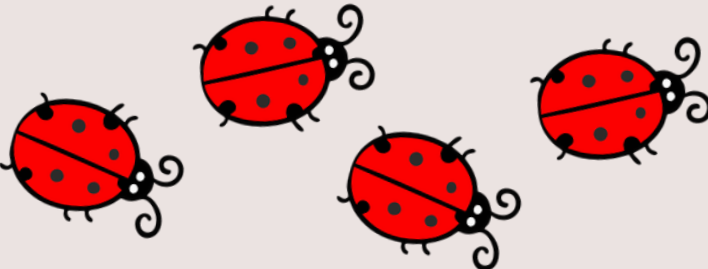
Complicated



Virtual
address
space

Physical
space
Huge pages

"DRAM"
address
space

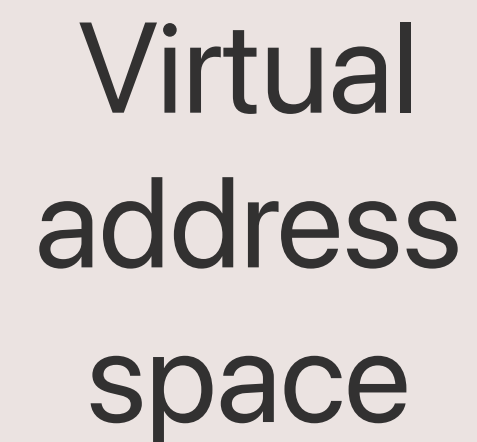


Bank

0101010101000001010101
0100110101110101010101
0101010101000001010101
0100110101110101010101
0101010101000001010101
0100110101110101010101
0101010101000001010101
0100110101110101010101

Caches

Complicated



~~Physical
Huge pages
space~~

"DRAM" address space

Self-evict

Caches

Bank

0101010101000010101

01001101011101010101

01010101010000010101

01001101011101010101

0101010101000010101

01001101011101010101

01010101010000010101

01001101011101010101

Done!

Done!?

TRR's Little Secret

Part III



Refresh Commands

Prevent data loss

Bank
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010

Refresh Commands

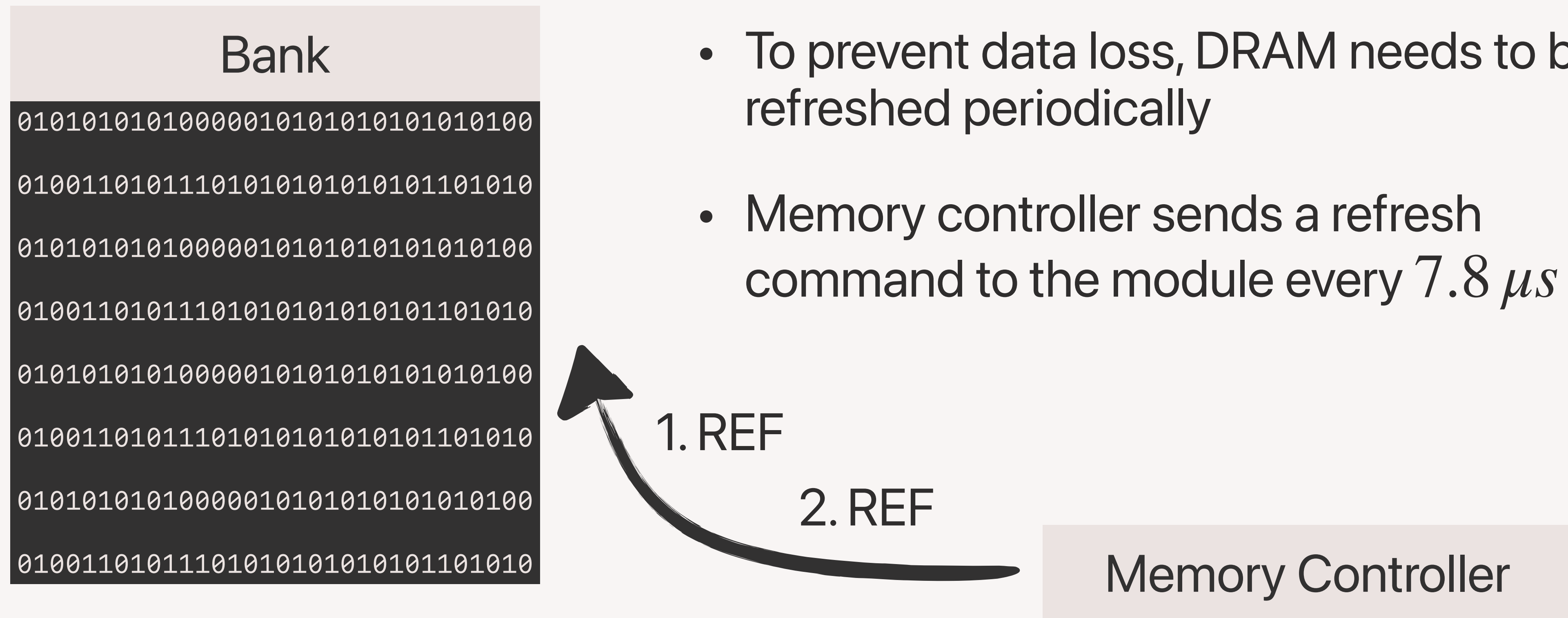
Prevent data loss

Bank
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010
01010101010000010101010101010100
01001101011101010101010101101010

- To prevent data loss, DRAM needs to be refreshed periodically
- Memory controller sends a refresh command to the module every $7.8 \mu s$

Refresh Commands

Prevent data loss



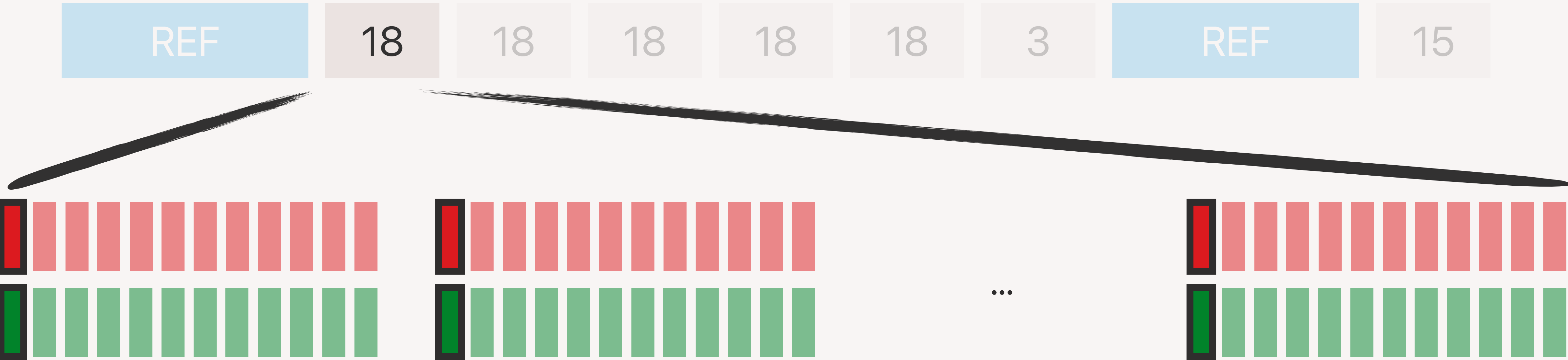
Request Layout

Example



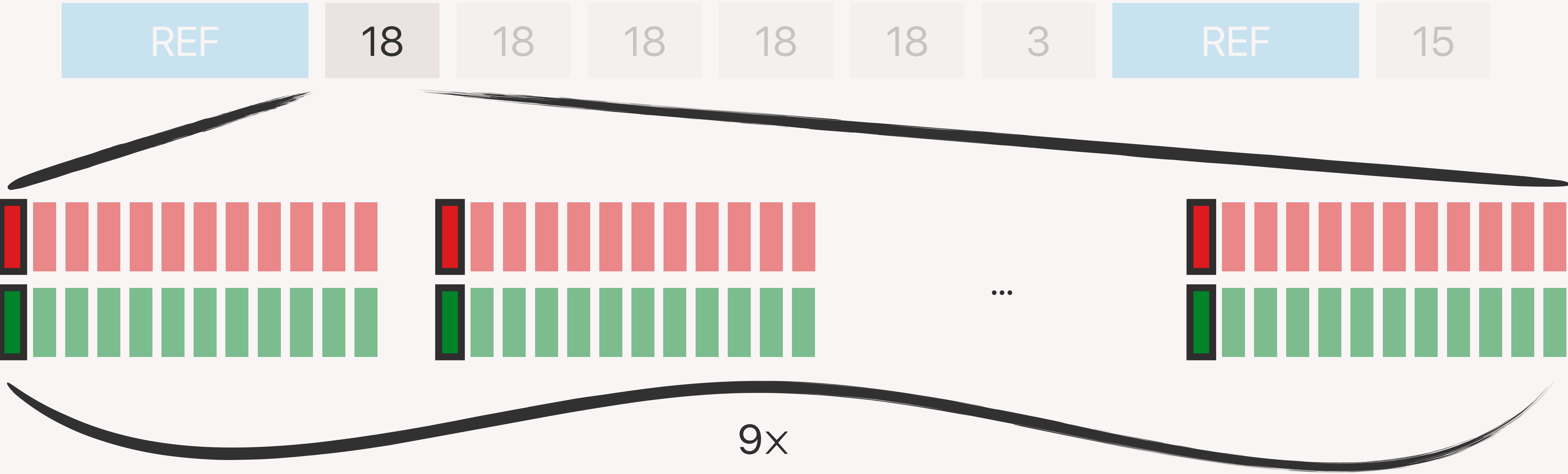
Request Layout

Example



Request Layout

Example



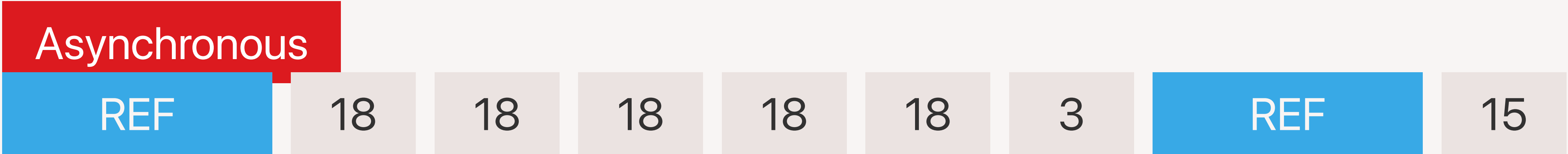
Request Layout

Example



Request Layout

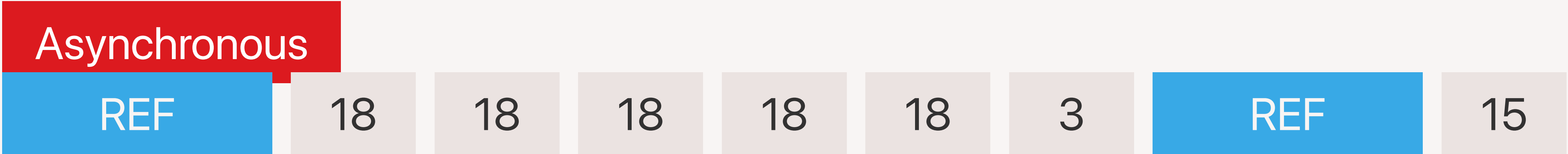
Example



Request Layout

Example

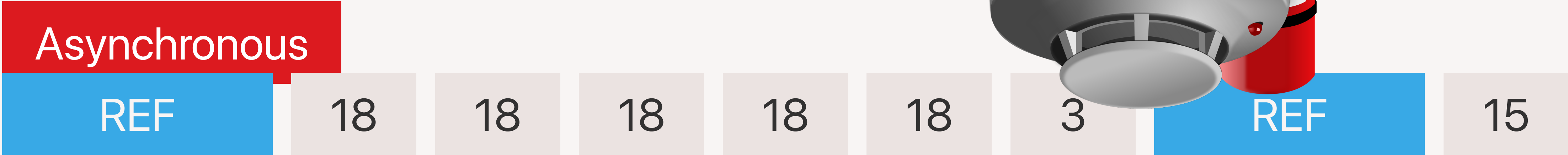
Will sample all aggressors over time



Request Layout

Example

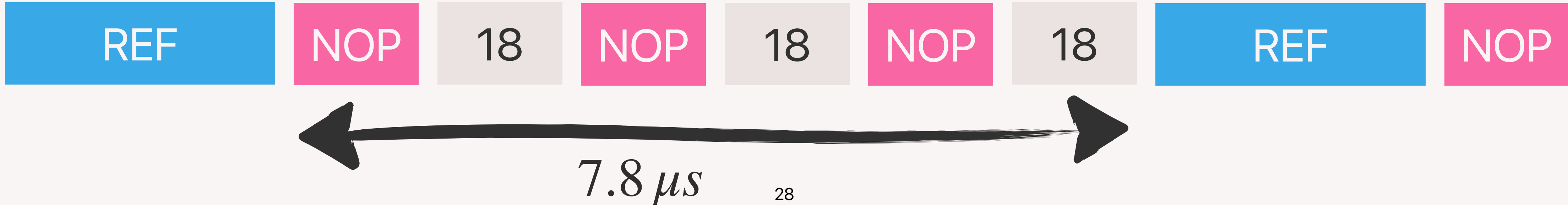
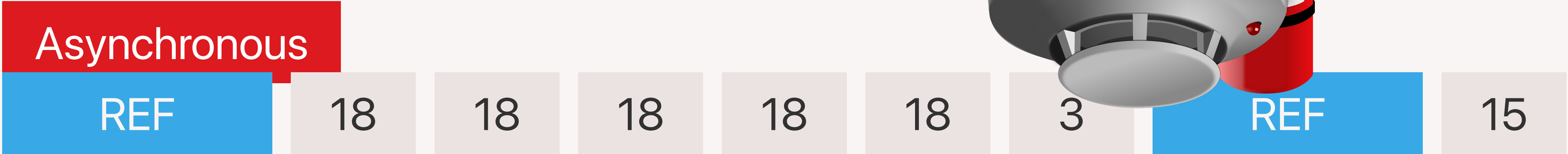
Will sample all aggressors over time



Request Layout

Example

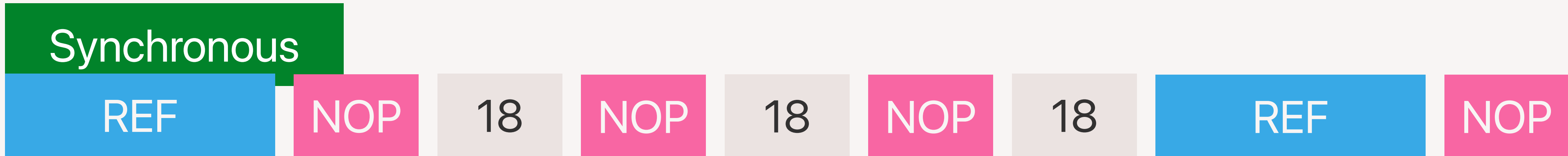
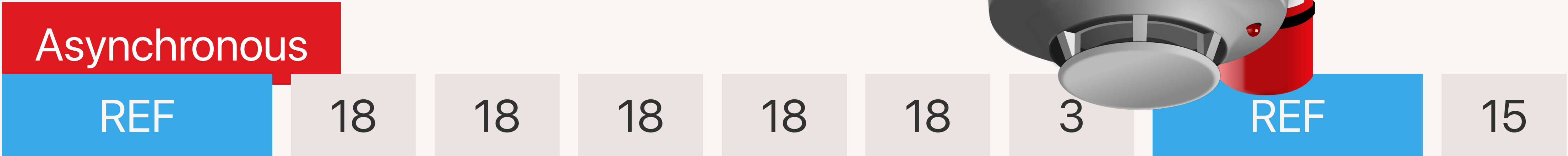
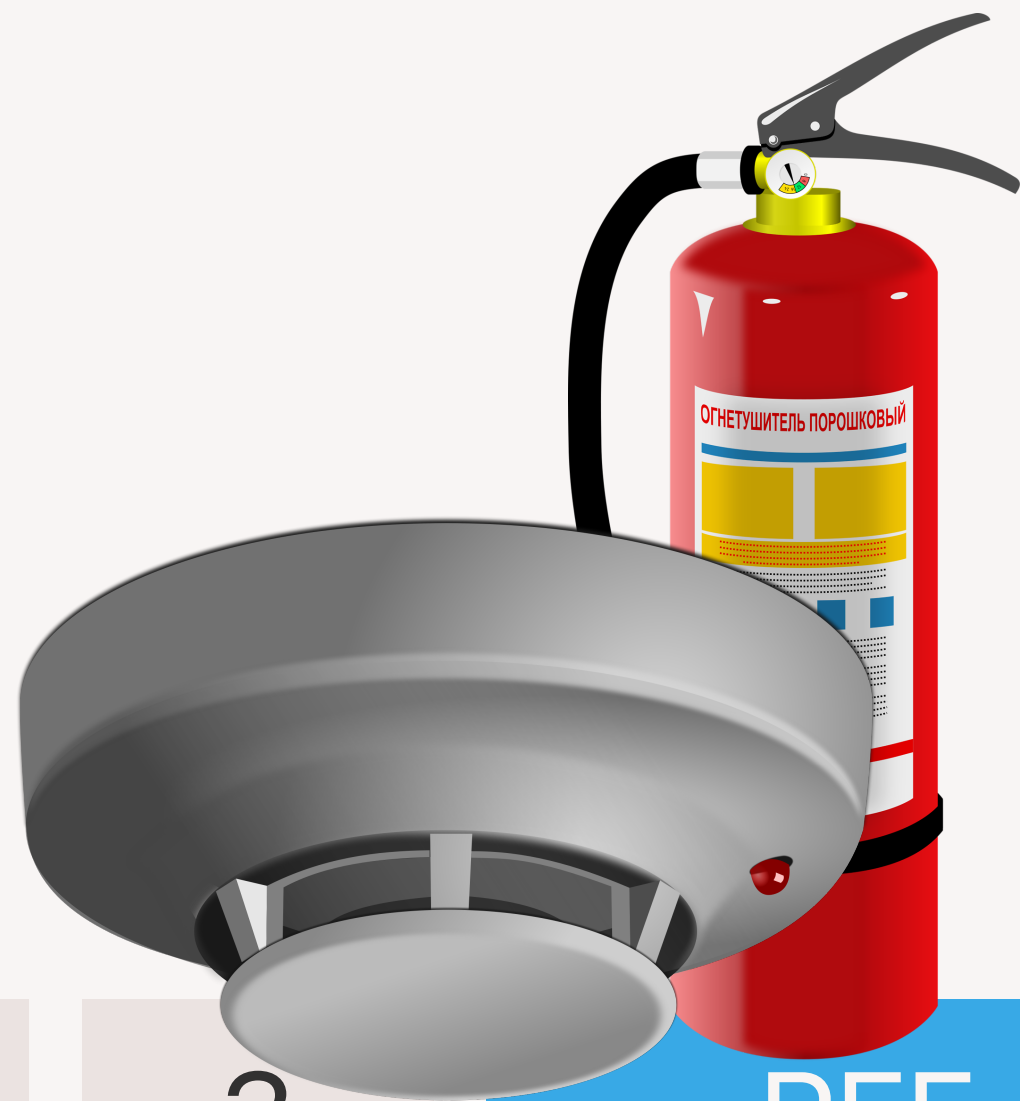
Will sample all aggressors over time



Request Layout

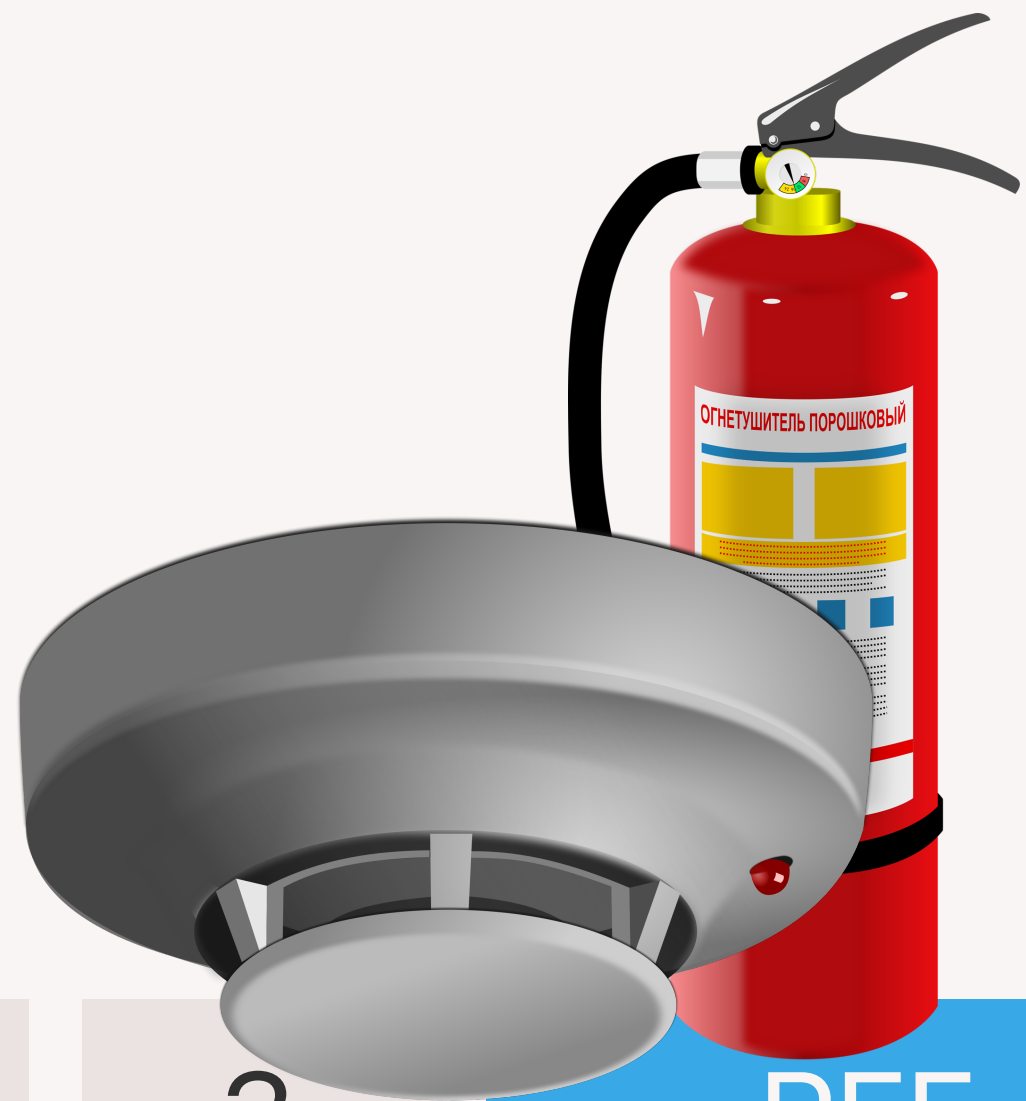
Example

Will sample all aggressors over time

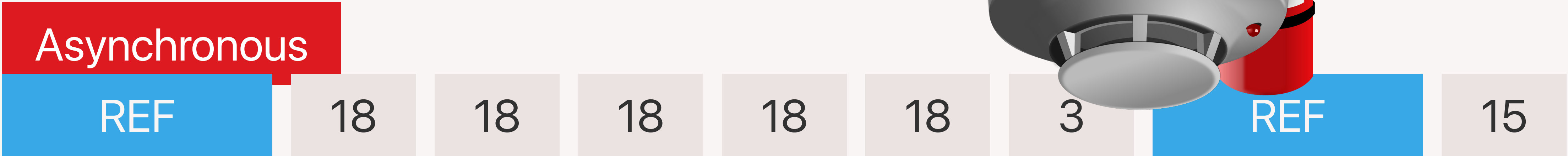


Request Layout

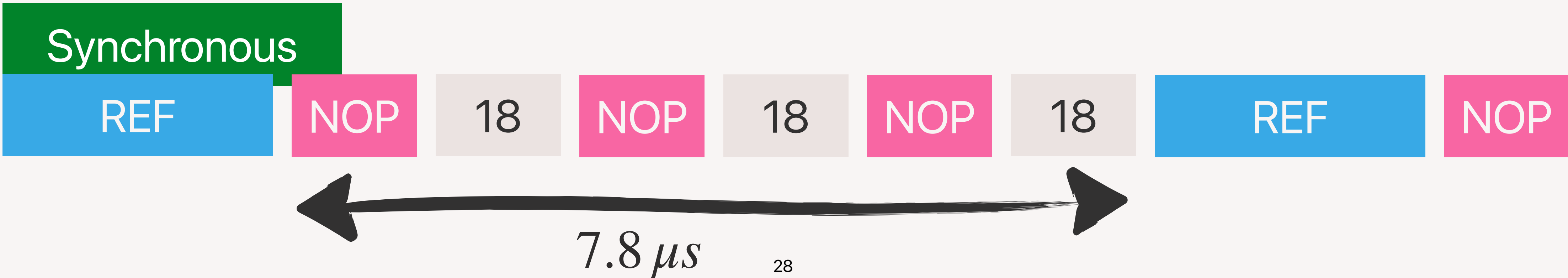
Example



Will sample all aggressors over time



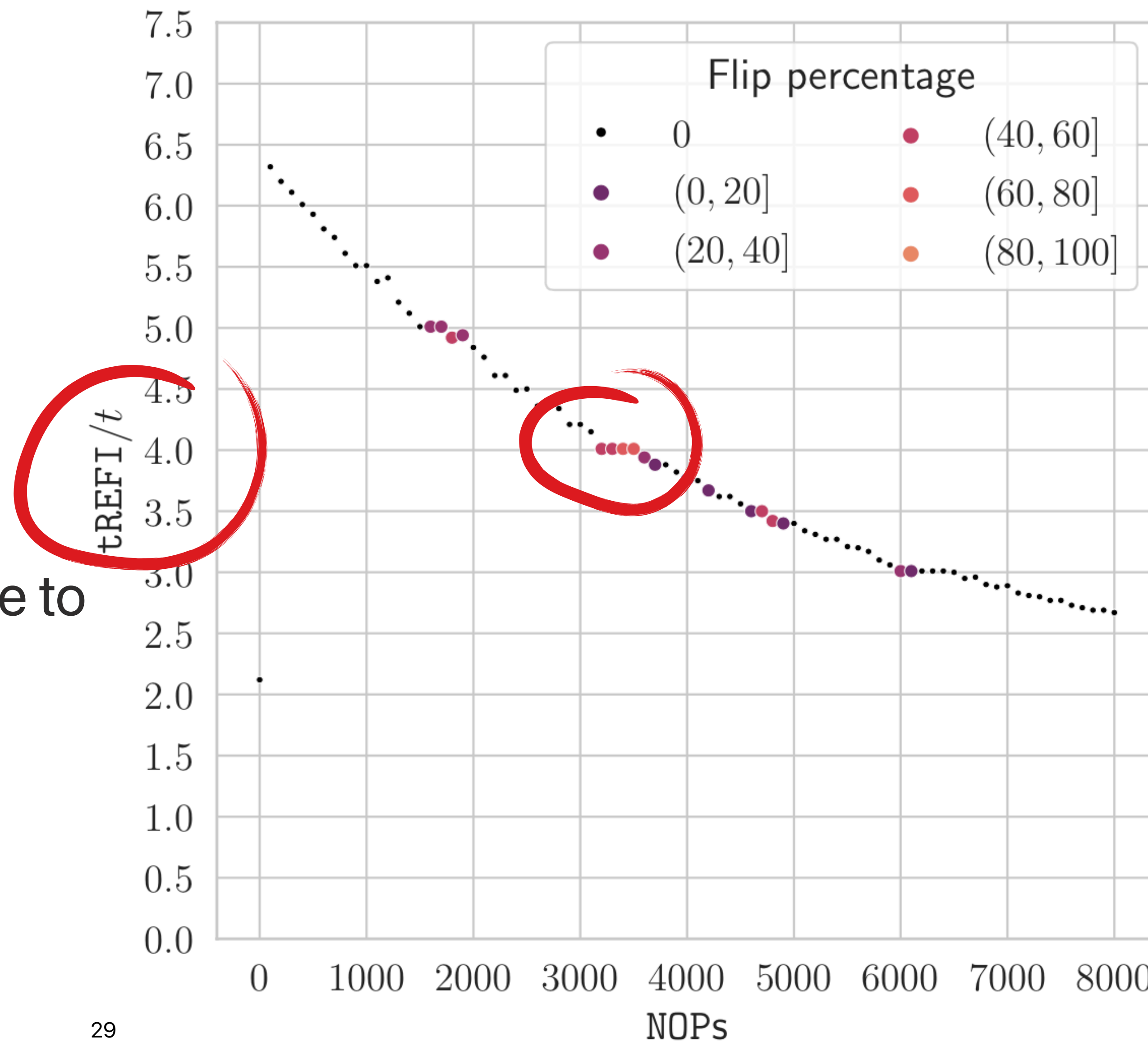
Will always sample the same aggressors, some will never be sampled



Bit Flips When Synchronized

By inserting nothing (NOPs)

1. We observe synchronization with refresh commands
2. Only when synchronized we are able to trigger bit flips



Rowhammer From JavaScript

Complicated



Virtual
address
space

Physical
space
Huge pages

"DRAM"
address
space

Bank

010101010100000101010101
010011010111010101010101
010101010100000101010101
010011010111010101010101
010101010100000101010101
010011010111010101010101
010101010100000101010101
010011010111010101010101

Hammer

REF

Self-evict

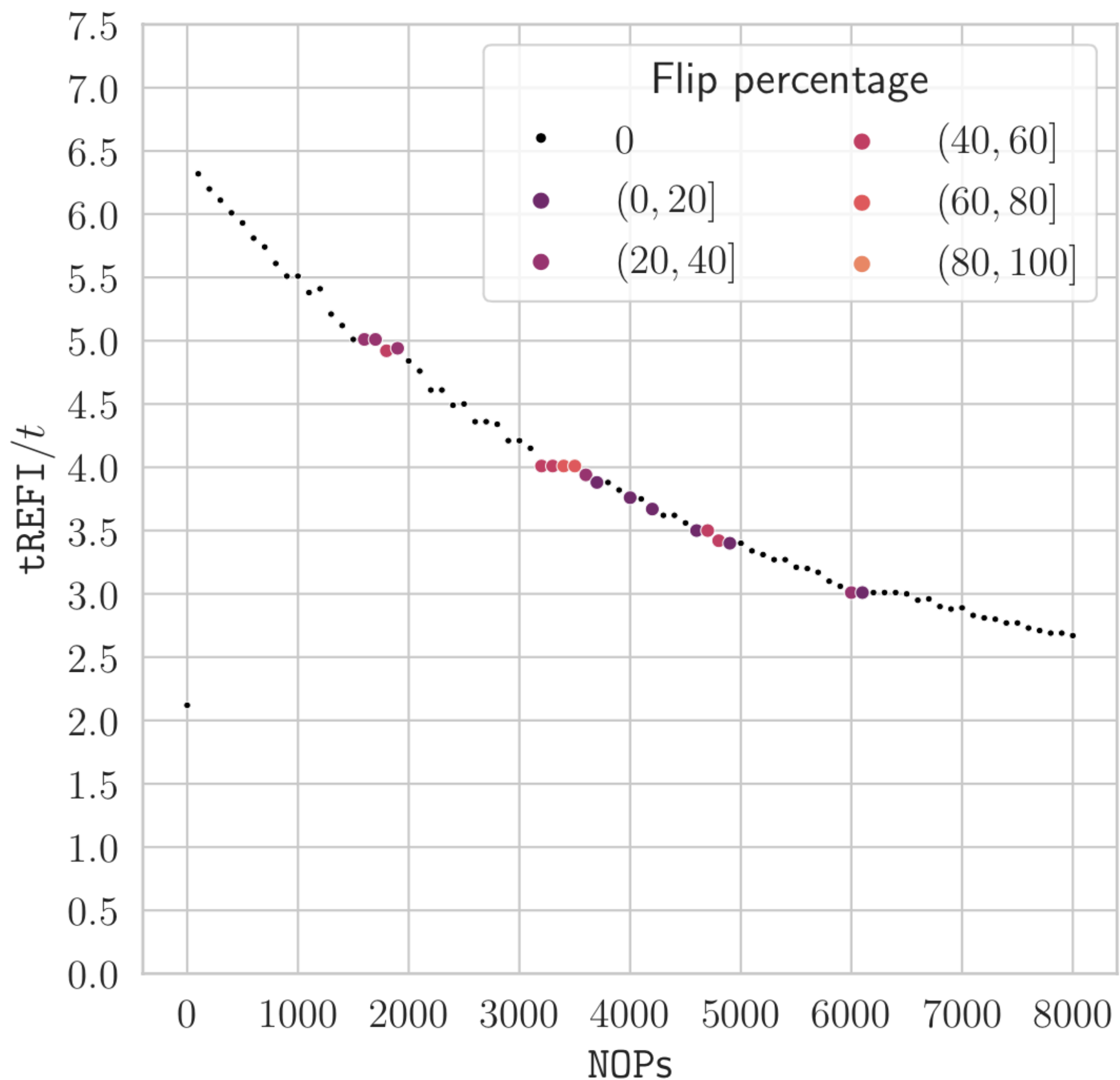
Caches

Synchronizing in JavaScript May Be Difficult

Can we do better?

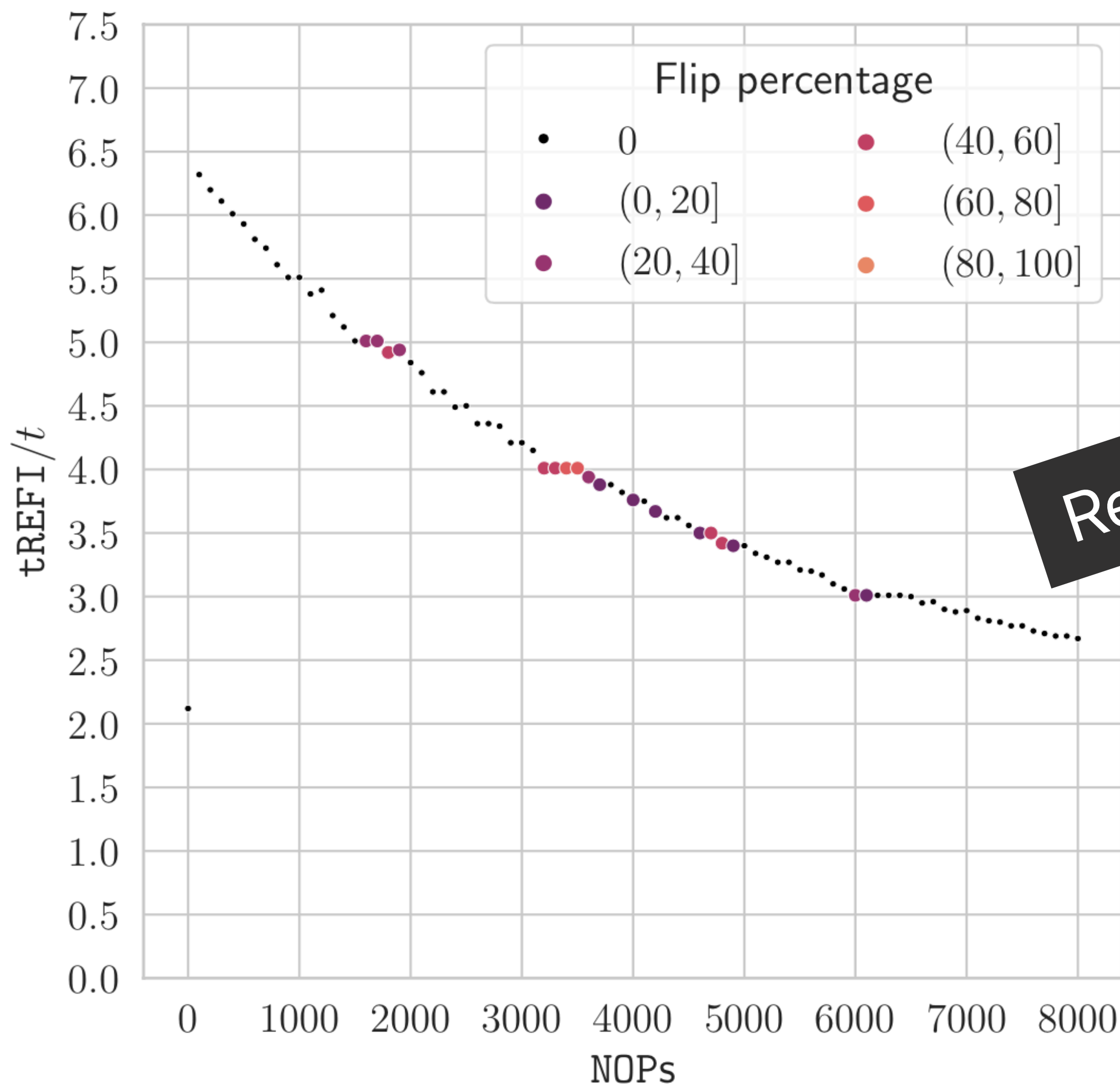
Synchronizing in JavaScript May Be Difficult

Can we do better?



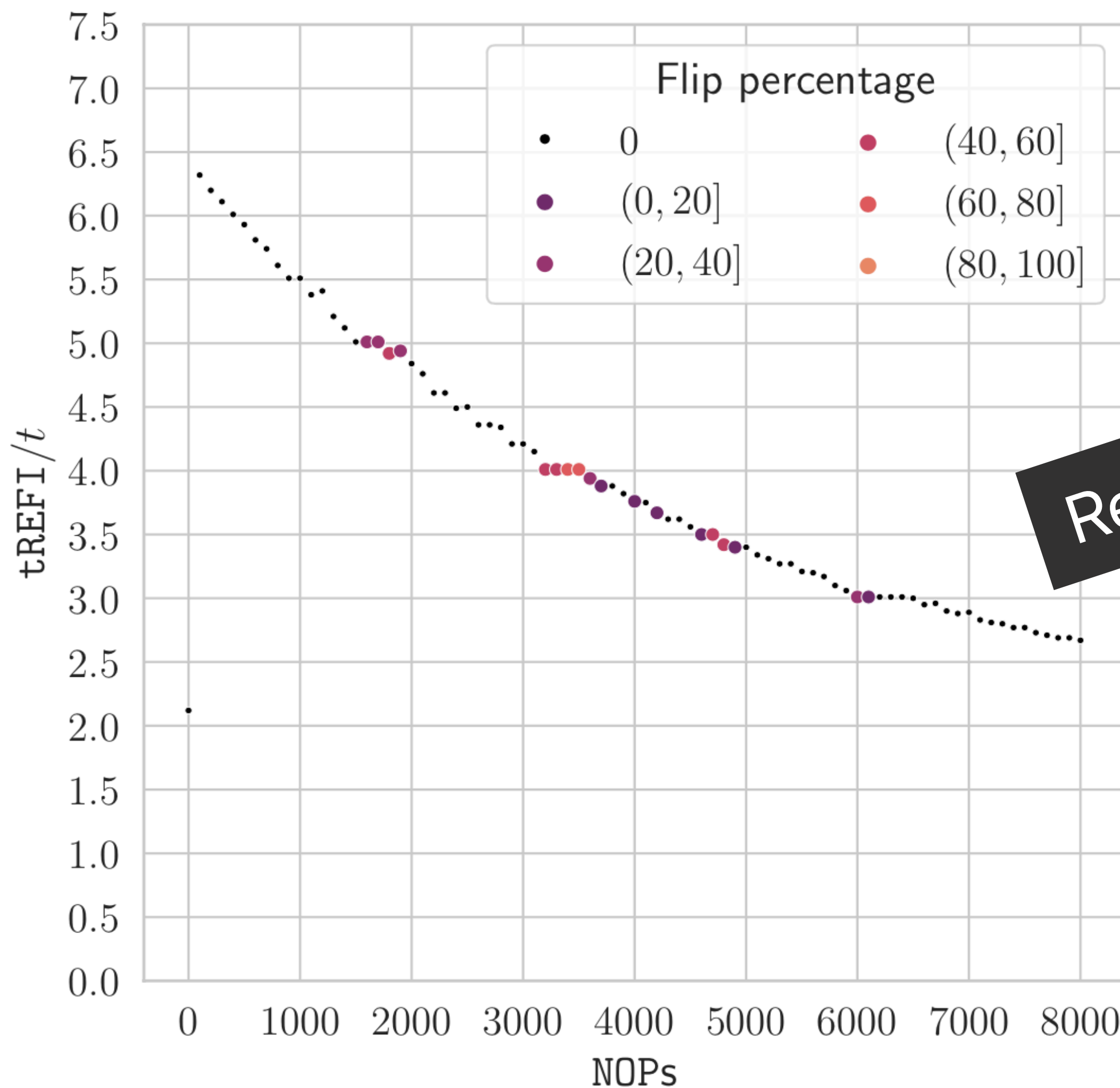
Synchronizing in JavaScript May Be Difficult

Can we do better?

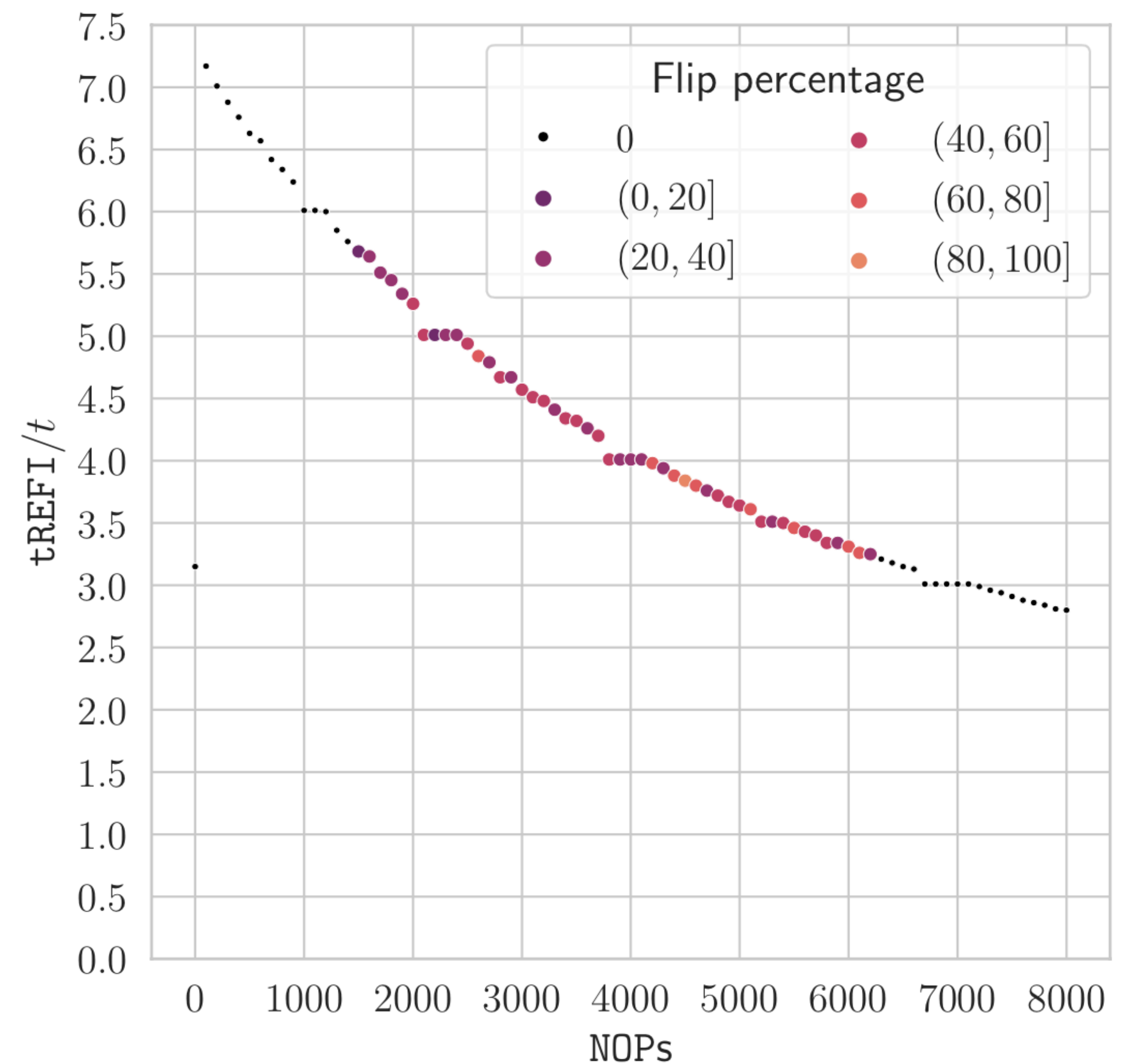


Synchronizing in JavaScript May Be Difficult

Can we do better?

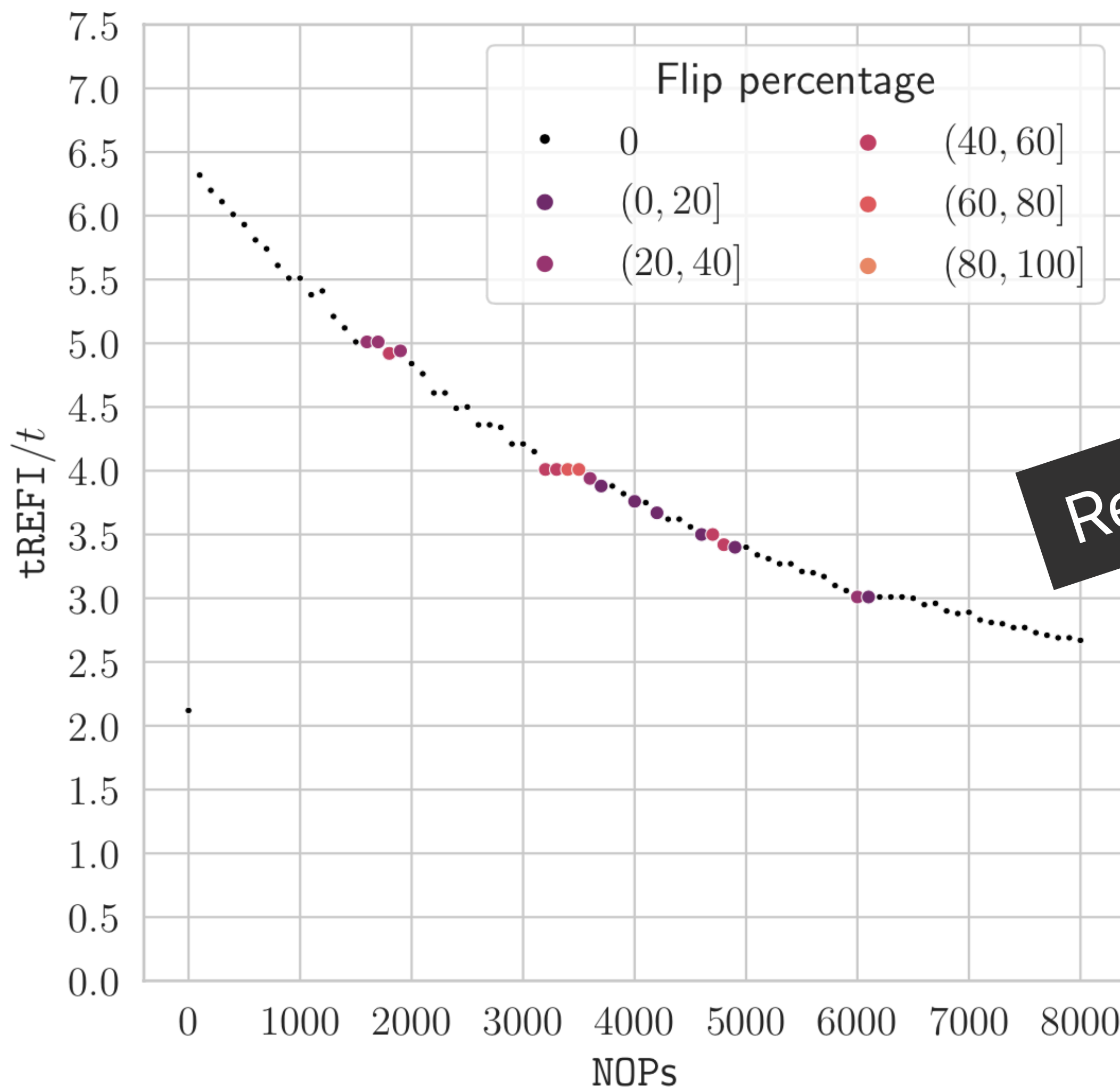


Rearrange

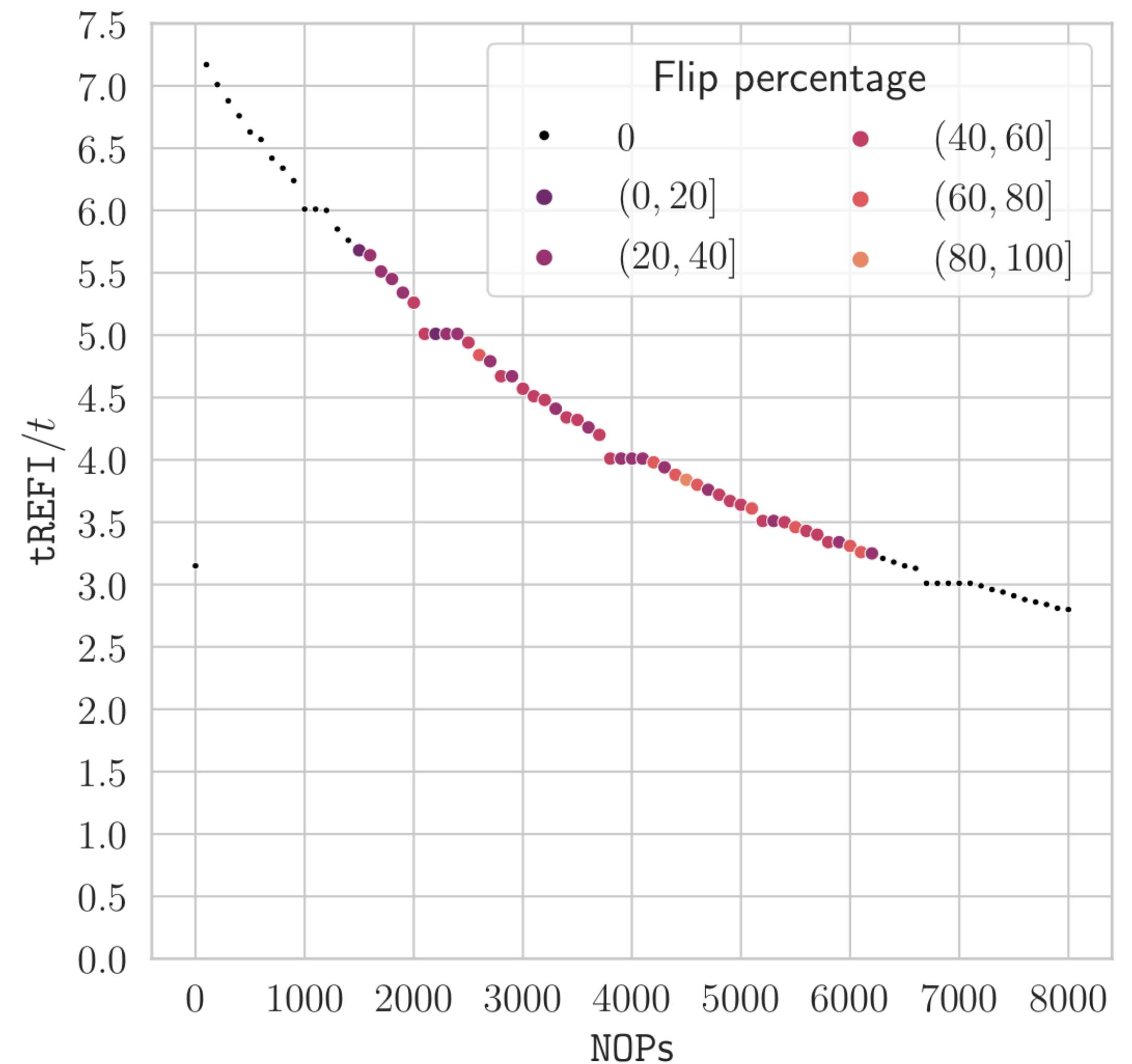


Synchronizing in JavaScript May Be Difficult

Can we do better? Yes!



Rearrange



Results

Pattern characteristics

System	Best TRRespass	Aggressors	Total length incl. hits
0	19-sided	18	96
1	10-sided	10	160
2	3-sided	4	64

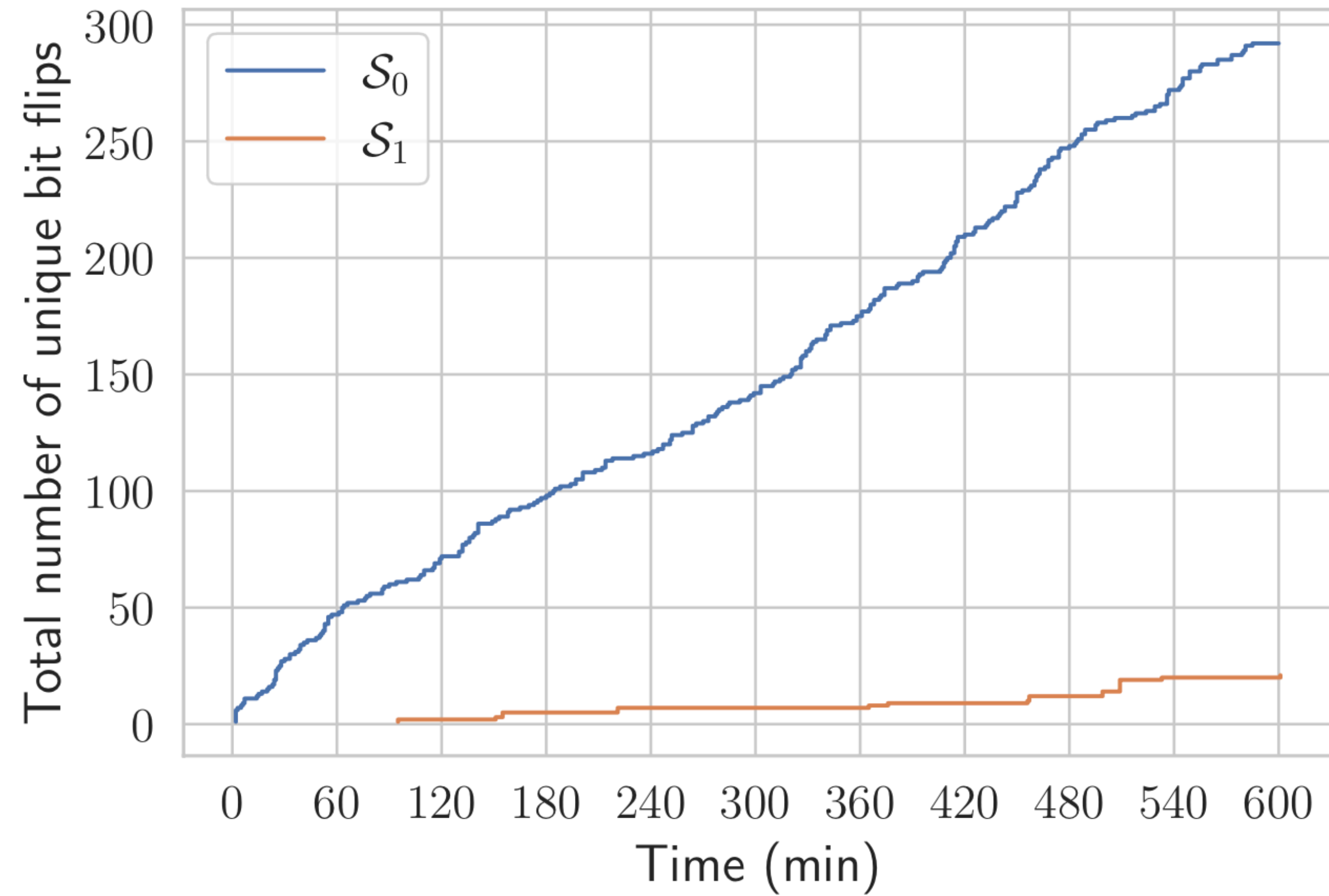
Results

Bit flips

System	Native flips	NOPs	JavaScript flips	XORs
0	Yes	1500-6200	Yes	300-900
1	Yes	100-1900, 3500-3700	Yes	0-400, 700
2	Yes	100	—	—

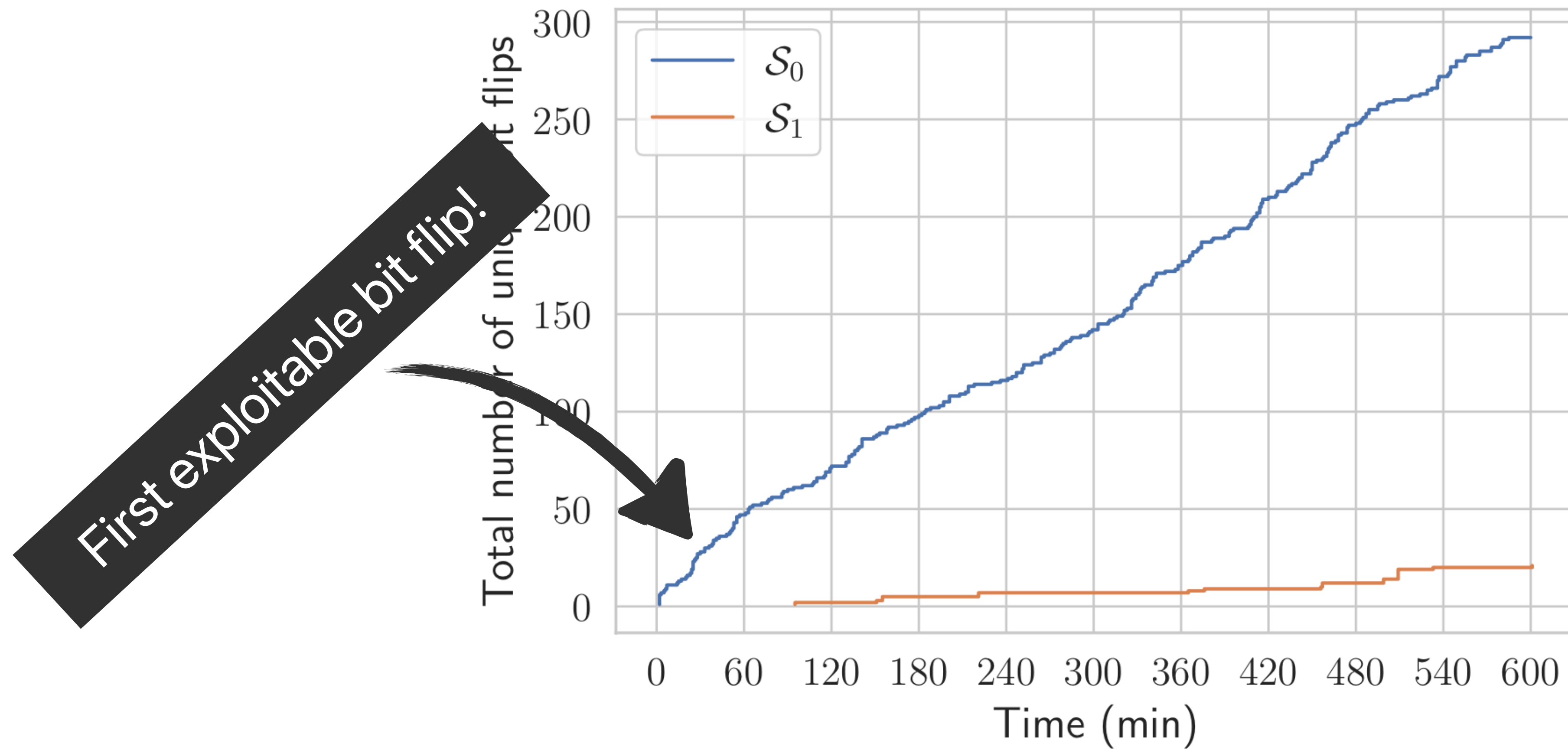
Results

Bit flips over time



Results

Bit flips over time



Conclusion

Part IV

Many-sided Rowhammer in JavaScript Is...

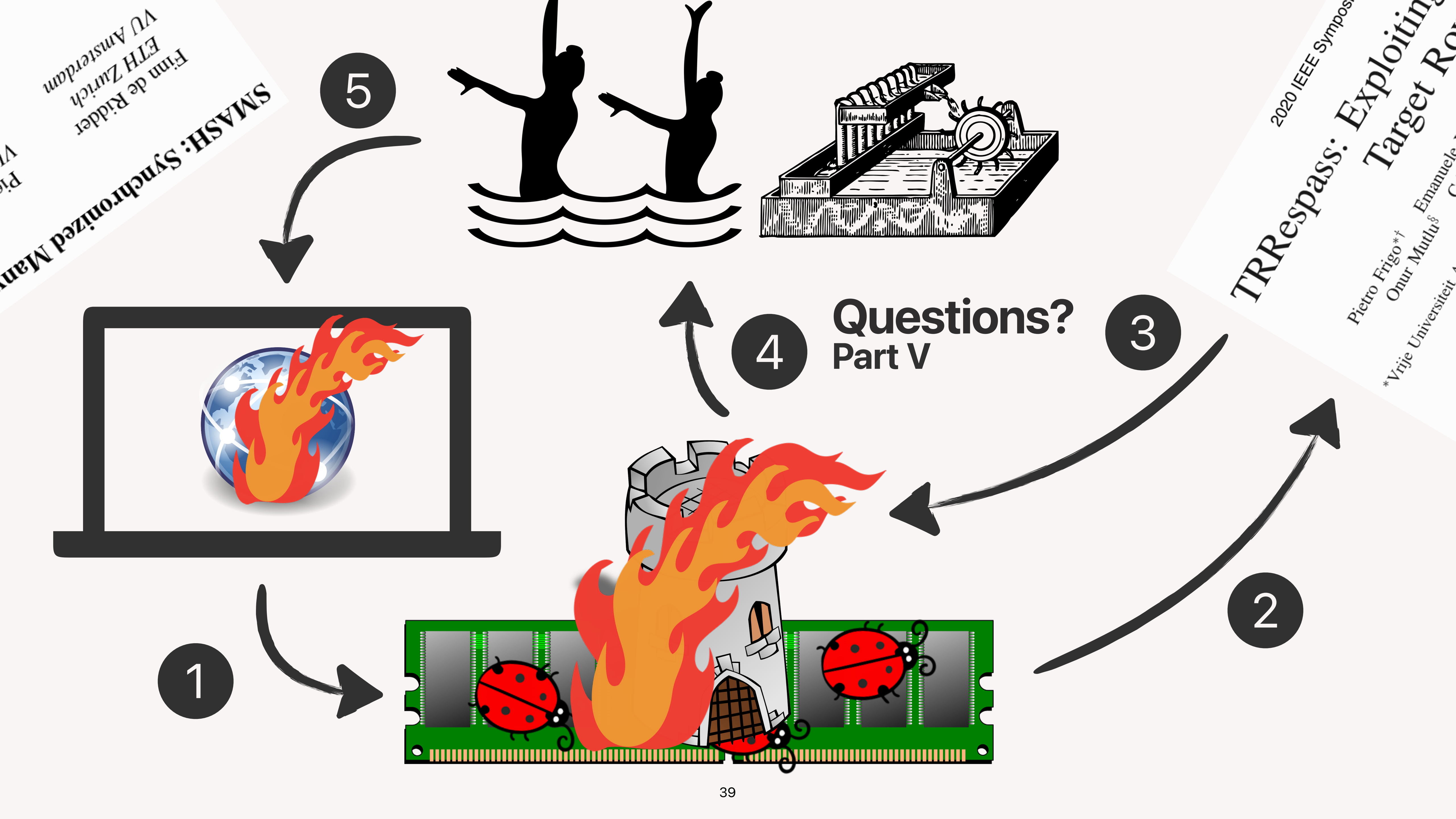
JS we can

- Possible if you create a synchronized self-evicting pattern
- Despite all hardware and software mitigations
- Re-enables past JavaScript-based Rowhammer attacks in 2021

Many-sided Rowhammer in JavaScript Is...

JS we can

- Possible if you create a synchronized self-evicting pattern
- Despite all hardware and software mitigations
- Re-enables past JavaScript-based Rowhammer attacks in 2021 (unfortunately)



Finn de Ridder
ETH Zurich
VU Amsterdam

2020 IEEE Symposium
TRRespass: Exploiting
Target Resource Access
Pietro Frigo*† Emanuele
Onur Mutlu*§
*Vrije Universiteit Amsterdam

Source code available at <https://github.com/vusec/smash>

Demo at <https://www.youtube.com/watch?v=k2D4D-kF-ic>

Questions?

Part V