

## Thunderbolt: Throughput-Optimized, QoS-Aware Power Capping at Scale

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1

## Motivation: power oversubscription and capping

#### \$200+B worldwide spend on data centers

#### **Power oversubscription:**

#### more capacity without construction

- Data center aggregated power usage is rarely close to the theoretical max
- But protective systems are needed to avoid overload due to rare power spikes

#### **Power capping:**

#### protective system that shaves power spikes

• Throttles running tasks without violating their SLOs



## Motivation: task QoS differentiation

#### Throughput-oriented tasks

- Completes on the order of hours or more
- Examples: web indexing, log processing
- Amenable to performance throttling (runs slower)
- Not amenable to disruption (wastes work)

#### Latency-sensitive tasks

- Responds to requests on the order of milliseconds to seconds
- Examples: web front-end, search service
- Not amenable to performance throttling (becomes unresponsive)

#### Tasks of different QoS are co-located

- Google's cluster scheduler does not assign priority or QoS to machines. They are assigned to tasks
- Improves machine utilization

**Goal:** Task QoS-aware capping that gently throttles throughput-oriented tasks and exempts latency-sensitive tasks



## Prior industry solutions did not meet our needs

Either task QoS-aware but has disruptive capping action...

**Example:** Capping system for medium voltage power plane [1]

Appropriate for clusters with low-priority tasks that can tolerate disruption

4

...Or has gentle throttling but coarser-grained QoS differentiation

Examples: Dynamo [2], CapMaestro [3]

Appropriate for clusters with coarser-grained QoS differentiation, such as machine level

[1] Sakalkar et al. Datacenter power oversubscription with a medium voltage power plane and priority-aware capping. ASPLOS 2020.
[2] Wu et al. Dynamo: Facebook's data center-wide power management system. ISCA 2016.
[3] Li et al. A scalable priority-aware approach to managing datacenter server power. HPCA 2019.

# Thunderbolt's contributions

Simultaneously achieves the following:

01

# 02

Power safety with minimized performance degradation

Efficient use of power budget while being responsive and effective to reduce power. Task-level QoS differentiation

Flexible to apply different throttling levels to tasks with different SLO, even on the same machine.

## Hardware platform independence

03

Applicable to all platforms of all vendors. Accelerates new platform introduction.

## 04

## Tolerance of power telemetry unavailability

Failover subsystem that can safely operate without power telemetry.



Enables oversubscription in logs processing clusters from 0 to 9--25%



# Architecture & Implementation

## Architecture

#### "Reactive capping" primary subsystem

Operates when power signals are available.

- 1. Meter watcher reads power from meters
- 2. Power notifier determines whether and how to throttle based on the *"load shaping"* policy
- 3. Machine manager sends throttling (load shaping) RPCs to node controllers
- 4. Node controller throttles tasks using "CPU bandwidth control"

#### "Proactive capping" failover subsystem

Operates when power signals are **unavailable**.

- 1. Risk assessor reads power history data and assesses risk of power overload
- 2. If risk is high, machine manager sends throttling ("CPU jailing") RPCs to node controllers
- 3. Node controller throttles tasks using CPU jailing



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## Mechanism and policy details





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If the machine has 2 logical CPUs, then its CPU utilization is capped at (70 + 90) / (100 \* 2) = 80%

#### Reactive capping mechanism: CPU bandwidth control

#### Linux kernel feature

• Platform independent

#### Task-level CPU cap

- A task is assigned to a control group (cgroup)
- A CPU cap is assigned to each cgroup by setting its *quota*

## Why not RAPL or DVFS?

#### RAPL

- Per-socket control. Cannot achieve task-level control without additional scheduling constraint.
- Intel specific. Not desirable for our clusters with diverse platforms.
- Precise power control and wide control dynamic range.

#### **DVFS**

- Supported by most modern platforms.
- Per-core control not supported by Intel pre-Haswell and some non-x86 platforms.
- Narrower power control dynamic range than bandwidth control.
- Higher throughput than bandwidth control under the same power budget.

**CPU bandwidth control**'s native task-level control and platform independence is vital for scalability (DVFS may be added for future efficiency optimization where per-core control is supported)

## CPU bandwidth control, DVFS, RAPL on Intel Skylake CPU





#### CPU power and set point

- Workload: power virus
- Power dynamic range: RAPL > BW control > DVFS

#### CPU power and throughput

- Workload: video transcoding
- Power dynamic range: BW control > DVFS
- Efficiency: DVFS > BW control

## Mechanism and policy details





## Reactive capping policy: load shaping

#### Randomized unthrottling, multiplicative decrease

- Machines unthrottled randomly when throttling ends
- Task CPU cap decreases multiplicatively when throttling is active

#### Two thresholds with two multipliers

• Balances safety and efficiency

#### QoS differentiation: exempting latency tasks

• Continuous monitoring ensures safe power with exempt tasks



hard multiplier = 0.01, soft multiplier = 0.8

## Load shaping on a production cluster

#### **Production cluster**

- Tens of thousands of machines
- Diverse workloads, both throughput-oriented and latency-sensitive

#### Power utilization pattern

- Sawtooth-like pattern
- Power reduced in 2s
- Smaller multiplier  $\Rightarrow$  oscillation with greater magnitude



## Load shaping on a production cluster

#### Failure of affected tasks

• Load shaping does not notably increase failures

#### 99%-ile read latency of exempt storage service

• Load shaping does not notably increase latency

	Duration	Failure fraction	Latency
Baseline	25 min.	0.00002	79 ms
0.95 soft mult.	5 min.	0.00000	79 ms
0.75 soft mult.	10 min.	0.00003	80 ms
0.5 soft mult.	5 min.	0.00007	78 ms

## Mechanism and policy details





## Proactive capping mechanism: CPU jailing

#### Deterministic machine CPU cap

- Deterministic cap is important for power safety
- A fraction, *J*, of logical CPUs on each machine are made unavailable to tasks
- Each machine's CPU utilization is capped at (1 J)
- *J* can be determined by translating power budget to CPU budget using a model of power-CPU relation

#### **Relaxed QoS differentiation**

- Jailed CPUs are inaccessible to all tasks
- Required for strong power guarantee
- Higher priority tasks have more access to remaining CPUs

#### CPU mask of tasks with 20% CPU jailing (J = 0.2)

logical	logical	logical	logical	logical
CPU	CPU	CPU	CPU	CPU
logical	logical	logical	logical	logical
CPU	CPU	CPU	CPU	CPU

green = accessible; gray = jailed

## 20% CPU jailing on a production cluster

#### **Production cluster**

- Tens of thousands of machines
- Diverse workloads, both throughput-oriented and latency-sensitive

#### Task failures

• 20% jailing does not notably affect failures

#### 99%-ile read latency of storage service

• 20% jailing does not notably affect latency

	Duration	Failure fraction	Latency
Baseline	60 min.	0.00003	79 ms
CPU jailing	55 min.	0.00002	86 ms

## Mechanism and policy details





## Proactive capping policy: risk assessment



# Risk assessment using a probabilistic model

- Conservative model that has low false negative rate
- Details out of scope for confidentiality; any conservative model that assesses risk and makes a binary decision can be used

# Deployment at Scale in Google

## Deployed in logs processing clusters



## Summary

**Power oversubscription** with power capping

Effective for reducing data center costs Throttling needs to be compatible with SLOs

Thunderbolt power capping system

Power safety with minimized performance degradation Task-level QoS differentiation Hardware platform independence Tolerance of power telemetry unavailability



2

Deployment at scale in **Google logs processing** clusters over years

9%--25% oversubscription achieved Triggered in production with negligible performance degradation

# Thank you