# Towards an Architecture for Trusted Edge IoT Security Gateways

**Matt McCormack**, Amit Vasudevan, Guyue Liu, Sebastián Echeverría, Kyle O'Meara, Grace Lewis, Vyas Sekar





Software Engineering Institute Carnegie Mellon

# IoT Insecurity is Growing

Mirai Botnet Shows Just How Vulnerable the IoT Really Is

iotsecurityfoundation.org

21 Hacked Cameras, DVRs Powered Today's Massive Internet Outage



krebsonsecurity.com

#### How a fish tank helped hack a casino

washingtonpost.com

#### An Elaborate Hack Shows How Much Damage IoT Bugs Can Do

Rube-Goldbergesque IoT hacks are surprisingly simple to pull off—and can do a ton of damage.

wired.com

## Prior Work: "Bolt-on" Security Gateways



Advantages: practical, deployable, agile

[Yu et al., HotNets 15], [Ko and Mickens, ANRW 18]





#### Requirements

## Contributions

Holistic Coverage

- Data plane
- Control plane

 $\longrightarrow \begin{array}{c} \text{Key security properties} \\ \text{of a trusted gateway} \end{array}$ 

Aligns with "Bolt-on" Trusted gateway architecture Security Gateways built on a micro-hypervisor

- General
- Legacy compatible
- Performant



#### Background: Extensible Micro-Hypervisor



[Vasudevan et al., IEEE SP 13, USENIX Security 16, IEEE EuroSP 18]

#### Trusted Data Plane Approach



#### **Trusted Data Plane Approach**



## **Promising Preliminary Results**

Prototype on Raspberry Pi 3

 Micro-hypervisor: uberXMHF (<u>https://uberxmhf.org</u>)



Data plane: Packet Signing Extension – OVS & Docker: +13% latency

Control plane: Policy Extension – Custom controller: +17% latency

### Conclusions

- Edge gateways offer hope for IoT security
  - Currently these gateways lack trust
- Vision for trusting edge IoT security gateways
  - Defined a holistic adversary model to derive our foundational trust properties
  - High-level architecture for trusted data and control plane built on top of a micro-hypervisor
- Thank you!
  - Contact: <u>mccorm1@andrew.cmu.edu</u>