CAERUS:

Chronoscopic Assessment Engine for Recovering Undocumented Specifications

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Undocumented Specification: Toy Example





We want to automate the task of finding timing sensitivities





Goal: a tool for uncovering timing sensitivities

- Automated: run with minimal user interaction
- Versatile: applicable to different target devices
- Extensible: system capabilities can be augmented



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Example Test Routine: Button Duration







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Duration (ms)	Mean	StdDev	Min/Max
1	1.005	2.985×10^{-3}	1.001/1.007
7	7.000	6.569×10^{-3}	6.993/7.055
34	34.00	8.413×10^{-3}	33.97/34.01
1 - HS	1.026	0	1.026/1.026
7 - HS	7.024	0	7.024/7.024
34 - HS	34.04	1.194×10^{-4}	34.02/34.88

• HS = High Speed crystal oscillator (precise)



Current & Future Work



Security Applications: Fault Injection Attacks

- CAERUS as an embedded device fuzzer
- Clock glitching (e.g., instruction skipping)
- CAERUS is useful for tasks such as finding the right clock cycle, etc...





Going Forward

- Released as open-source under Mozilla Public License
- Stream-lining installation, set-up
- Currently have library support for RS232, looking to add CAN, J1939
- Analog to test other attacks (e.g., brownout, reset)
- Combine peripheral devices

Summary

- Legacy systems & timing sensitivity
- CAERUS architecture
- Minimum button duration example
- Security applications
- Source available on github: https://github.com/caerus-timing

