#### **2017 USENIX Annual Technical Conference**

# **PARIX: Speculative Partial Writes** in Erasure-Coded Systems

Huiba Li<sup>\*</sup>, Yiming Zhang<sup> $\theta$ </sup>, Zhiming Zhang<sup>\*</sup>, Shengyun Liu<sup> $\theta$ </sup>, Dongsheng Li<sup>θ</sup>, Xiaohui Liu<sup>θ</sup>, Yuxing Peng<sup>θ</sup>

\*Meituan Open Service, <sup>0</sup>NUDT







## **Erasure Coding (EC)**

#### 4\*3=12 (300% redundancy)





# Usage of EC

- Widely used in distributed storage systems \* Especially large-scale cloud storage services
- Except in read-write hot storage
- Because of performance
- Overheads include:
  - Coding calculation
  - \* I/O pattern deterioration, especially for partial writes



# **Coding with Vectorial Instructions**





**Coding Schemes** 

# **Coding with Vectorial Instructions**





coding is no longer the bottleneck



### **Partial Write in EC**

#### write

### data blocks



#### parity blocks

### **Partial Write in EC**

#### write

### data blocks



#### parity blocks

unaligned write



# **A Simple Approach to Partial Write**

### By fully re-encoding





# **A Simple Approach to Partial Write**

### By fully re-encoding



#### high 1/0 amplification unfriendly to parallelism





# An Incremental Approach to Partial Write

By incremental encoding





# An Incremental Approach to Partial Write

• By incremental encoding





moderate 1/0 amplification friendly to inter-disk parallelism





# An Incremental Approach to Partial Write

By incremental encoding







# **Cost of in-place read-and-write**

- its latency is equivalent to that of random seek
- performance hurts a lot \* random write: reduced by half
  - \* sequential write: reduced to that of random write
- the major obstacle for EC \* to get used in read-write hot storage







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An approach to accelerate parity writing





An approach to accelerate parity writing











There's still one in-place read-and-write. It's the source of overhead.





### **Pervasiveness of Partial Write**

- partial write is caused by unalignment
- unalignment is the norm
  - \* the upper layers of I/O stack just don't know the fact of EC and its alignment
- It is the major obstacle ✤ for EC to get used in readwrite hot storage systems
- Must be addressed!



#### An analysis of the MSR I/O traces





• Consider a series of r writes to a same data block  $d_i$ :  $d_{i}^{(0)} \leftarrow d_{i}^{(1)}, d_{i}^{(2)}, \cdots, d_{i}^{(r)}$ 

- The parities are  $p_i$  (j=1,2,...,k)
- By incremental parity update equation, we have:  $\Delta p_i^{(1)} = a_{ij} \times \Delta d_i^{(1)}, \ \Delta p_i^{(2)} = a_{ij} \times \Delta d_i^{(2)}$  $p_i^{(r)} = p_i^{(0)} + \sum_{i} \Delta p_i^{(x)} = p_j^{(0)} + a_{ij}(d_i^{(1)} - d_i^{(0)} + d_i^{(2)} - d_i^{(1)} + d_i^{(3)} - d_i^{(2)} + \dots)$ x = 1 $= p_{i}^{(0)} + a_{ij} \times (d_{i}^{(r)} - d_{i}^{(0)})$



in Galois Field  $GF(2^{8})$ 



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in Galois Field  $GF(2^8)$ 

$$_{j}(d_{i}^{(1)} - d_{i}^{(0)} + d_{i}^{(2)} - d_{i}^{(1)} + d_{i}^{(3)} - d_{i}^{(2)} + d_{i}^{(2$$

intermediate values are ALL reduced, no need to read it at all!



# PARIX: Logging Data on Parities

- Instead of parities deltas, as in parity logging
- Each parity records m series of change logs
  - \* respectively for m data blocks
  - \* stored as a single journal file
  - \* interleaved with each other
  - \* every  $d^{(0)}$  always comes after corresponding  $d^{(1)}$
- Example:  $d_{2}^{(1)}, d_{1}^{(1)}, d_{2}^{(0)}, d_{4}^{(1)}, d_{1}^{(0)}, d_{1}^{(2)}, d_{2}^{(2)}$



$$^{2)}, ..., d_i^{(k)} (i = 1..m, k = 0..r_i), ...$$

### The Speculation

### • Whether the parities need $d_i^{(0)}$ or not?

Instead, we speculate about it: Assume d<sub>i</sub><sup>(0)</sup> is NOT needed (mostly right)
Send d<sub>i</sub><sup>(0)</sup> only when it is actually needed (sometimes only)





It is too costly to maintain consensus among nodes about this

































penalty of failure is only a network RTT!



### Implementation

- which hosts all our businesses:
  - USErS
  - tourism, plane & train tickets, movie tickets, payment, ...
- Architecture
  - Master-Server-Client
  - \* No single-point-of-failure



Based on Ursa, a block store for our public & private cloud,

✤ food delivery: ~13M orders/day, ~67M monthly active users, ~200M total

\* crowd-sourced reviews (about businesses), coupons, hotel reservation,





### Evaluation

- 10 Servers, each with: ✤ 12 HDD, 7200 RPM, - attached to an LSI 3008 SAS HBA, w/o flash cache \* 2-way Intel Xeon CPU ✤ 128GB RAM
- 10Gb Ethernet Connected with a non-blocking switch



### Evaluation





### Evaluation





# Summary

- Performance is the major obstacle \* for EC to get used in read-write hot storage \* especially in the case of partial write
- \* with a very small penalty in corner cases
- Evaluations show that its performance meets expectations • much better than existing approaches Close to that of 3-replica scheme



# • PARIX: a novel approach to eliminate overhead in common cases



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# **Thanks:** 2017 USENIX Annual Technical Conference

