

Scalable In-Memory Transaction Processing with HTM

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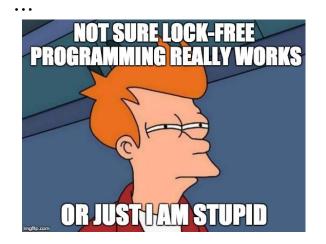
HTM simplifies implementing concurrent programs



Lock-free programming

Atomic Buildins

__sync_bool_compare_and_swap(...)
__sync_fetch_and_add(...)
__sync_synchronize(...)

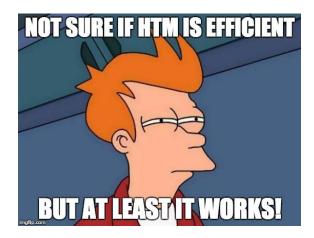


Hardware transactional memory

TSX Instructions

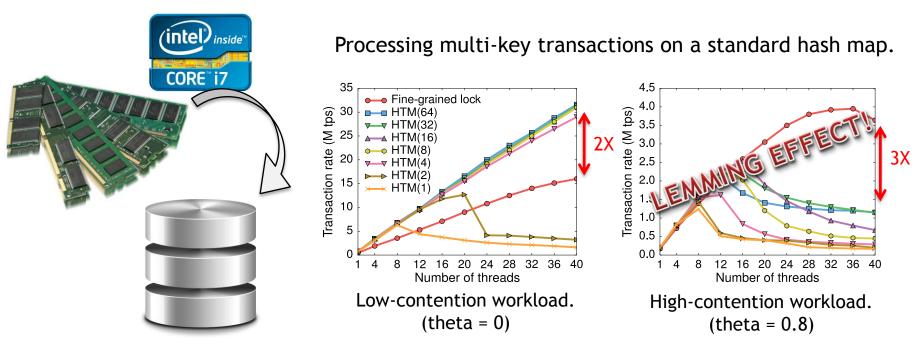
_xbegin() _xend()

•••



HTM is not a silver bullet for transaction processing



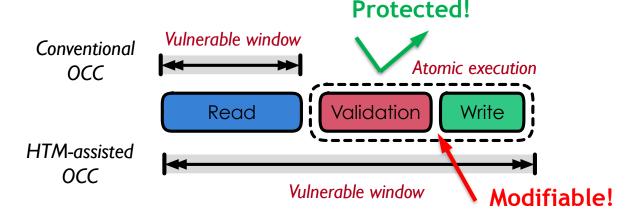


HTM-assisted main-memory database



HTM is not a silver bullet for transaction processing

- Existing works apply HTM to OCC protocol.
 - High database transaction abort rate;
 - High database transaction restart overhead.



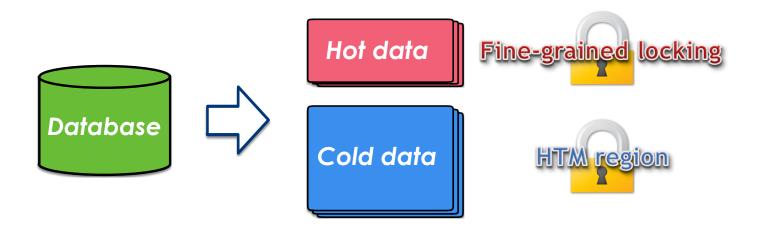


Our proposal: HTCC

- A new HTM-assisted concurrency control protocol that targets at supporting scalable and robust transaction processing even under highly contended workload.
 - Reduce transaction abort rate using a hybrid protocol;
 - Minimize transaction restart overhead using delta restoration.

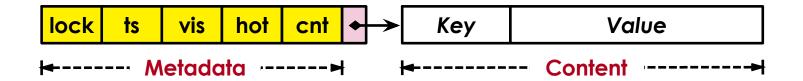


• Split the data into hot and cold records and process them differently.





• Data structure.





background thread

• Data structure.

record1 N

record2	Ν	

record3 N

record4 Y





• Data structure.

	hot	cnt	Periodically check abort count.
record1	Ν	97	
record2	Ν	5	
record3	Ν	9	
record4	Y	23	background thread
record5	Ν	17	



• Data structure.

	hot cnt	Detect top K hot records.
record1	N 97	
	\	
record2	N 5	
record3	N 9	
record4	Y 23	background thread
record5	N 17	



• Data structure.

Set ti	cnt	hot	
	97	Y	record1
	5	N	record2
	9	Ν	record3
back	23	N	record4
e e e	23	IN	1600104

Ν

record5

Set the hot flag transactionally!

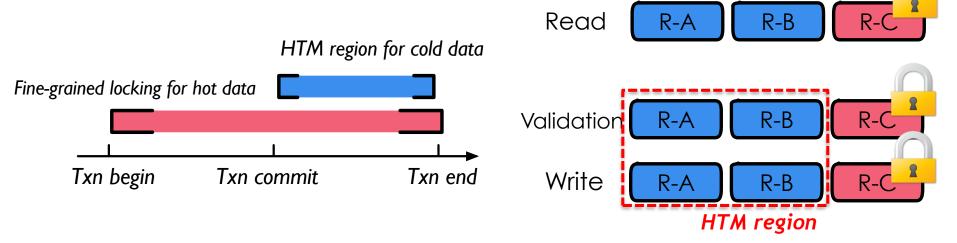


background thread



Hybrid Protocol

• Transaction phases.



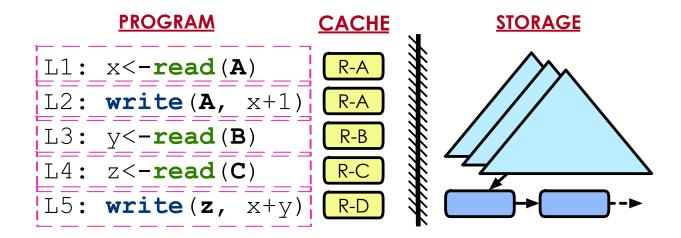
COMMIT!

Fine-grained locking performs well for high-contention workload; HTM performs well for low-contention workload.



Delta Restoration

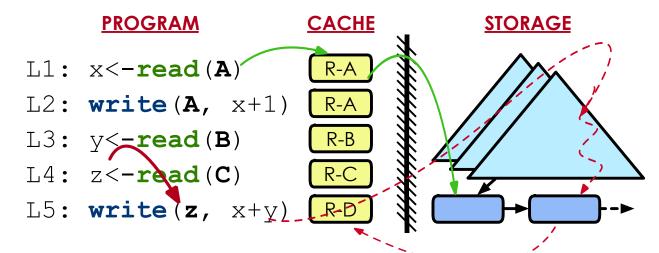
• Workset caching during the *read phase*.





Delta Restoration

• Operation restoration during the *validation phase*.



Accesses to cold records are still performed optimistically using HTM; Deadlock never happens because of HTM's guarantee of atomicity and isolation.



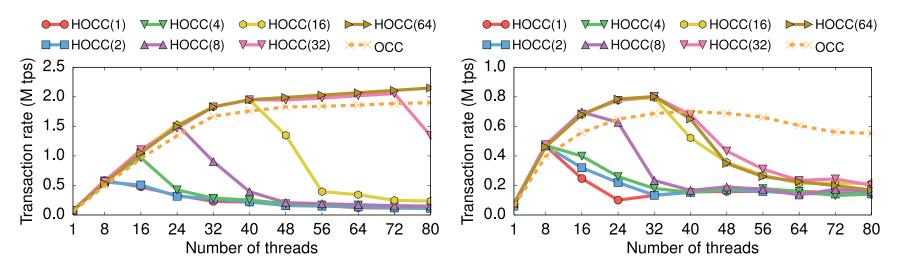
Experiments

- Intel Xeon Processor E7-4820, 4 sockets, 40 cores.
- We compare with the following protocols:
 - 2PL: classic two-phase locking.
 - OCC: classic optimistic concurrency control.
 - SOCC: Silo's OCC implementation.
 - HOCC: Existing HTM-assisted OCC.
 - HTO: Existing HTM-assisted timestamp ordering.



Experiments: Bottlenecks

Database transaction rate with different restart threshold.

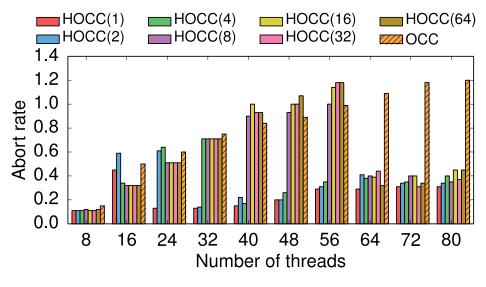


TPC-C: 40 warehouse (low contention). TPC-C: 4 warehouse (high contention).



Experiments: Bottlenecks

• Database transaction abort rate with different restart threshold.

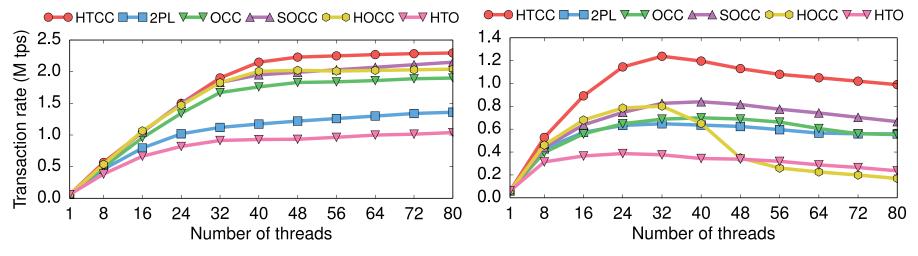


TPC-C: 4 warehouse (high contention).



Experiments: Scalability

• Database transaction rate under different workloads.



TPC-C: 40 warehouse (low contention).

TPC-C: 4 warehouse (high contention).



Conclusion

- We proposed HTCC, an HTM-assisted concurrency control protocol that achieves scalable and robust in-memory transaction processing on multicores.
 - Hybrid synchronization mechanism for reducing transaction abort rate;
 - Workset caching for minimizing transaction restart overhead.



Thanks!